



MMIC SURFACE MOUNT

Power Amplifier

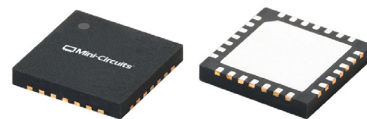
PMA6-73-10W+

Mini-Circuits

50Ω 5.6 to 7.1 GHz +39 dBm P_{SAT}

THE BIG DEAL

- High Gain, Typ. 28.7 dB
- High P_{SAT}, Typ. +39.3 dBm
- Excellent PAE, Typ. 32.6%
- 6x6 mm 28-Lead QFN-Style Package
- High Linearity Design Optimized for High PAR Communication Signals

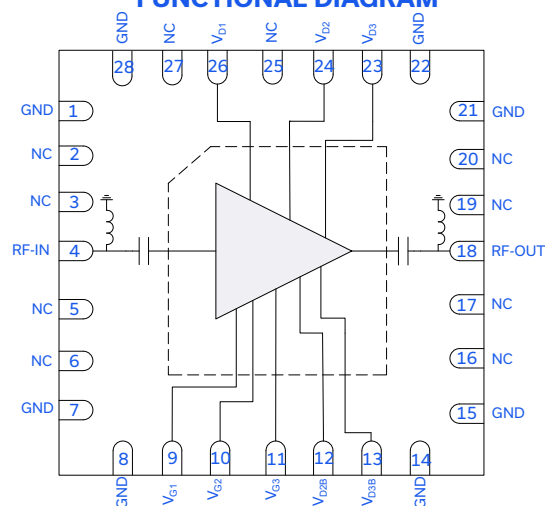


Generic photo used for illustration purposes only

APPLICATIONS

- Back Haul Radio Systems
- Radar Systems

FUNCTIONAL DIAGRAM



PRODUCT OVERVIEW

Mini-Circuits' PMA6-73-10W+ is a GaAs MMIC Power Amplifier operating from 5.6 to 7.1 GHz. This internally matched 50Ω amplifier provides 28.7 dB of gain, +39.3 dBm saturated output power, and +45.7 dBm output IP3, while operating from a +8 V power supply and consuming 1.75 A of bias current. This amplifier is fully matched at the input and output, allowing for seamless implementation, and it is assembled in a 6x6 mm 28-Lead QFN-style package. These characteristics make it ideally suited for back haul radio systems and radar systems that require high operating output power, while maintaining very low distortion characteristics.

KEY FEATURES

Feature	Advantages
High P _{SAT} Typ. +39.3 dBm	With a high saturated output power, this device can be used as the power amplifier for Back Haul Radio and/or Radar Systems.
High Gain Typ. 28.7 dB	Reduces the number of amplification stages at the system level.
6x6 mm 28-Lead QFN-Style Package	Small footprint saves space in dense layouts while providing low inductance, repeatable transitions, and excellent thermal contact to the PCB. Industry standard packaging allows for ease of assembly in high volume manufacturing processes.



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Parameter	Condition (GHz)	Min.	Typ.	Max.	Units
Frequency Range		5.6		7.1	GHz
Gain	5.6	28.1	29.4		dB
	6.0	27.8	29.1		
	6.4	27.3	28.7		
	6.8	27.4	28.8		
	7.1	27.4	28.7		
Output Power at 1 dB Compression (P _{1dB})	5.6		+36.9		dBm
	6.0		+38.1		
	6.4		+38.3		
	6.8		+37.6		
	7.1		+37.8		
Output Power at Saturation (P _{SAT}) ²	5.6		+38.5		dBm
	6.0		+38.9		
	6.4		+39.3		
	6.8		+38.8		
	7.1		+38.2		
Power Added Efficiency (PAE) at P _{SAT}	6.4		32.6		%
Output Third-Order Intercept (P _{OUT} = +17 dBm/Tone)	5.6		+43.2		dBm
	6.0		+45.4		
	6.4		+45.5		
	6.8		+44.1		
	7.1		+43.8		
Input Return Loss	5.6		12		dB
	6.0		15		
	6.4		17		
	6.8		25		
	7.1		22		
Output Return Loss	5.6		16		dB
	6.0		11		
	6.4		10		
	6.8		12		
	7.1		14		
Isolation	5.6		69		dB
	6.0		75		
	6.4		70		
	6.8		75		
	7.1		69		
Noise Figure	5.6		6.4		dB
	6.0		6.1		
	6.4		5.8		
	6.8		5.7		
	7.1		5.8		
Device Operating Voltage (V _{DD}) ³		+6	+8	+8	V
Device Operating Current (I _{DD}) ^{4, 5}			1.75		A
Gate Voltage (V _{G1} , V _{G2})			-0.9		V
Gate Voltage (V _{G3})			-0.9		V
Device Gate Current (I _{GG}) ⁶				28	mA
DC Current Variation vs. Temperature ⁷			-0.37		mA/°C
DC Current Variation vs. Voltage ⁸			-1.92		μA/mV

1. Tested on Mini-Circuits Characterization Test Board TB-PMA6-7310WC+. See Figure 2. Loss de-embedded to the RF input and output pins of the device.

2. P_{SAT} is defined as when the Output Power changes 0.1 dB per 1 dB change in Input Power.3. V_{DD} = V_{D1} = V_{D2} = V_{D3} = V_{D2B} = V_{D3B}4. Current at P_{IN} = -15 dBm. Current at saturation reaches 3.6 A.5. I_{DD} = I_{DD1} + I_{DD2} + I_{DD3}; I_{DD12} = I_{DD1} + I_{DD2} = 750 mA; I_{DD3} = 1000 mA.

6. At saturation.

7. (Current at +95°C - Current at -40°C)/(+135°C).

8. (Current at +8 V - Current at +6 V)/(+2 V).





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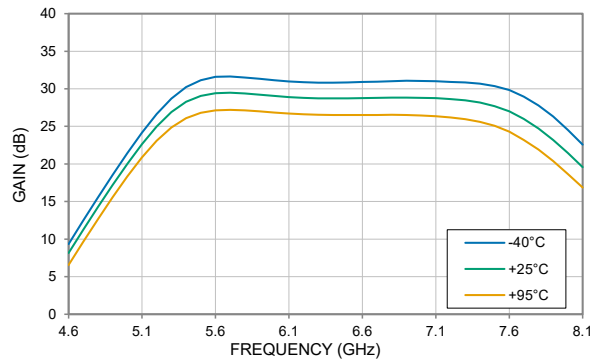
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TYPICAL PERFORMANCE GRAPHS

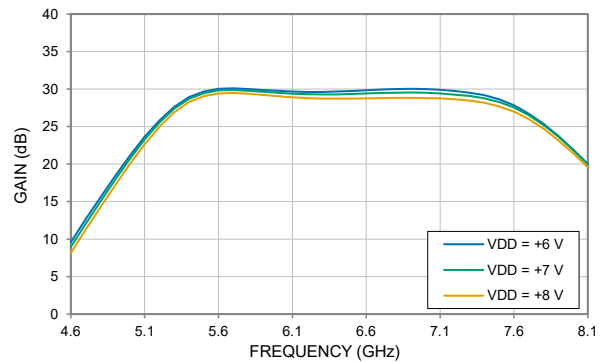
GAIN vs. TEMPERATURE

$P_{IN} = -15$ dBm, $V_{DD} = +8$ V



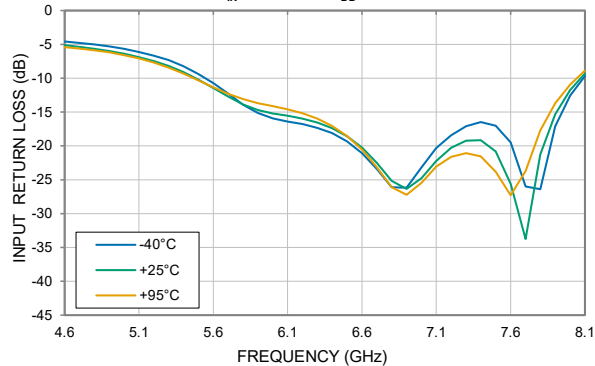
GAIN vs. DEVICE VOLTAGE

$P_{IN} = -15$ dBm, TEMPERATURE = +25°C



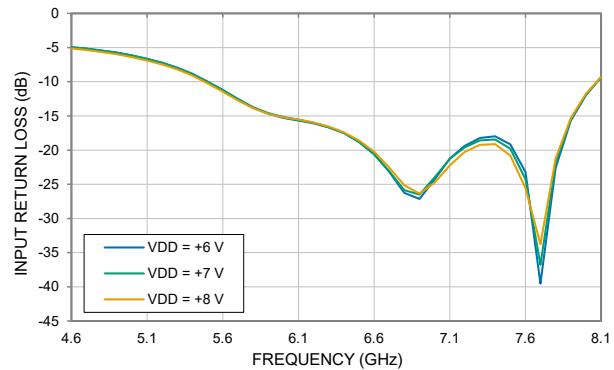
INPUT RETURN LOSS vs. TEMPERATURE

$P_{IN} = -15$ dBm, $V_{DD} = +8$ V



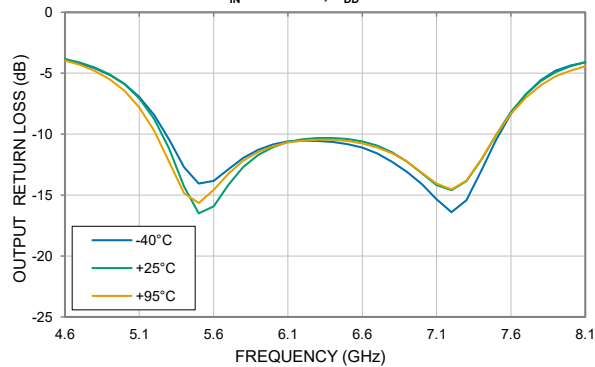
INPUT RETURN LOSS vs. DEVICE VOLTAGE

$P_{IN} = -15$ dBm, TEMPERATURE = +25°C



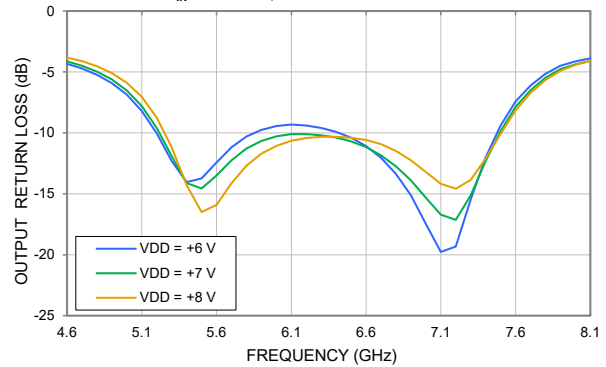
OUTPUT RETURN LOSS vs. TEMPERATURE

$P_{IN} = -15$ dBm, $V_{DD} = +8$ V



OUTPUT RETURN LOSS vs. DEVICE VOLTAGE

$P_{IN} = -15$ dBm, TEMPERATURE = +25°C





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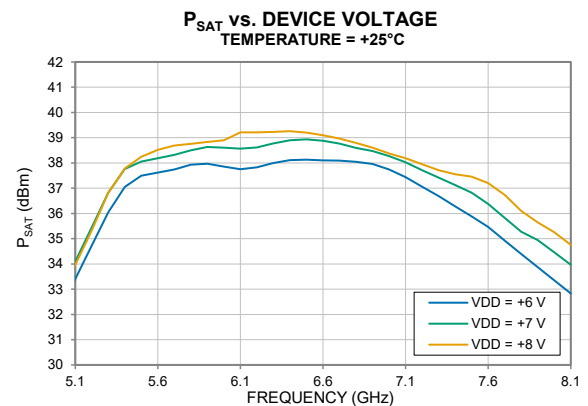
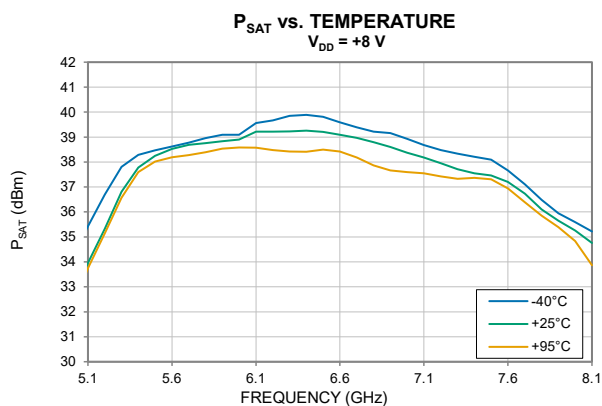
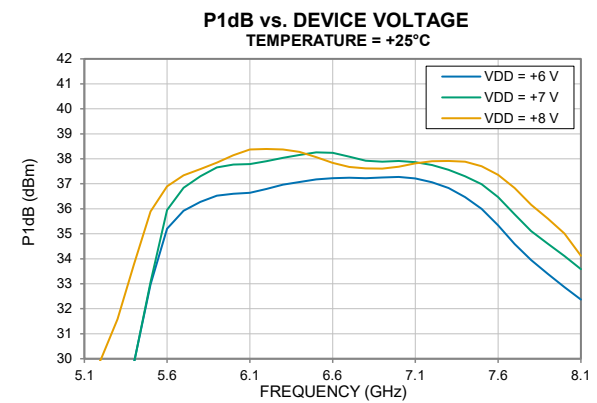
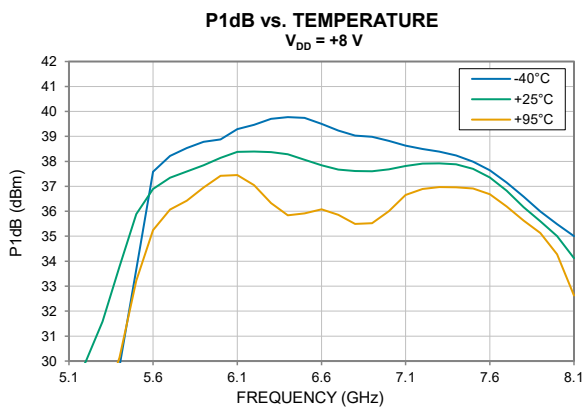
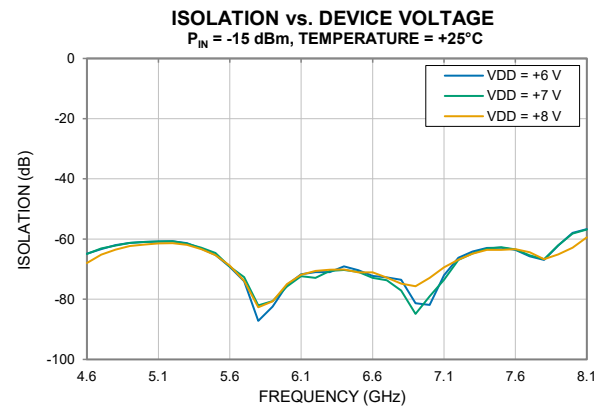
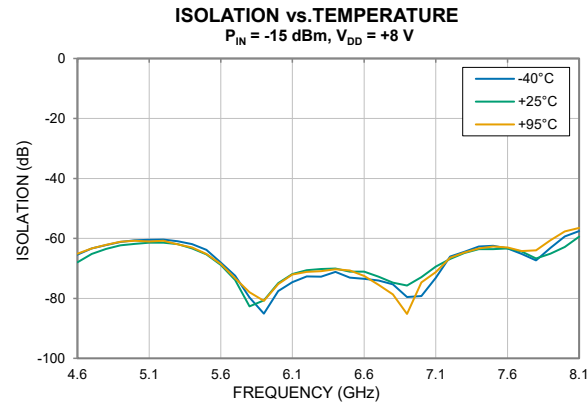
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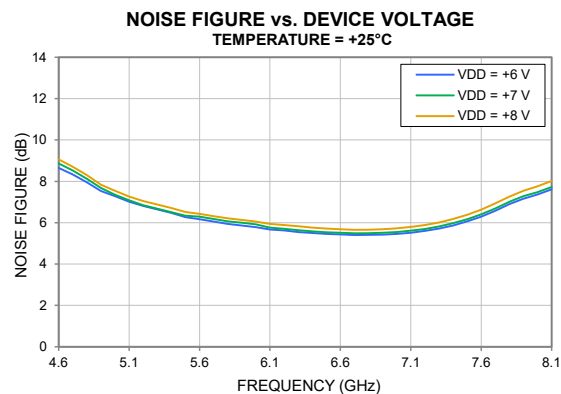
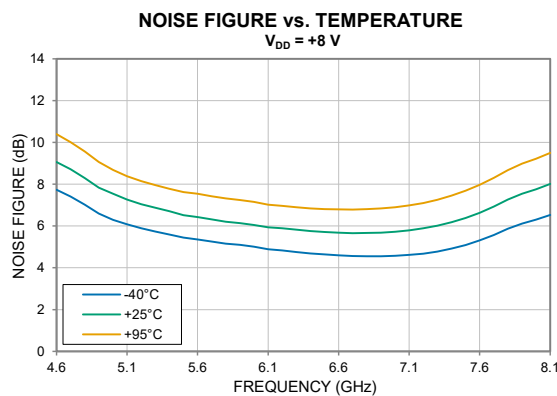
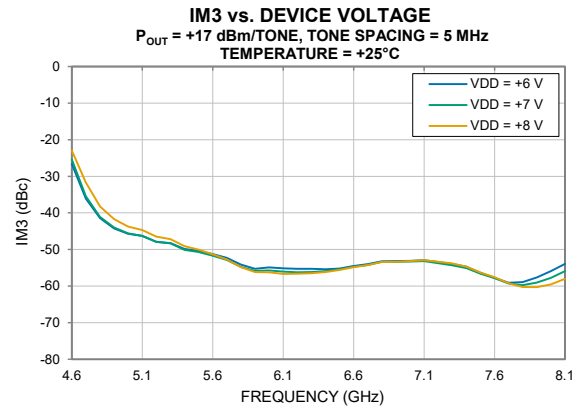
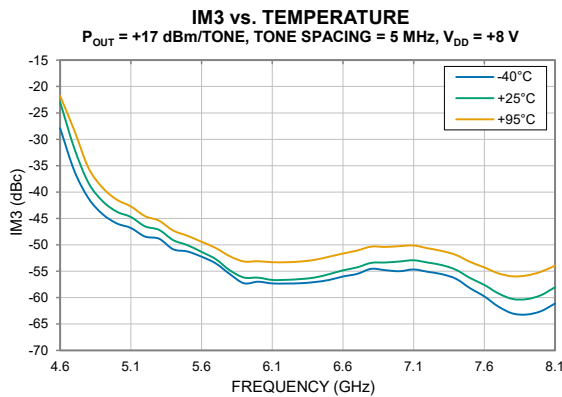
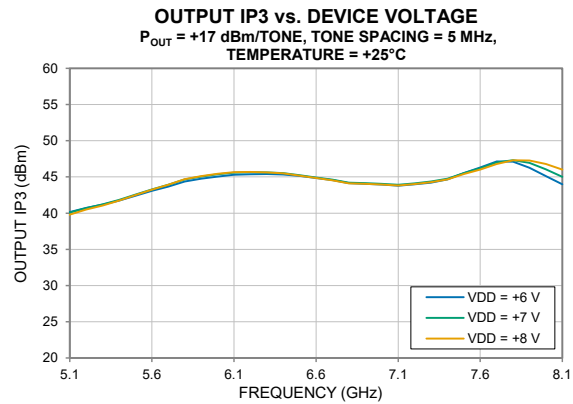
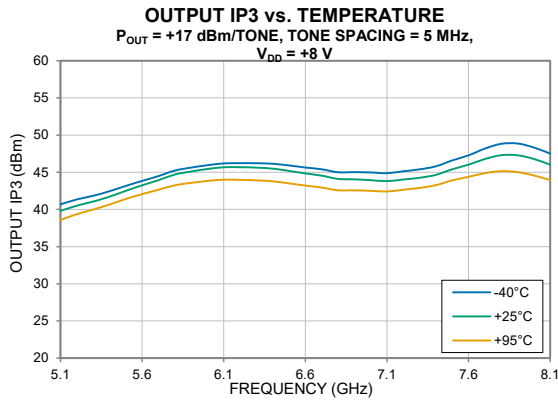
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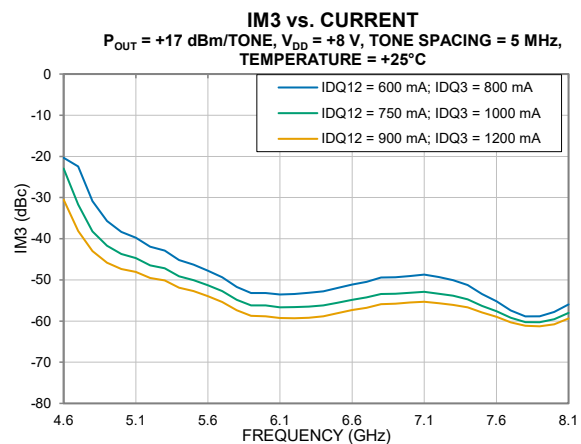
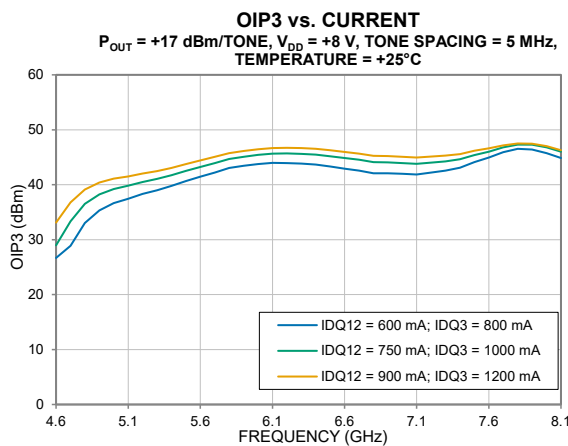
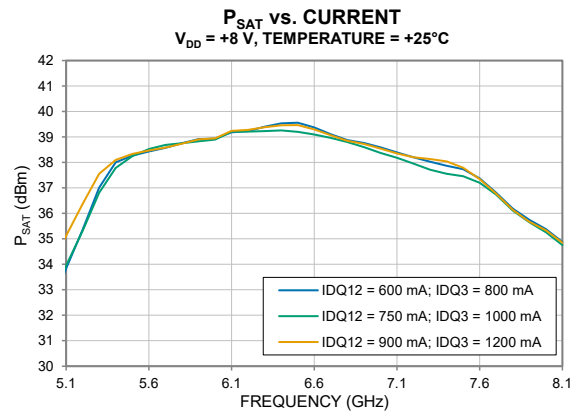
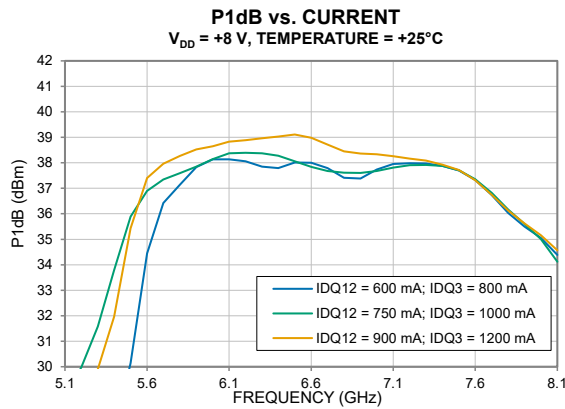
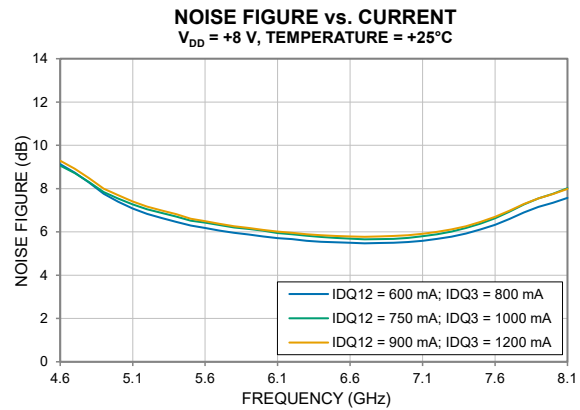
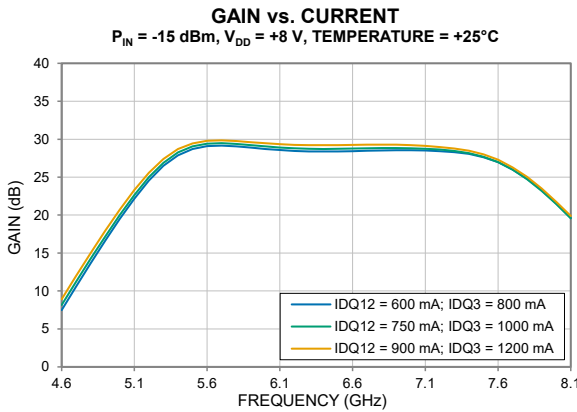
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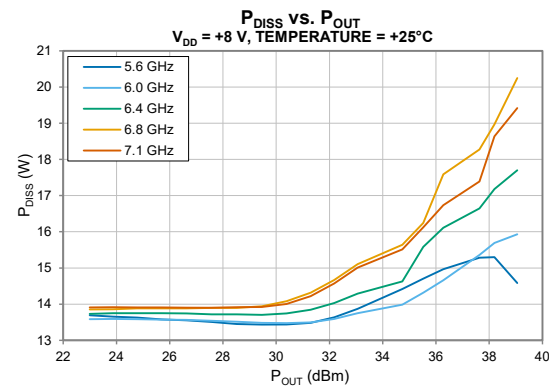
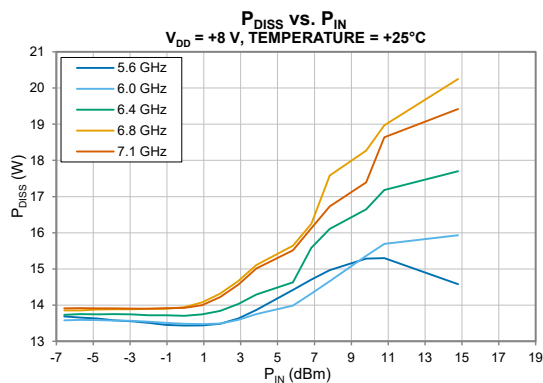
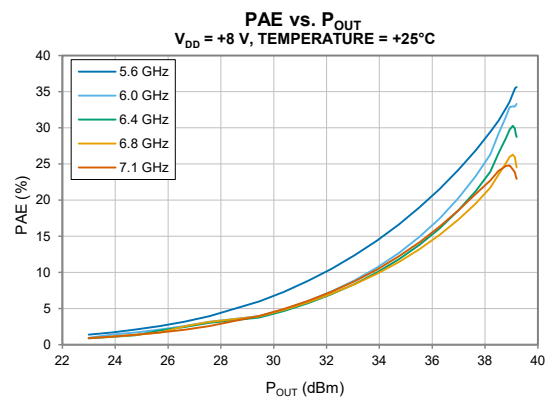
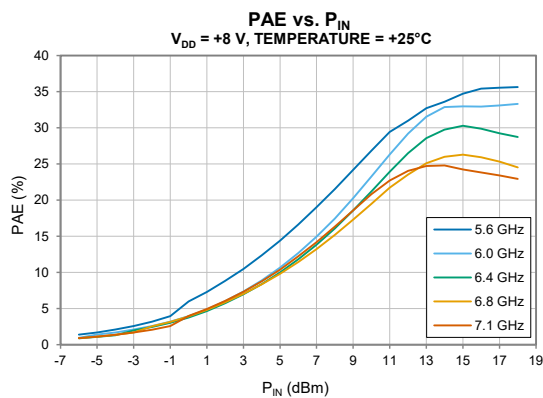
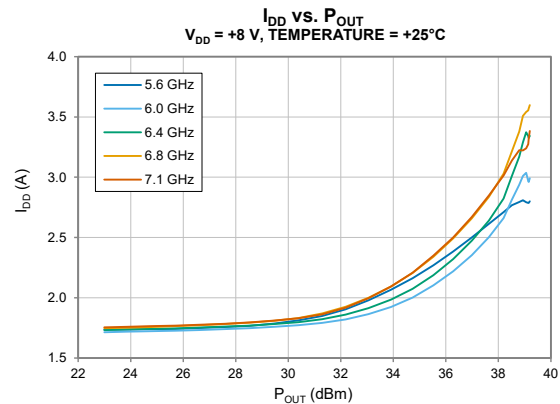
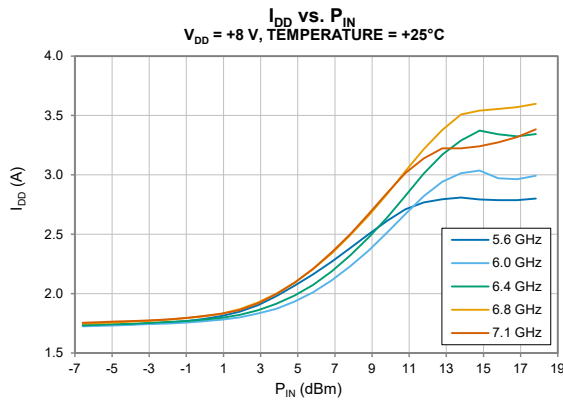
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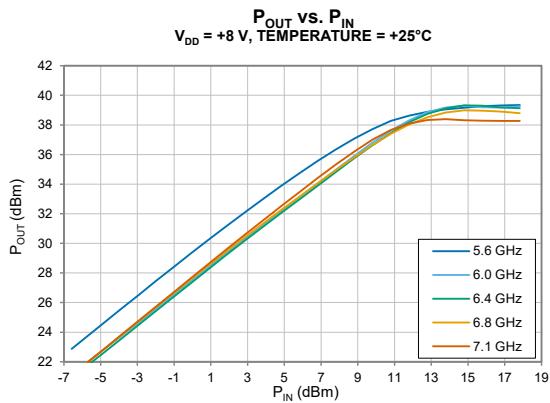
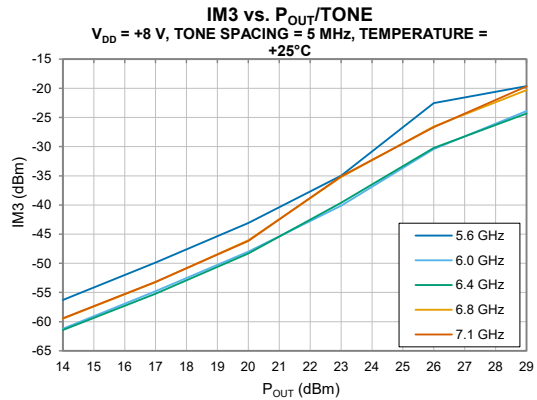
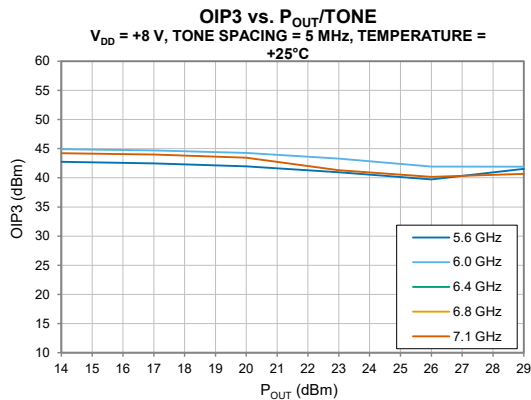
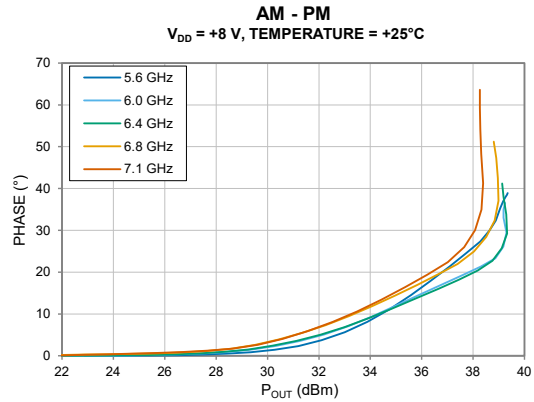
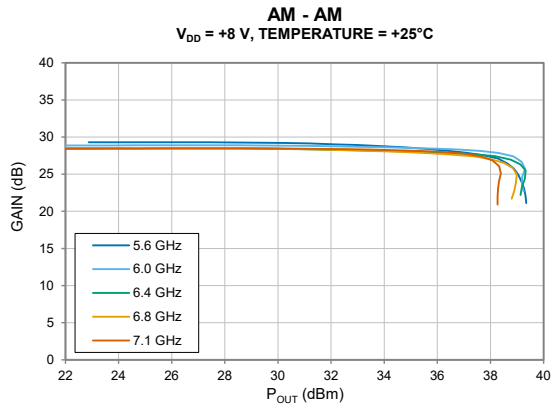
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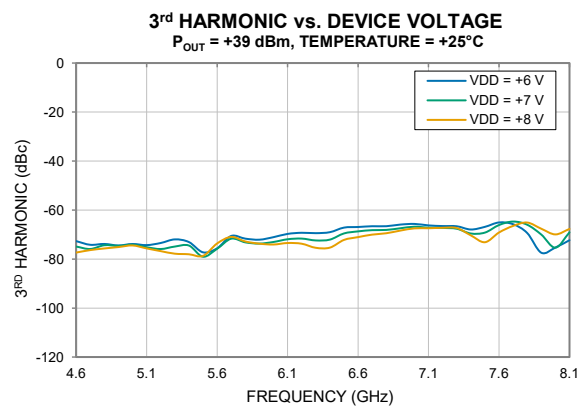
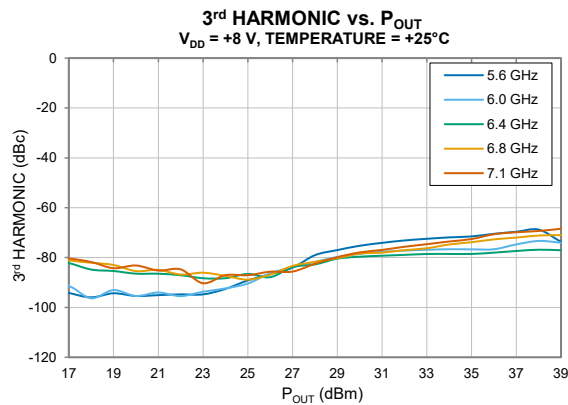
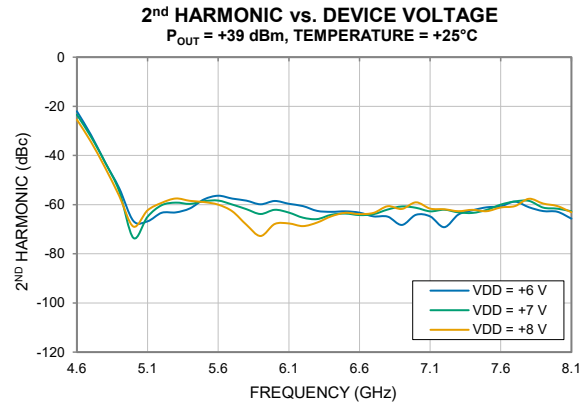
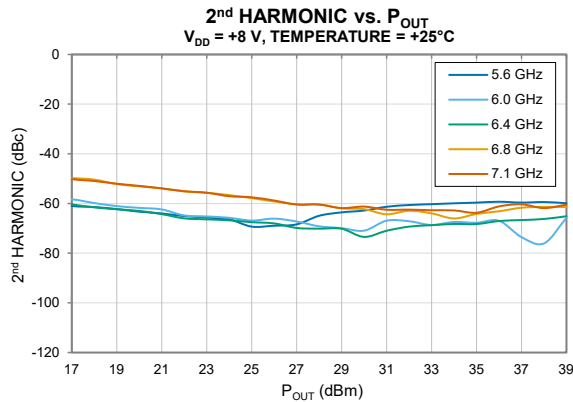
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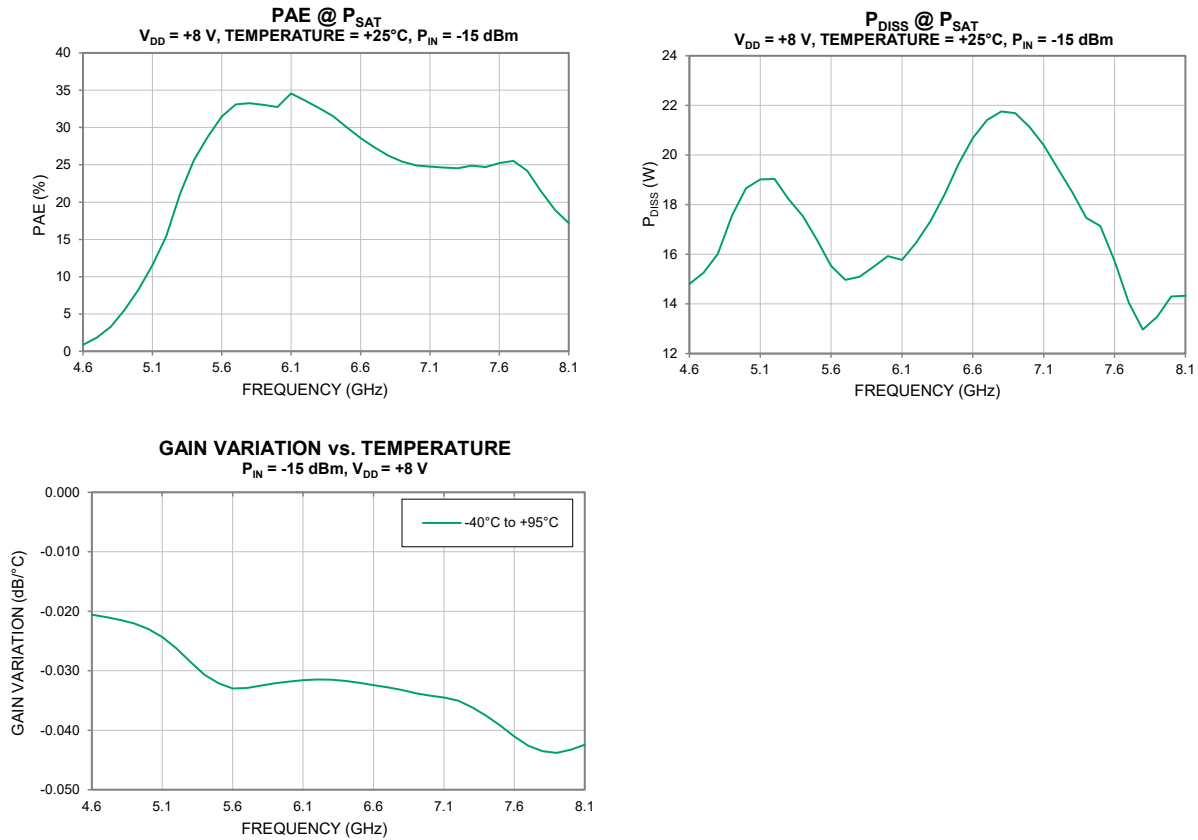
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ABSOLUTE MAXIMUM RATINGS⁹

Parameter	Ratings
Operating Temperature ¹⁰	-40°C to +95°C
Storage Temperature	-65°C to +150°C
Junction Temperature ¹¹	+175°C
Total Power Dissipation	22 W
Input Power (CW), $V_{DD} = +8$ V	+35 dBm
DC Drain Voltage at V_{DD} ¹²	+9 V
DC Gate Voltage at V_{G1} , V_{G2} , V_{G3}	$-3 \text{ V} \leq V_{G1}, V_{G2}, V_{G3} \leq -0.4 \text{ V}$
DC Drain Current I_{DD}	5 A
DC Gate Current I_{GG}	28 mA

9. Permanent damage may occur if these limits are exceeded. Maximum ratings are not intended for continuous normal operation.

10. Bottom of Die.

11. Peak temperature on top of Die.

12. $V_{DD} = V_{D1} = V_{D2} = V_{D3} = V_{D2B} = V_{D3B}$

THERMAL RESISTANCE

Parameter	Ratings
Thermal Resistance (Θ_{JC}) ¹³	4.9 °C/W

13. $\Theta_{JC} = (\text{Hot Spot Temperature on Die} - \text{Temperature at Ground Lead}) / \text{Dissipated Power}$

ESD RATING

	Class	Voltage Range	Reference Standard
HBM	1C	1000 to < 2000	ANSI/ESDA/JEDEC JS-001-2023
CDM	C2A	500 to < 750	ANSI/ESDA/JEDEC JS-002-2022



ESD HANDLING PRECAUTION: This device is designed to be Class 1C for HBM. Static charges may easily produce potentials higher than this with improper handling and can discharge into DUT and damage it. As a preventive measure Industry standard ESD handling precautions should be used at all times to protect the device from ESD damage.

MSL RATING

Moisture Sensitivity: MSL1 in accordance with IPC/JEDEC J-STD-020E/JEDEC J-STD-033C





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FUNCTIONAL DIAGRAM

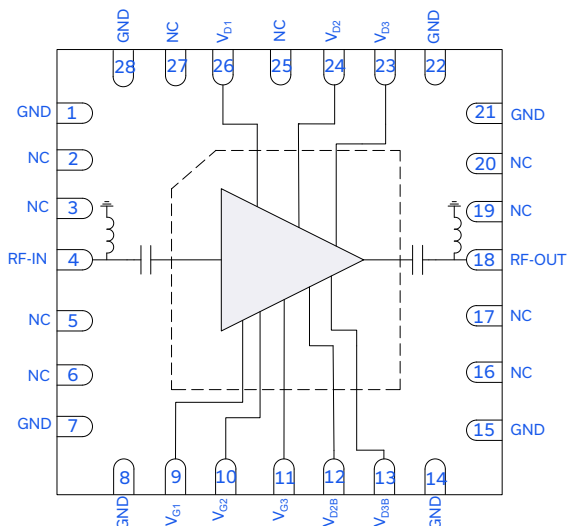


Figure 1. PMA6-73-10W+ Functional Diagram

PIN DESCRIPTION

Function	Pin Number	Description (Refer to Figure 1)
RF-IN	4	RF-IN pin connects to RF Input port.
RF-OUT	18	RF-OUT pin connects to RF Output port.
V _{D1}	26	DC Input pin connects to First Stage Drain Voltage port.
V _{G1} ¹⁴	9	DC Input pin connects to First Stage Gate Voltage port.
V _{D2}	24	DC Input pin connects to Drain Voltage port of 2 nd stage top half.
V _{G2} ¹⁴	10	DC Input pin connects to 2 nd Stage Gate Voltage port.
V _{D3}	23	DC Input pin connects to 3 rd Stage Gate Voltage port.
V _{G3}	11	DC Input pin connects to Third Stage Gate Voltage port.
V _{D2B}	12	DC Input pin connects to Drain Voltage of 2 nd stage bottom half.
V _{D3B} ¹⁵	13	DC Input pin connects to Drain Voltage port of 3 rd stage bottom half.
NC	3, 5, 17, 19, 27	Not connected on test board. May be connected to ground or left open.
NC	2, 6, 16, 20, 25	Connected to ground on test board. May be connected to ground or left open.
GND	1, 7, 8, 14, 15, 21, 22, 28 PADDLE	Connect to ground.

14. V_{G1} and V_{G2} are connected on TB-PMA6-7310WC+

15. For optimal performance voltage should be applied to V_{D3} and V_{D3B} ports. During characterization, both ports were connected via a jumper on TB-PMA6-7310WC+.



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CHARACTERIZATION BOARD

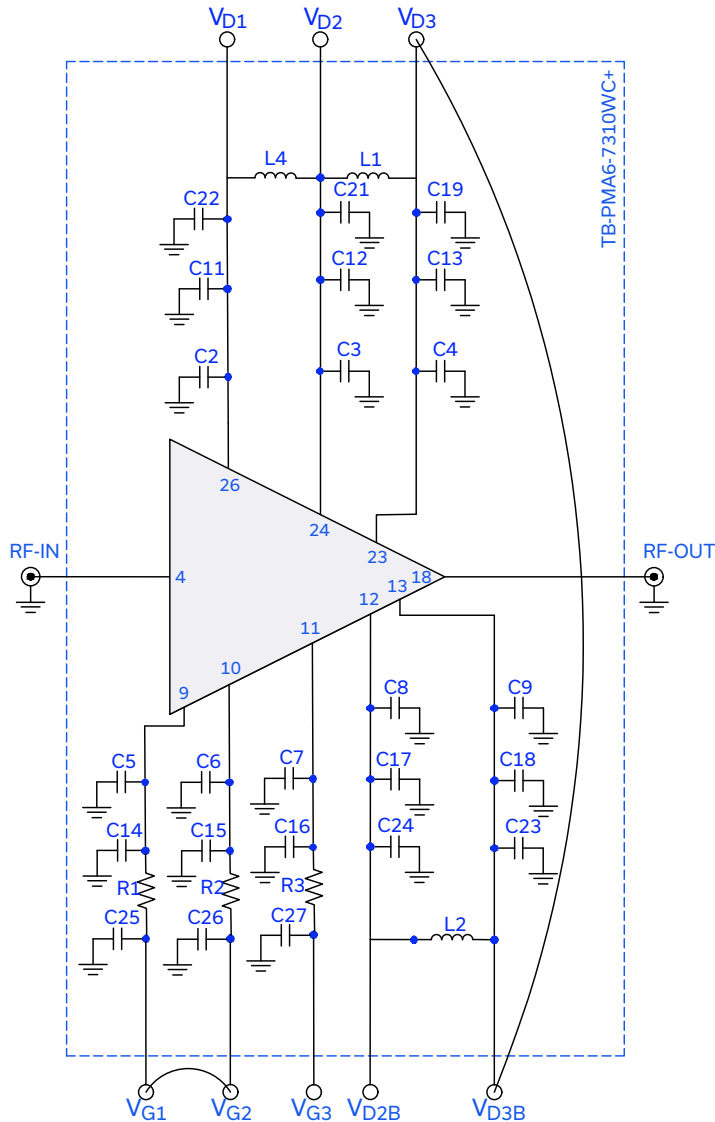


Figure 2. PMA6-73-10W+ Characterization Circuit

Component	Value	Size	Part Number	Manufacturer
C2 - C9	0.001 μ F	0402	GRM1555C1H102JA01D	MURATA
C11 - C18	0.1 μ F	0402	GRM155R71E104KE14D	MURATA
C19, 21 - C27	1 μ F	0603	06035C105KAT2A	KYOCERA AVX
L1, L2	33 nH	0603	0603AF-33NXJRW	COILCRAFT
L4	56 nH	0402	0402DF-560XJRW	COILCRAFT
R1	24 Ω	0402	RK73H1ETTP24R0F	KOA SPEER
R2	12 Ω	0402	RK73H1ETTP12R0F	KOA SPEER
R3	0 Ω	0402	RK73Z1ETTP	KOA SPEER



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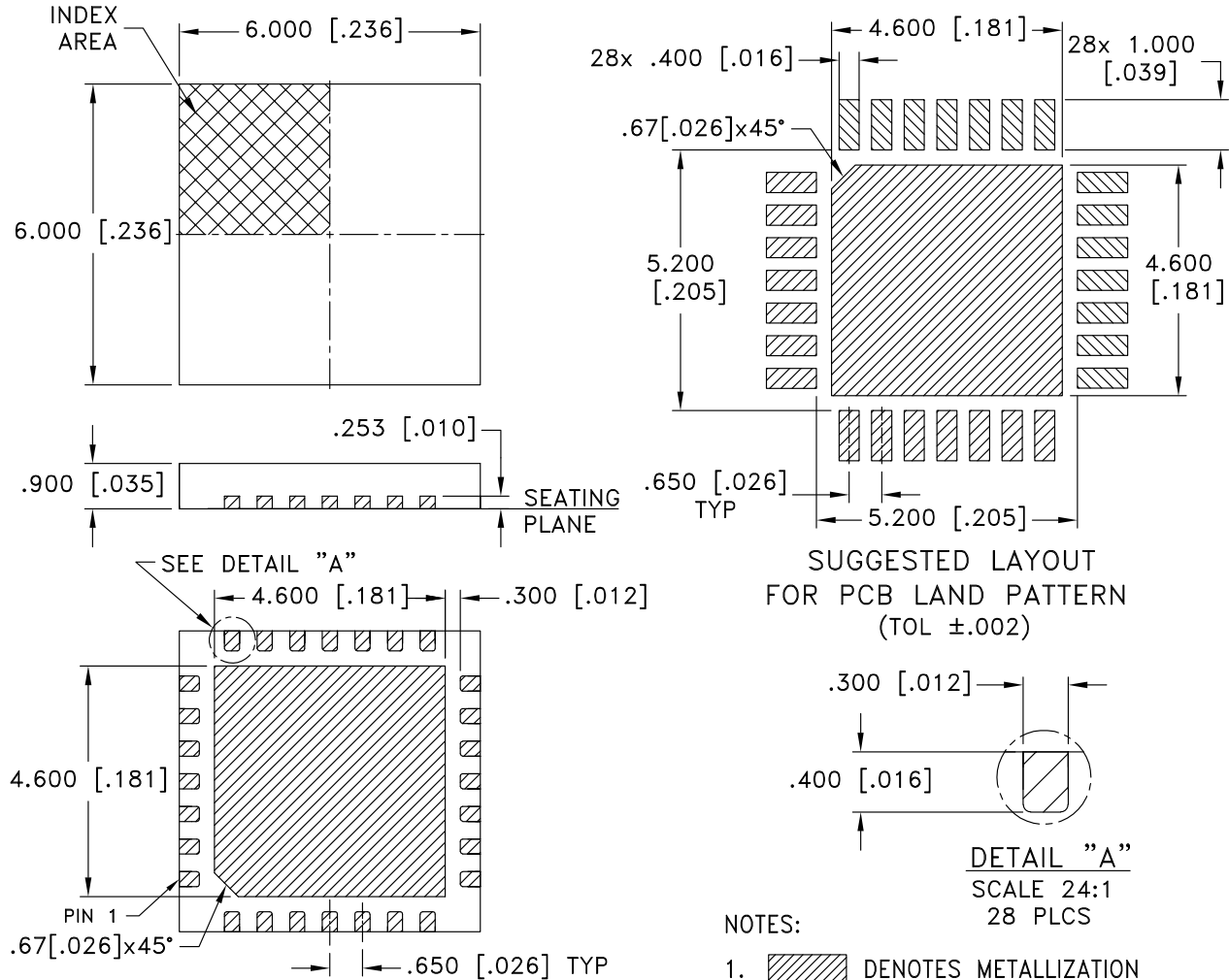
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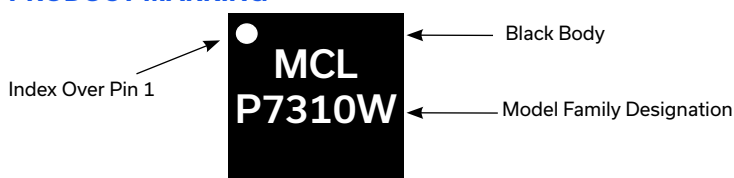
CASE STYLE DRAWING



Weight: 0.11 grams

Dimensions are in mm [inches]. Tolerances: 3 Pl. ±.050 [0.002] mm [Inches]

PRODUCT MARKING



Marking may contain other features or characters for internal lot control.

Mini-Circuits

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ADDITIONAL DETAILED INFORMATION IS AVAILABLE ON OUR DASHBOARD

[CLICK HERE](#)

Performance Data & Graphs	Data
	Graphs
	S-Parameter (S2P Files) Data Set (.zip file)
Case Style	WQ3359 Plastic package, exposed paddle, Lead Finish: Matte-Tin
RoHs Status	Compliant
Tape & Reel	F68
Standard Quantities Available on Reel	7" reels with 20, 50, 100, 200, 500, or 1K 13" reels with 2K, 3K, or 4K
Suggested Layout for PCB Design	PL-825
Evaluation Board	TB-PMA6-7310WC+
	Gerber File
Environmental Ratings	ENV08T1

NOTES

- A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
- B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
- C. The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the standard terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/terms/viewterm.html

