## Engineering Development Model

## Diplexer PLUG-IN

## DPLE-EDU2596

## **Important Note**

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.

Please click "Back", and then click "Contact Us" for Applications support.

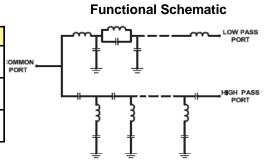


CASE STYLE : 99-01-2411

		ELEC	TRICAL SPECIE	FICATIONS 750	Ω @ +25°C		
Paran	neter	Port	Frequency (MHz)	Min.	Тур.	Max.	Units
	Insertion	Lowpass	5-85	-	1	1.5	dB
	Loss	Highpass	102-1220	-	0.8	1.5	dB
Passband	nd Return Loss	Lowpass	5-85	20	24	-	dB
Fassballu		Highpass	102-1220	18	24	-	dB
	Return L055	Common	5-85	20	24	-	dB
		Common	102-1220	18	24	-	dB
			5-80	50	55	-	dB
			80-85	45	50	-	dB
	oss over isolati	on	85-102	-	10	-	dB
			102-108	42	50	-	dB
			108-250	45	50	-	dB
			250-1220	40	45	-	dB

MAXIMUM RATINGS	
Operating Temperature	-40°C to 85°C
Storage Temperature	-55°C to 100°C
RF Power Input	1W

Highpass port	9	
Lowpass port	1	
Common port	5	
Ground	2,3,4,6,7,8	



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