



LTCC SURFACE MOUNT

Bandpass Filter

BFHK-1742+

50Ω 16 to 19 GHz

THE BIG DEAL

- Ultra-High Stopband Rejection, Typ. 85 dB
- Standard Small 1812 (4.5mm x 3.2mm) Case Style
- Rugged Ceramic Construction
- Protected by US Patents 11,638,370 and 11,744,057

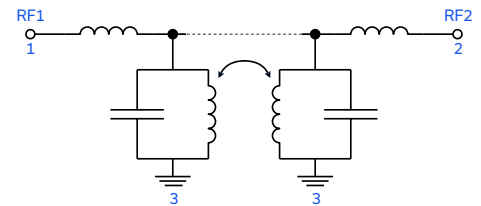


Generic photo used for illustration purposes only

APPLICATIONS

- Test & Measurement Equipment
- Aerospace and Defense Signal Conditioning

FUNCTIONAL DIAGRAM



PRODUCT OVERVIEW

The BFHK-1742+ LTCC Bandpass Filter achieves a miniature size and highly repeatable performance by utilizing a proprietary LTCC material system and distributed filter topology. The passband loss between 16.0 – 19.0 GHz is typically 2.9 dB, with typical stopband rejection of 85 dB up to 11.5 GHz and 70 dB up to 40 GHz. This model handles up to 1 W RF input power and has a wide operating temperature range from -55°C to +125°C.

KEY FEATURES

Features	Advantages
Ultra-High Rejection	Typical stopband rejections of 85 dB up to 11.5 GHz and 70 dB up to 40 GHz.
LTCC Construction	The use of LTCC technology allows for repeatable performance in a rugged ceramic package, well suited for tough environment with high humidity and temperature extremes. See Mini-Circuits Environmental Rating ENV06T12 for more information.
Cost Effective	LTCC is a scalable technology that is cost effective due to its ease of production in high quantities.
Small Size 1812 (4.5 mm x 3.2 mm)	Allows for highly dense circuit board layouts, while minimizing the effects of parasitics.
Surface Mountable	Suitable for very high-volume automated assembly processes.

REV. OR
ECO-022153
BFHK-1742+
MCL NY
240620





ELECTRICAL SPECIFICATIONS^{1,2,3} AT +25°C

Parameter	F#	Frequency (GHz)	Min.	Typ.	Max.	Units
Center Frequency ⁴	—	—	—	17.4	—	GHz
Passband	Insertion Loss	F1-F2	—	2.9	4.0	dB
	Return Loss	F1-F2	—	12.0	—	dB
Stopband, Lower	Insertion Loss	DC-F3	73	85	—	dB
Stopband, Upper	Insertion Loss	F4-F5	74	84	—	dB
	Insertion Loss	F5-F6	60	70	—	dB

1. Tested on Evaluation Board P/N TB-BFHK-1742C+ with the connector and feedline effects de-embedded using the 2XThru IEEE P370 method.

2. Bi-directional, RF1 and RF2 ports can be interchanged.

3. In applications where DC voltage and/or current is present at either the input or output ports, external DC blocking capacitors are required.

4. Typical variation ±3.5%

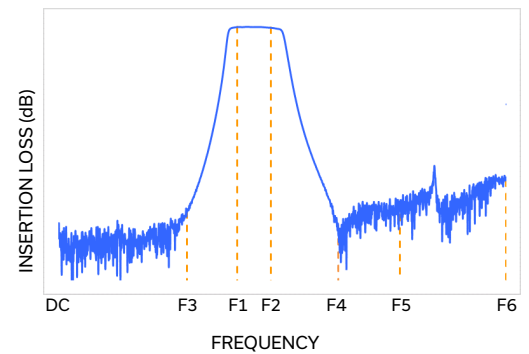
ABSOLUTE MAXIMUM RATINGS⁵

Parameter	Ratings
Operating Temperature	-55°C to +125°C
Storage Temperature	-55°C to +125°C
Input Power ⁶	1 W

5. Permanent damage may occur if any of these limits are exceeded.

6. Power rating applies only to signals within the passband. Power rating above +25°C operating temperature decreases linearly to 0.5 W at +125°C.

TYPICAL FREQUENCY RESPONSE AT +25°C





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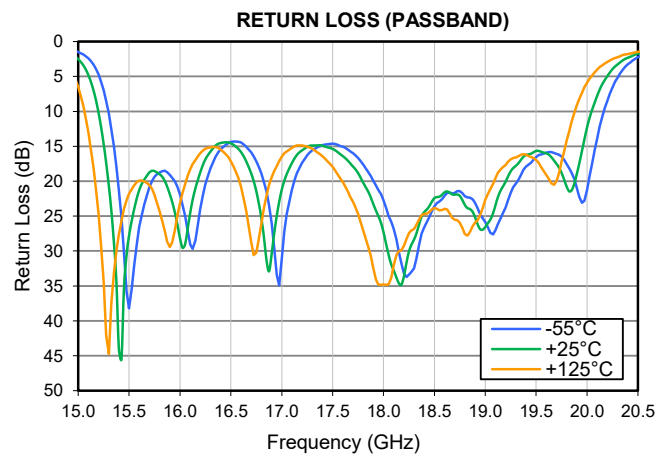
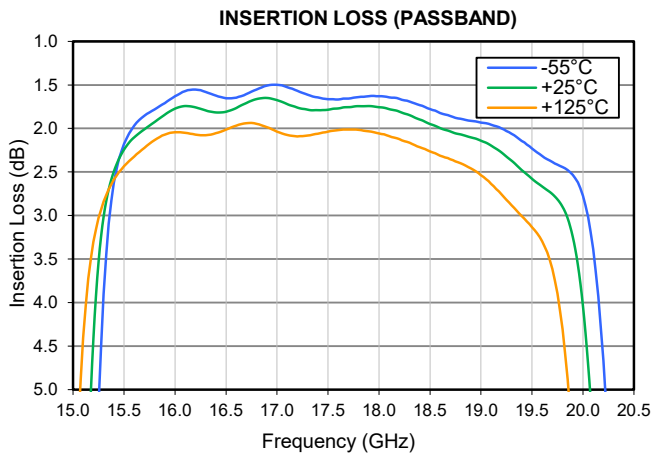
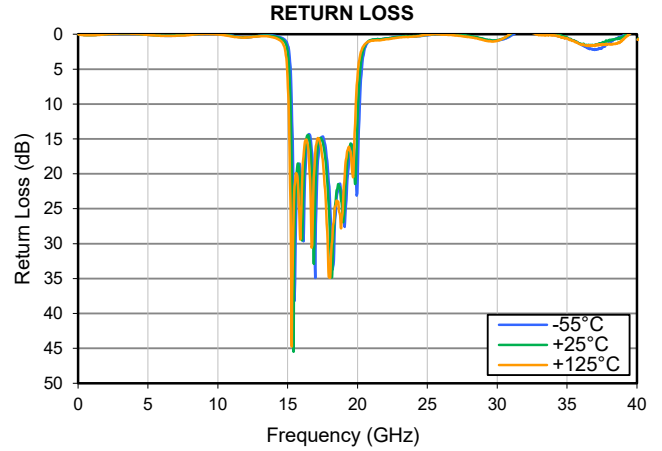
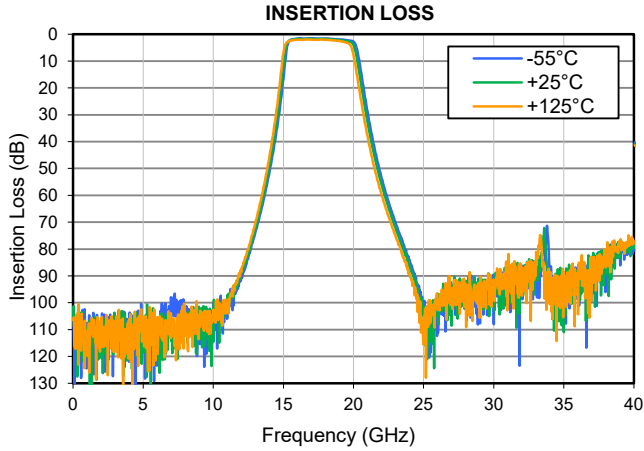
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TYPICAL PERFORMANCE GRAPHS





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FUNCTIONAL DIAGRAM

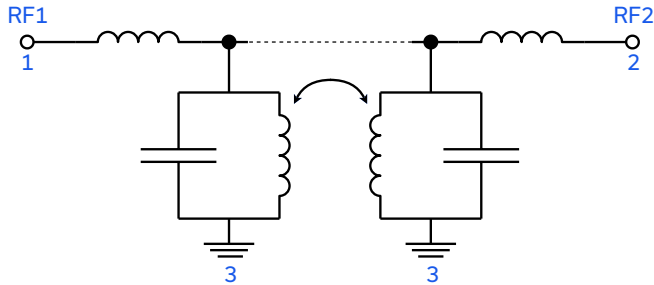
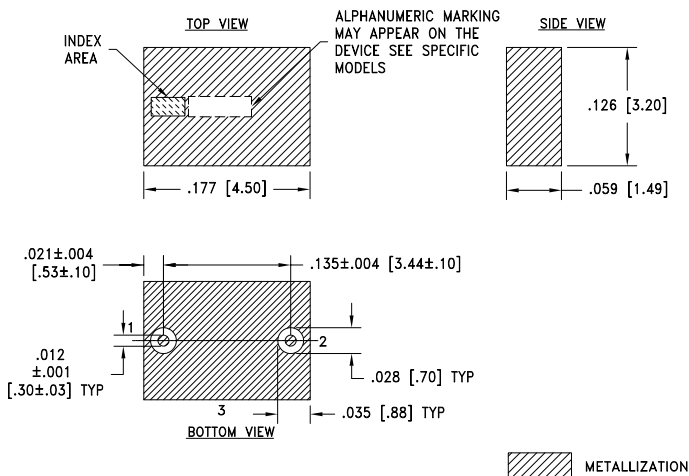


Figure 1. BFHK-1742+ Functional Diagram

PAD DESCRIPTION

Function	Pad Number	Description
RF1 ²	1	Connects to RF Input Port
RF2 ²	2	Connects to RF Output Port
GROUND	3	Connects to Ground on PCB, (See drawing PL-730)

CASE STYLE DRAWING

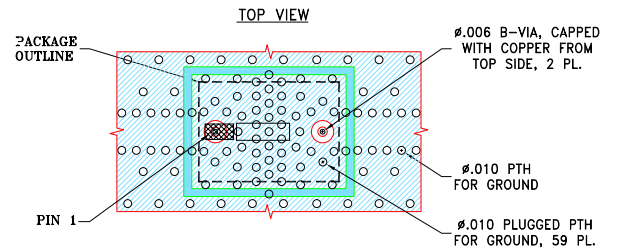


Weight: .126 grams.
Dimensions are in inches [mm]. Tolerances: 2 Pl.±.01; 3 Pl. ±.005 Inches

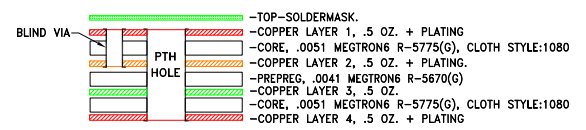
PRODUCT MARKING*: F559

*Marking may contain other features or characters for internal lot control.

SUGGESTED PCB LAYOUT (PL-730)



STACK-UP DIAGRAM



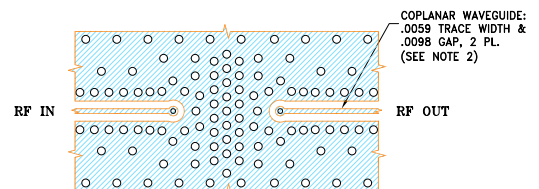
- TOTAL FINISHED THICKNESS 0.0228±.004.
- B-VIA PRESENT FROM COPPER LAYER 1 TO COPPER LAYER 2.
- PTH PRESENT FROM COPPER LAYER 1 TO COPPER LAYER 4.
- INDICATED PLUGGED PTH'S ARE PLUGGED WITH EPOXY AND CAPPED WITH COPPER FROM TOP SIDE.
- LAYER 4 IS CONTINUOUS GROUND PLANE.

NOTES:

- PCB IS MULTILAYER PCB, SEE STACK-UP DIAGRAM.
- TRACE WIDTH & GAP PARAMETERS ARE SHOWN FOR MEGTRON6 R-5775(G), CLOTH STYLE:1080 WITH DIELECTRIC THICKNESS .0051; COPPER: 1/2 OZ.+PLATING. FOR OTHER MATERIALS TRACE WIDTH AND GAP MAY NEED TO BE MODIFIED.
- COPPER LAYER 4 OF THE PCB ARE CONTINUOUS GROUND PLANE.

- DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)
- DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

LAYER 2, B-VIA & PTH



LAYER 3 & PTH

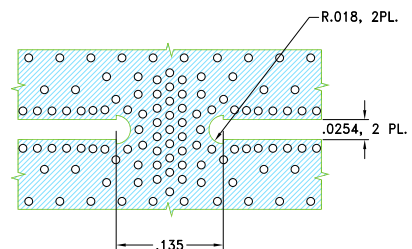


Figure 2. Suggested PCB Layout PL-730



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ADDITIONAL DETAILED INFORMATION IS AVAILABLE ON OUR DASHBOARD. [CLICK HERE](#)

Performance Data & Graphs	Data Graphs S-Parameter (S2P Files) Data Set (.zip file) De-embedded to device pads
Case Style	NM1812C-3 Lead Finish: Tin over Nickel Plating
RoHS Status	Compliant
Tape and Reel	F77
Suggested Layout for PCB Design	PL-730
Evaluation Board	TB-BFHK-1742C+ Gerber File
Environmental Rating	ENV06T12

- NOTES**
- A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
 - B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
 - C. The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the standard terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/terms/viewterm.html

