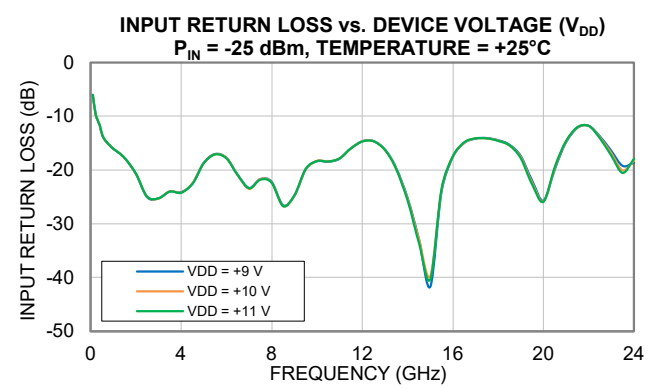
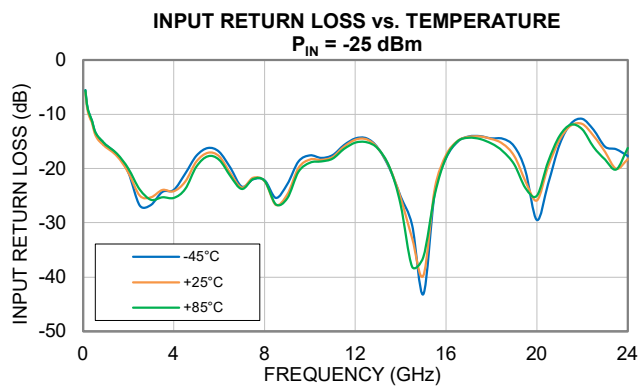
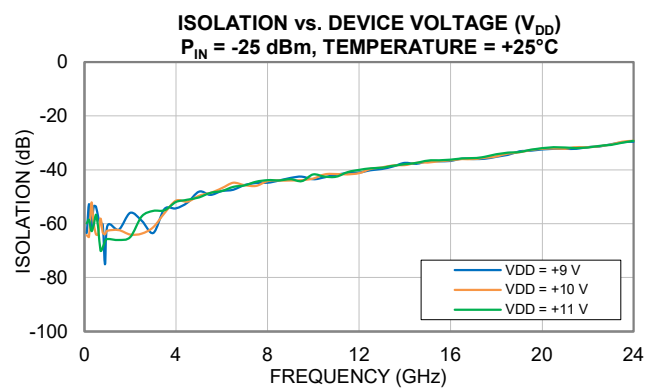
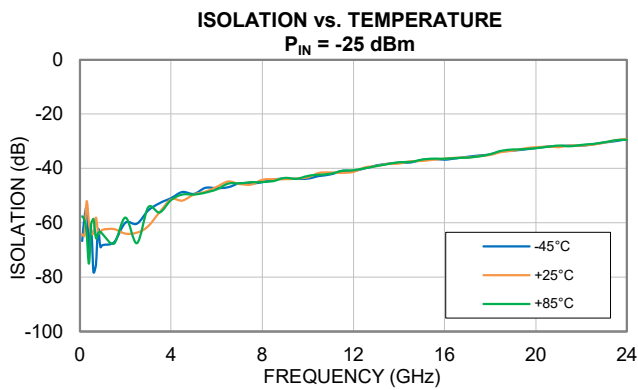
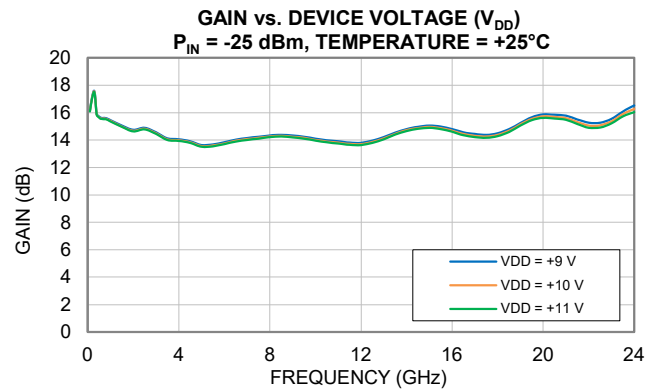
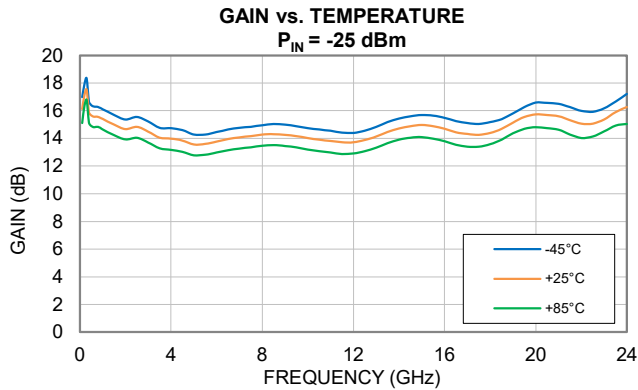


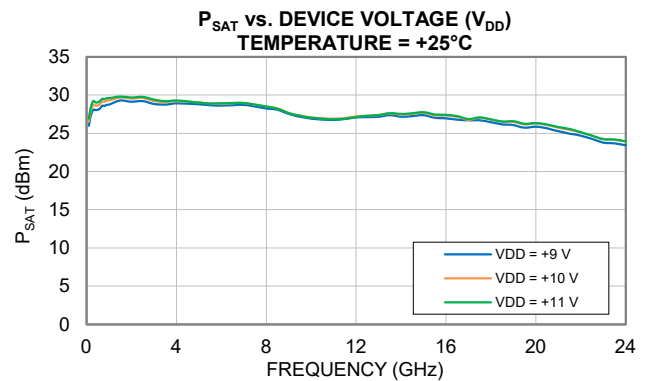
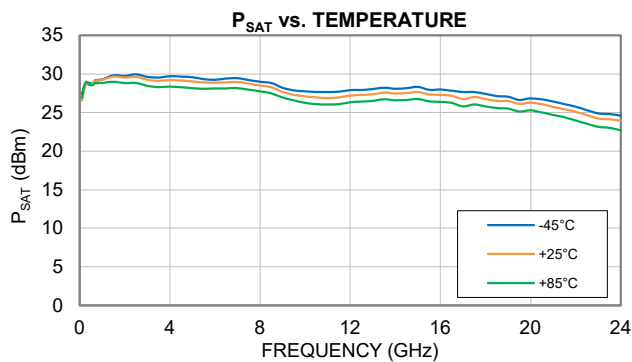
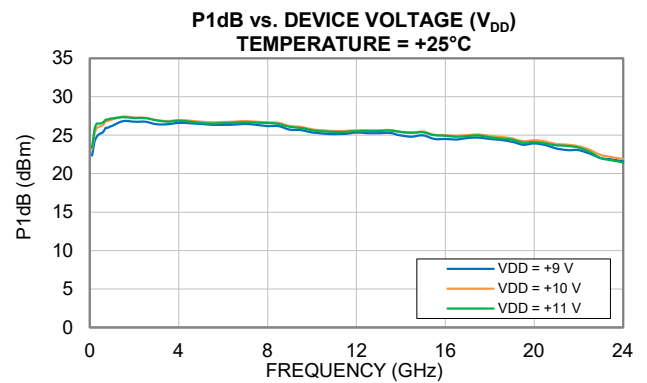
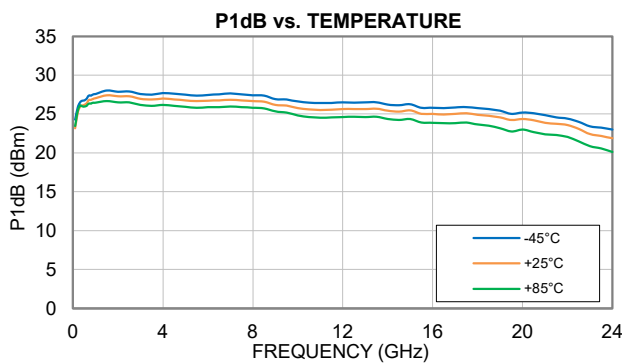
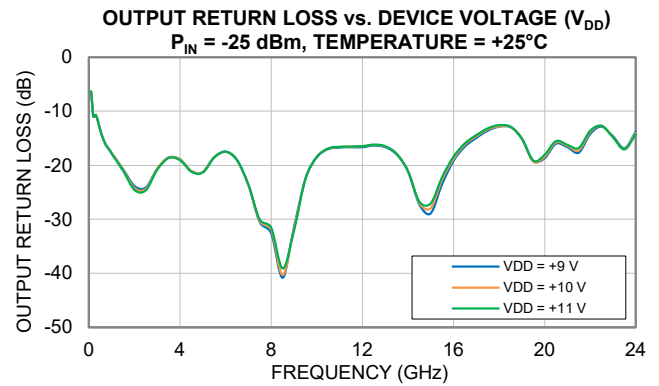
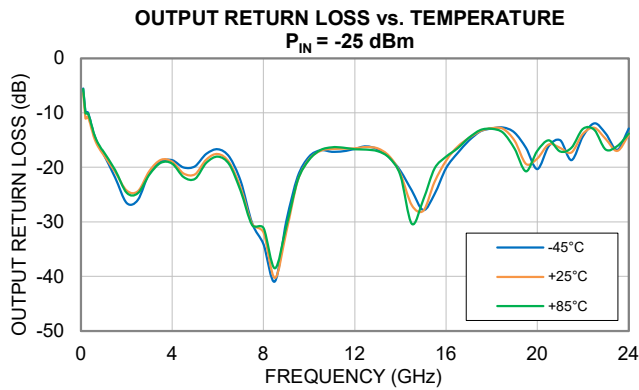
Typical Performance Curves

Note: Data over temperature was taken at $V_{DD} = +10$ V. At $+25^\circ\text{C}$, V_{GG1} has been adjusted to achieve $I_{DD} = 300$ mA. V_{GG1} was not adjusted at -45°C or $+85^\circ\text{C}$. For over voltage data, V_{GG1} was adjusted until $I_{DD} = 300$ mA at all V_{DD} levels specified. All data taken with $V_{GG2} = +3.5$ V.



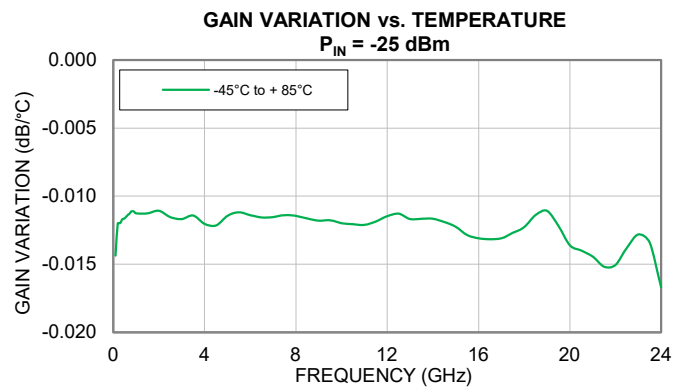
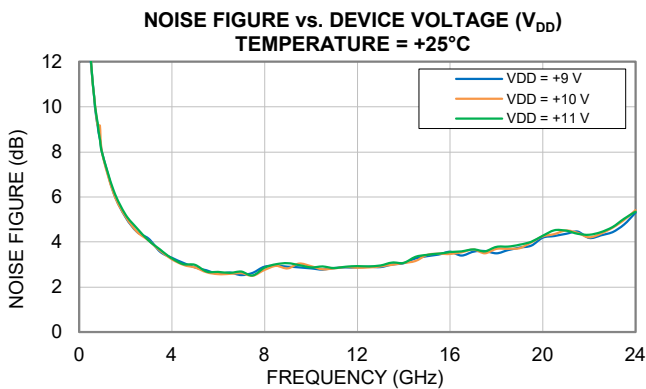
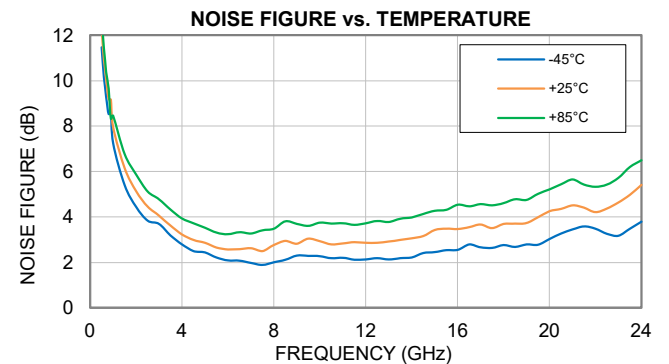
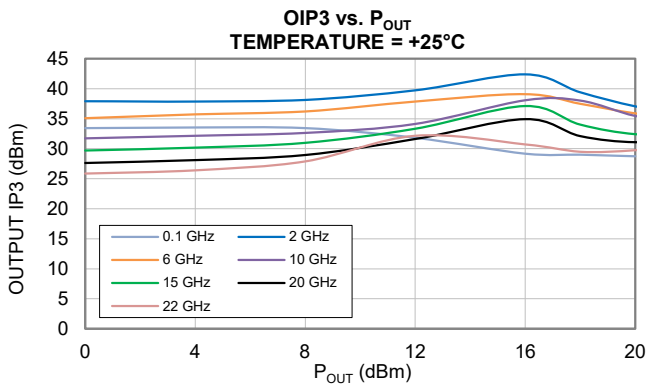
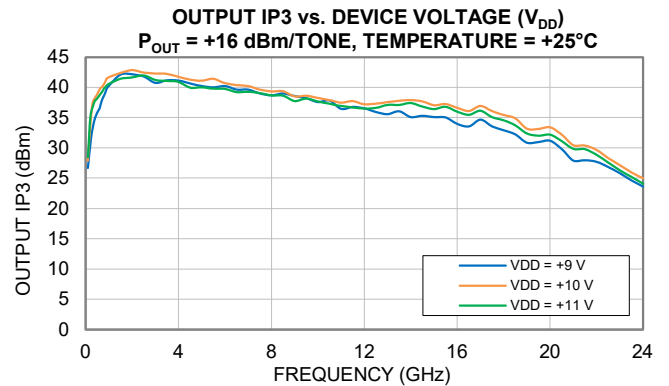
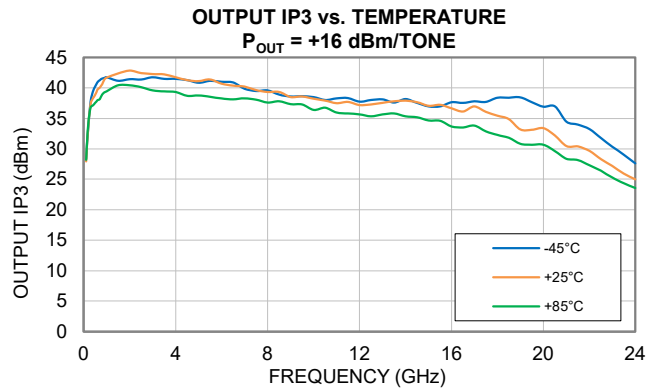
Typical Performance Curves

Note: Data over temperature was taken at $V_{DD} = +10$ V. At $+25^{\circ}\text{C}$, V_{GG1} has been adjusted to achieve $I_{DD} = 300$ mA. V_{GG1} was not adjusted at -45°C or $+85^{\circ}\text{C}$. For over voltage data, V_{GG1} was adjusted until $I_{DD} = 300$ mA at all V_{DD} levels specified. All data taken with $V_{GG2} = +3.5$ V.



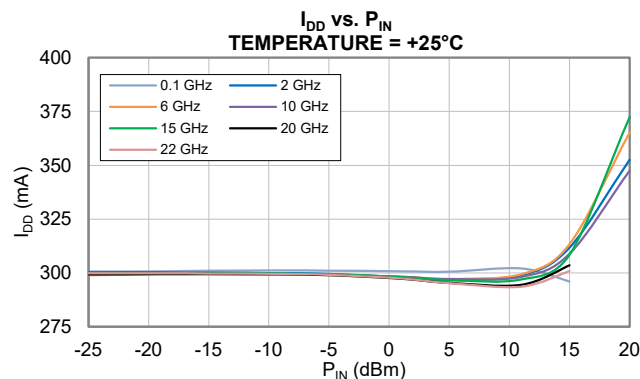
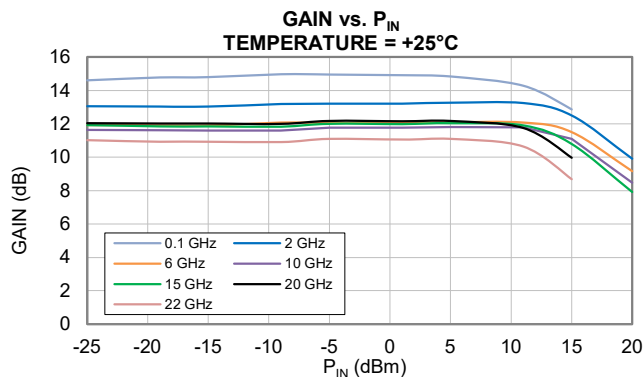
Typical Performance Curves

Note: Data over temperature was taken at $V_{DD} = +10$ V. At $+25^\circ\text{C}$, V_{GG1} has been adjusted to achieve $I_{DD} = 300$ mA. V_{GG1} was not adjusted at -45°C or $+85^\circ\text{C}$. For over voltage data, V_{GG1} was adjusted until $I_{DD} = 300$ mA at all V_{DD} levels specified. All data taken with $V_{GG2} = +3.5$ V.



Typical Performance Curves

Note: Data over temperature was taken at $V_{DD} = +10$ V. At $+25^{\circ}\text{C}$, V_{GG1} has been adjusted to achieve $I_{DD} = 300$ mA. V_{GG1} was not adjusted at -45°C or $+85^{\circ}\text{C}$. For over voltage data, V_{GG1} was adjusted until $I_{DD} = 300$ mA at all V_{DD} levels specified. All data taken with $V_{GG2} = +3.5$ V.



Typical Performance Curves

Note: Data was taken at $V_{DD} = +10\text{ V}$ and $V_{GG2} = +3.5\text{ V}$. At $+25^\circ\text{C}$, V_{GG1} has been adjusted to achieve $I_{DD} = 300\text{ mA}$. Data was taken on a modified TB-AVA-223MPC+ test board using an external bias tee. See Figure 3.

