

NEW!  
Two & Three  
Section Models

MMIC

# REFLECTIONLESS FILTERS DICE

50Ω DC to 21 GHz

## The Big Deal

- High Stopband rejection, up to 50 dB
- Patented design terminates stopband signals
- Pass band cut-off up to 11 GHz
- Stop band up to 26 GHz
- Excellent repeatability through IPD\* process



**X-Series**

Available in Low Pass  
and High Pass designs

## Product Overview

Mini-Circuits' **X-Series** of reflectionless filters now includes 2- and 3-section models, giving you ultra-high rejection in the stopband – up to 50 dB! Reflectionless filters employ a patented filter topology which absorbs and terminates stopband signals internally rather than reflecting them back to the source. This new capability enables unique applications for filter circuits beyond those suited to traditional approaches. Traditional filters are reflective in the stopband, sending signals back to the source at 100% power. These reflections interact with neighboring components and often result in intermodulation and other interferences. By eliminating stopband reflections, reflectionless filters can readily be paired with sensitive devices and used in applications that otherwise require circuits such as isolation amplifiers or attenuators.

Key Features	Advantages
Easy integration with sensitive reflective components, e.g. mixers, multipliers	Reflectionless filters absorb unwanted signals, preventing reflections back to the source. This reduces generation of additional unwanted signals without the need for extra components like attenuators, improving system dynamic range and saving board space.
High stopband rejection, up to 50 dB	Ideal for applications where suppression of strong spurious signals and intermodulation products is needed.
Enables stable integration of wideband amplifiers	Because reflectionless filters maintain good impedance in the stopband; they can be integrated with high gain, wideband amplifiers without the risk of creating instabilities in these out of band regions.
Cascadable	Reflectionless filters can be cascaded in multiple sections to provide sharper and higher attenuation, while also preventing any standing waves that could affect passband signals. Low & highpass filters can be cascaded to realize bandpass filters.
Excellent power handling in a tiny surface mount device up to 7W in passband	High power handling extends the usability of these filters to the transmit path for inter-stage filtering.
Excellent repeatability of RF performance	Through semiconductor IPD process, X-series filters are inherently repeatable for large volume production.
Excellent stability over temperature	With $\pm 0.3$ dB variation over temperature ideal for use in wide temperature range applications without the need for additional temperature compensation.
Operating temperature up to 105°C	Suitable for operation close to high power components.

\*IPD – Integrated Passive Device, is a GaAs semiconductor process



# Reflectionless Low Pass Filter Die

## XLF-362H-D+

50Ω DC to 3910 MHz

### Features

- Match to 50Ω in the stop band, eliminates undesired reflections
- Cascadable
- Excellent stopband rejection, 48 dB typ.
- Temperature stable, up to 105°C
- Protected by US Patents 8,392,495; 9,705,467, additional patent pending
- Protected by China Patent 201080014266.1
- Protected by Taiwan Patent I581494

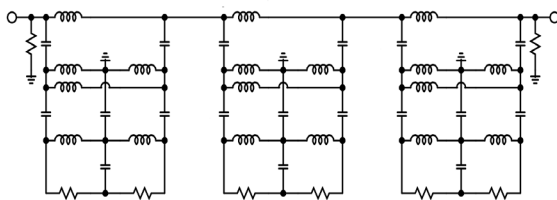
### Applications

- ISM Applications
- Mobile
- WiMAX
- Radio location.

### General Description

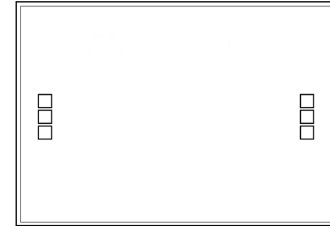
Mini-Circuits' XLF-362H-D+ three-section reflectionless filter Die employs a novel filter topology which absorbs and terminates stop band signals internally rather than reflecting them back to the source. This new capability enables unique applications for filter circuits beyond those suited to traditional approaches. Traditional filters are reflective in the stop band, sending signals back to the source at 100% of the power level. These reflections interact with neighboring components and often result in inter-modulation and other interferences. Reflectionless filters eliminate stop band reflections, allowing them to be paired with sensitive devices and used in applications that otherwise require circuits such as isolation amplifiers or attenuators.

### Simplified Schematic and Pad description



Pad#	Description
2	RF-IN
5	RF-OUT
1,3,4,6	Ground
Die bottom	Ground

Note: 1. Bond Pad material - Gold  
2. Bottom of Die - Gold plated

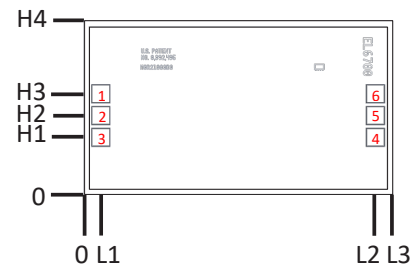


#### +RoHS Compliant

The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

Ordering Information: Refer to Last Page

### Bonding Pad Position



Dimensions in μm, Typical

L1	L2	L3	H1	H2	H3	H4	Thickness	Bond pad size
80	1340	1420	265	365	465	800	100	78 x 78

**Electrical Specifications<sup>1</sup> at 25°C**

Parameter	F#	Frequency (MHz)	Min.	Typ.	Max.	Unit
<b>Pass Band</b>	Insertion Loss	DC - F1	—	1.3	—	dB
	Frequency Cut-off	F2	—	3.0	—	
	VSWR	DC - F1	—	1.3	—	:1
<b>Stop Band</b>	Rejection	F3 - F3'	—	28	—	dB
		F3' - F4	—	48	—	
		F4 - F5	—	50	—	
	VSWR	F3 - F3'	—	1.2	—	
		F3' - F4	—	1.4	—	:1
		F4 - F5	—	2.2	—	

<sup>1</sup> Measured on Mini-Circuits Characterization Test Board. Die packaged in 3x3mm, 12-lead MCLP package and soldered on TB-844-362H+.

**Absolute Maximum Ratings<sup>4</sup>**

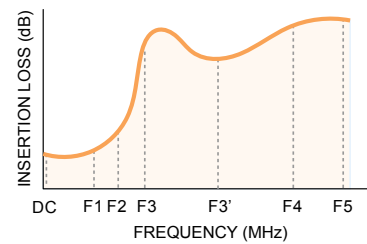
Parameter	Ratings
Operating Temperature	-55°C to +105°C
Storage Temperature	-65°C to +150°C
RF Power Input, Passband (DC-F1) <sup>2</sup>	4W at 25°C
RF Power Input, Stopband (F2-F5) <sup>3</sup>	1.6W at 25°C

<sup>2</sup> Passband rating derates linearly to 2W at 105°C ambient

<sup>3</sup> Stopband rating derates linearly to 0.8W at 105°C ambient

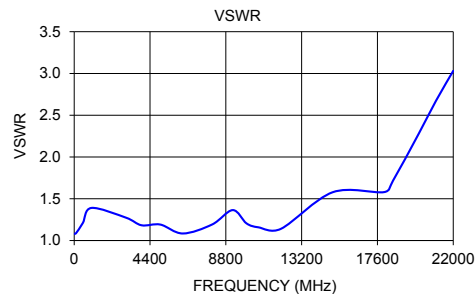
<sup>4</sup> Permanent damage may occur if any of these limits are exceeded.

SPECIFICATION DEFINITION

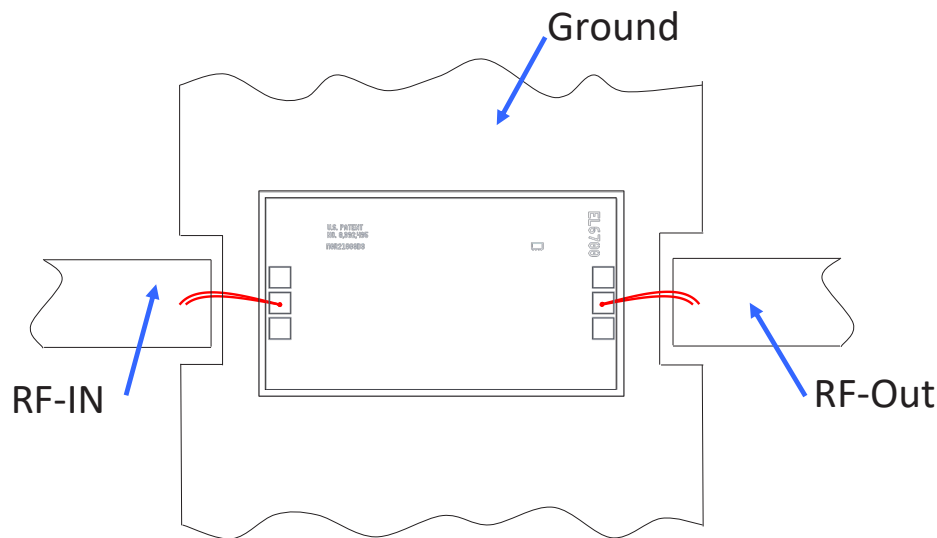


**Typical Performance Data at 25°C**

Frequency (MHz)	Insertion Loss (dB)	VSWR (:1)
10	0.81	1.08
100	0.83	1.09
500	0.91	1.21
1000	1.09	1.39
3000	1.60	1.27
3910	1.80	1.18
5000	2.15	1.19
6300	2.89	1.08
8000	5.87	1.19
9200	16.43	1.36
10000	29.34	1.21
10700	39.66	1.16
12000	40.37	1.14
15000	53.48	1.58
18000	48.52	1.58
18500	48.02	1.72
20000	47.32	2.28
21000	47.05	2.67
22000	45.88	3.03



## Assembly Diagram



Note: Ground bond wires are optional.

## Assembly and Handling Procedure

- 1. Storage**  
Dice should be stored in a dry nitrogen purged desiccators or equivalent.
- 2. ESD**  
MMIC Gallium Arsenide (GaAs) filter dice are susceptible to electrostatic and mechanical damage. Die are supplied in antistatic protected material, which should be opened in clean room conditions at an appropriately grounded anti-static workstation. Devices need careful handling using correctly designed collets, vacuum pickup tips or sharp antistatic tweezers to deter ESD damage to dice.
- 3. Die Attach**  
The Die mounting surface must be clean and flat. Using conductive silver filled epoxy, recommended epoxies are DieMat DM6030Hk-PT/H579/H579 or Ablestik 84-1LMISR4. Apply sufficient epoxy to meet required epoxy bond line thickness, epoxy fillet height and epoxy coverage around total Die periphery. Parts shall be cured in a nitrogen filled atmosphere per manufacturer's cure condition. It is recommended to use antistatic Die pick up tools only.
- 4. Wire Bonding**  
Bond pad openings in the surface passivation above the bond pads are provided to allow wire bonding to the dice gold bond pads. Thermosonic bonding is used with minimized ultrasonic content. Bond force, time, ultrasonic power and temperature are all critical parameters. Suggested wire is pure gold, 1 mil diameter. Bonds must be made from the bond pads on the Die to the package or substrate. All bond wires should be kept as short as low as reasonable to minimize performance degradation due to undesirable series inductance.



*Typical Performance Data*

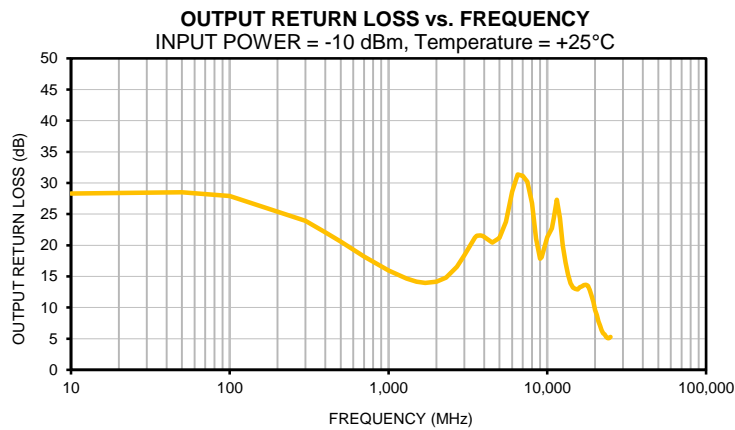
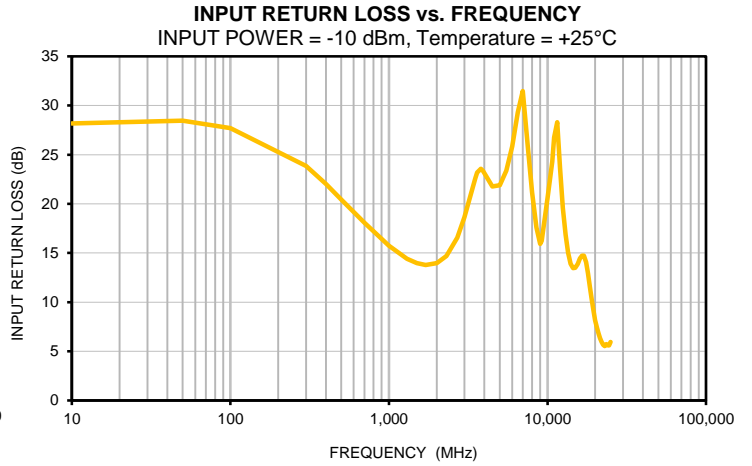
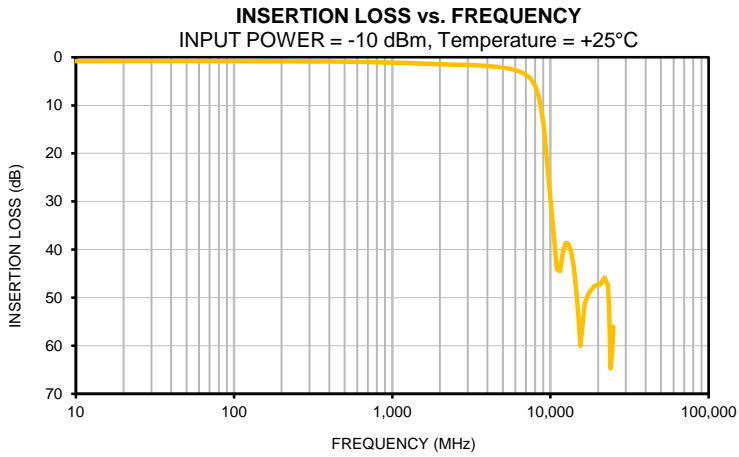
**Test Condition @ Temperature = +25°C**

FREQ.	INSERTION LOSS	INPUT RETURN LOSS	OUTPUT RETURN LOSS
(MHz)	(dB)	(dB)	(dB)
10	0.81	28.17	28.31
50	0.82	28.45	28.49
100	0.83	27.70	27.90
300	0.86	23.84	23.92
400	0.89	22.02	22.10
500	0.91	20.45	20.57
700	0.98	18.06	18.21
1000	1.09	15.74	15.95
1300	1.22	14.42	14.65
1500	1.29	13.97	14.17
1700	1.36	13.77	13.98
2000	1.44	13.97	14.17
2300	1.50	14.70	14.83
2700	1.56	16.57	16.56
3000	1.60	18.66	18.38
3500	1.70	22.60	21.24
3600	1.72	23.20	21.51
3800	1.77	23.57	21.60
3930	1.81	23.34	21.50
4500	1.98	21.77	20.45
5000	2.15	21.89	21.19
5500	2.37	23.37	23.78
6000	2.67	25.85	28.59
6500	3.06	29.30	31.37
7000	3.62	31.47	31.13
7500	4.46	25.89	30.19
8000	5.87	21.17	26.75
8500	8.47	17.61	21.10
9000	13.46	15.92	17.85
9200	16.43	16.25	18.10
10000	29.34	20.62	21.26
10700	39.66	24.28	22.72
11000	44.14	26.76	24.34
11500	44.47	28.30	27.29
12000	40.37	23.67	24.37
12500	38.61	19.60	19.99
13000	38.85	16.87	17.22
13500	40.46	15.03	15.32
14000	43.36	13.91	13.91
14500	47.54	13.46	13.23
15000	53.47	13.49	12.99
15500	59.98	13.86	12.92
16000	55.39	14.43	13.24
16500	51.16	14.71	13.39
17000	50.04	14.74	13.62
17500	49.09	14.07	13.66
18000	48.52	12.96	13.52
18500	48.02	11.59	12.83
19000	47.61	10.30	11.85
19500	47.40	9.10	10.87
20000	47.29	8.17	9.62
20500	47.25	7.41	8.79
21000	47.05	6.83	7.78
21500	46.27	6.33	7.11
22000	45.82	5.95	6.38
22500	46.79	5.65	5.89
23000	47.27	5.53	5.70
23500	51.41	5.72	5.27
24000	64.72	5.59	5.08
24500	61.42	5.61	5.11
25000	56.07	5.96	5.30
25500	52.92	6.63	5.70
26000	50.81	7.27	6.27

Note: "Test data of Die packaged in industry standard 3x3mm, 12-lead MCLP package"



## Typical Performance Curves



Note: "Test data of Die packaged in industry standard 3x3mm, 12-lead MCLP package"

All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-40° to 85° C or -40° to 105° C or -55° to 105° C or -45° to 105° C Ambient Environment	Refer to Individual Model Data Sheet
Storage Environment (Die)	-65° to 150°C	Individual Model Data Sheet
Storage Environment(Packaging)	-40° to 70°C and 40 to 60% humidity (In Factory Shipped Package)	