



CERAMIC BALUN

RF Transformer

TCW1-6000+



50Ω 3200 to 6000 MHz 1:1 Ratio

FEATURES

- Wideband, 3200 to 6000 MHz
- Miniature size 0603 (1.6x0.8mm)
- LTCC construction
- Low cost
- Aqueous washable



Generic photo used for illustration purposes only

CASE STYLE: JC0603C

+RoHS Compliant

The +Suffix identifies RoHS Compliance.
See our website for methodologies and qualifications

APPLICATIONS

- WLAN
- A/D conversion
- WiFi
- Transmitters and receivers
- Cellular

ELECTRICAL SPECIFICATIONS AT 25°C

Parameter	Frequency (MHz)	Min.	Typ.	Max.	Unit
Impedance Ratio		1			
Frequency Range		3200	—	6000	MHz
Avg. Insertion Loss ¹	3200 - 6000	—	1.1	2.0	dB
Amplitude Unbalance	3200 - 6000	—	1.5	2.2	dB
Phase Unbalance ²	3200 - 6000	—	10	13	Degree

1. Reference demo board TB-912+.

2. Relative to 180°

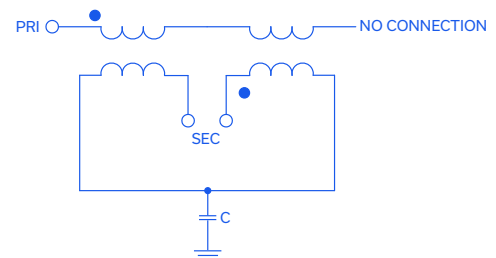
MAXIMUM RATINGS

Parameter	Ratings
Operating Temperature	-55°C to 100°C
Storage Temperature	-55°C to 100°C
RF Power ³	0.5W

3. Passband rating

Permanent damage may occur if any of these limits are exceeded.

CONFIGURATION R



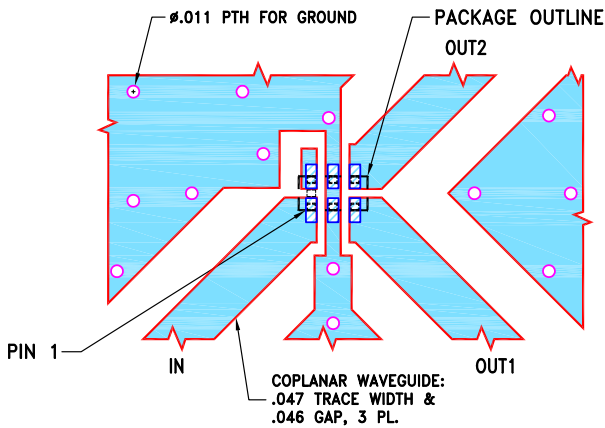


PAD CONNECTIONS

PRIMARY DOT (Unbalanced Port)	1
GND or DC feed + RF	2
SECONDARY DOT (Balanced)	3
SECONDARY (Balanced)	4
NO CONNECTION	6
GND	5

PRODUCT MARKING: N/A

DEMO BOARD MCL P/N: TB-912+
SUGGESTED PCB LAYOUT (PL-574)

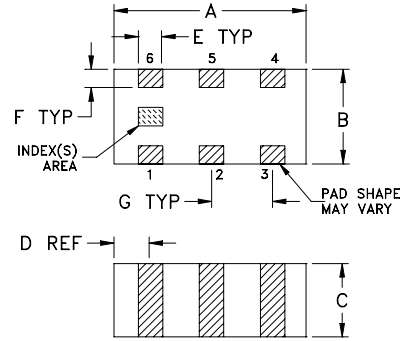


NOTES:

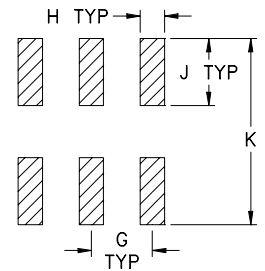
1. TRACE WIDTH & GAP PARAMETERS ARE SHOWN FOR ROGERS RO4350B WITH DIELECTRIC THICKNESS $.020 \pm .0015$. COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH & GAP MAY NEED TO BE MODIFIED.
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.

- DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER).
- DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK.

OUTLINE DRAWING



PCB Land Pattern



Suggested Layout,
Tolerance to be within $\pm .002$

OUTLINE DIMENSIONS (Inches/mm)

A	B	C	D	E	F
.063	.031	.024	.012	.008	.006
1.60	0.79	0.61	0.30	0.20	0.15
G	H	J	K	wt	
.020	.010	.022	.053	grams	
0.51	0.25	0.56	1.35	0.005	

TAPE & REEL INFORMATION: F114



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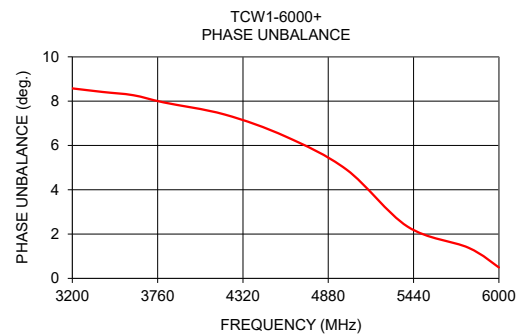
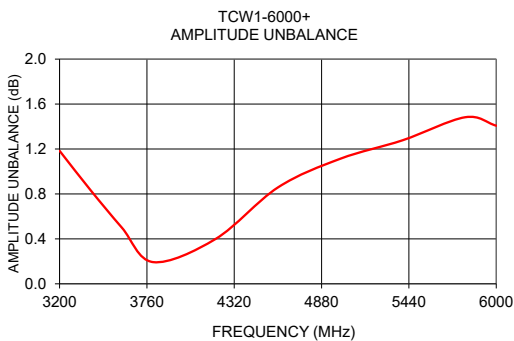
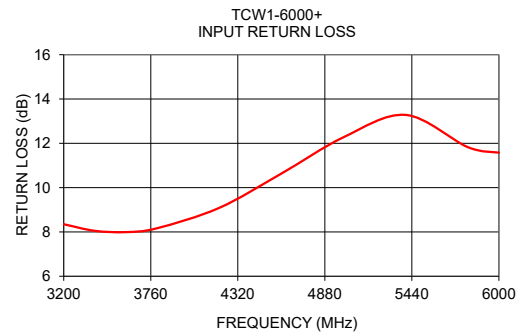
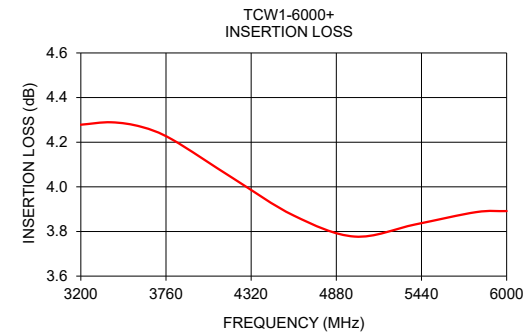


50Ω 3200 to 6000 MHz 1:1 Ratio

TYPICAL PERFORMANCE DATA⁴

Frequency (MHz)	Insertion Loss (dB)	VSWR (:1)	Amplitude Unbalance (dB)	Phase Unbalance (deg)
3200.0	4.28	8.35	1.18	8.57
3400.0	4.29	8.05	0.83	8.41
3600.0	4.27	7.99	0.50	8.27
3800.0	4.21	8.17	0.19	7.94
4200.0	4.04	9.08	0.40	7.40
4600.0	3.87	10.63	0.86	6.40
5000.0	3.78	12.27	1.11	4.91
5400.0	3.83	13.28	1.28	2.34
5800.0	3.89	11.83	1.48	1.38
6000.0	3.89	11.58	1.41	0.49

4. Measured with Agilent E5071B network analyzer using impedance conversion and port extension.



NOTES

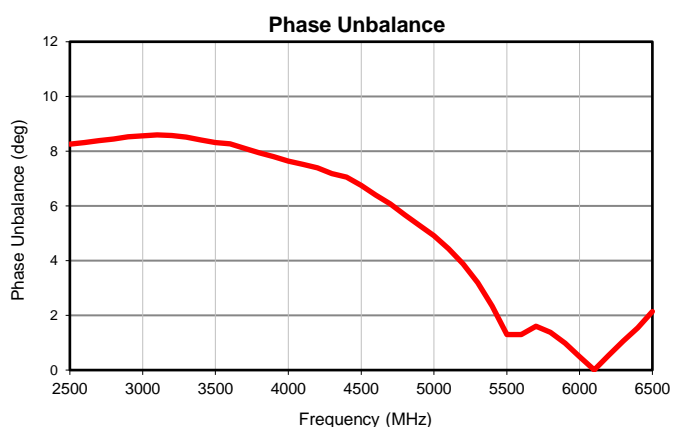
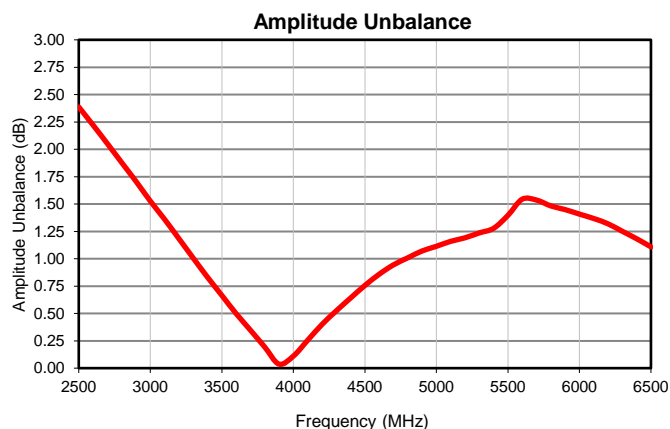
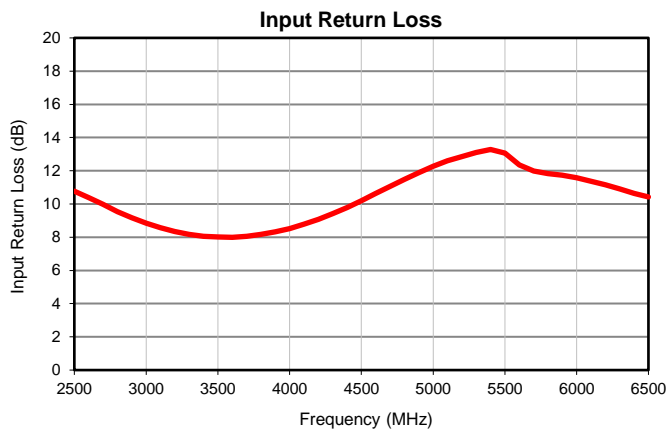
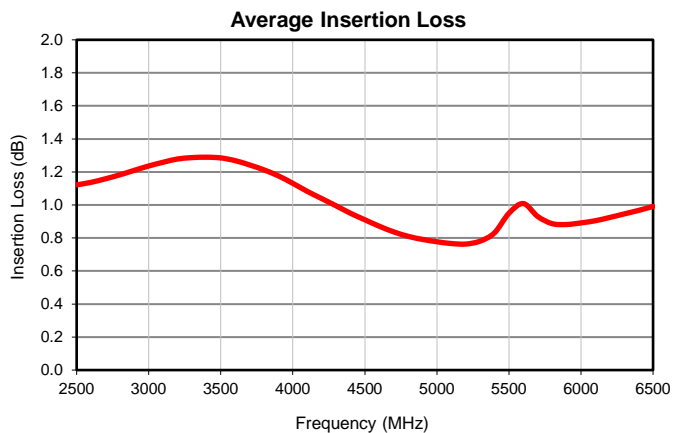
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Typical Performance Data

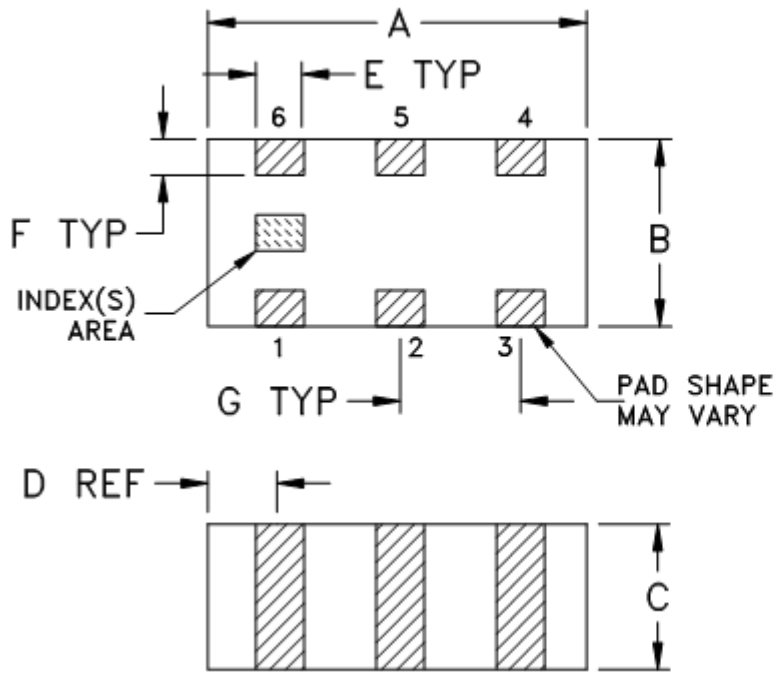
FREQUENCY (MHz)	AVERAGE INSERTION LOSS (dB)	INPUT RETURN LOSS (dB)	AMPLITUDE UNBALANCE (dB)	PHASE UNBALANCE (deg.)
2500	1.12	10.78	2.39	8.26
2600	1.14	10.38	2.22	8.31
2700	1.16	9.97	2.05	8.38
2800	1.18	9.55	1.88	8.45
2900	1.21	9.18	1.71	8.53
3000	1.24	8.85	1.53	8.57
3100	1.26	8.57	1.36	8.60
3200	1.28	8.35	1.18	8.57
3300	1.29	8.17	1.01	8.51
3400	1.29	8.05	0.83	8.41
3500	1.28	8.01	0.67	8.32
3600	1.27	7.99	0.50	8.27
3700	1.24	8.05	0.35	8.10
3800	1.21	8.17	0.19	7.94
3900	1.18	8.32	0.04	7.80
4000	1.13	8.52	0.11	7.63
4100	1.08	8.78	0.25	7.52
4200	1.04	9.08	0.40	7.40
4300	1.00	9.43	0.52	7.19
4400	0.95	9.80	0.64	7.05
4500	0.91	10.19	0.76	6.75
4600	0.87	10.63	0.86	6.40
4700	0.84	11.07	0.94	6.07
4800	0.81	11.49	1.01	5.67
4900	0.79	11.90	1.07	5.28
5000	0.78	12.27	1.11	4.91
5100	0.77	12.59	1.16	4.43
5200	0.76	12.86	1.19	3.88
5300	0.78	13.11	1.24	3.19
5400	0.83	13.28	1.28	2.34
5500	0.95	13.06	1.40	1.30
5600	1.01	12.36	1.54	1.29
5700	0.93	11.99	1.54	1.60
5800	0.89	11.83	1.48	1.38
5900	0.88	11.73	1.45	0.99
6000	0.89	11.58	1.41	0.49
6100	0.90	11.36	1.37	0.01
6200	0.92	11.15	1.32	0.54
6300	0.95	10.91	1.25	1.06
6400	0.97	10.64	1.18	1.54
6500	0.99	10.43	1.11	2.13

Typical Performance Data

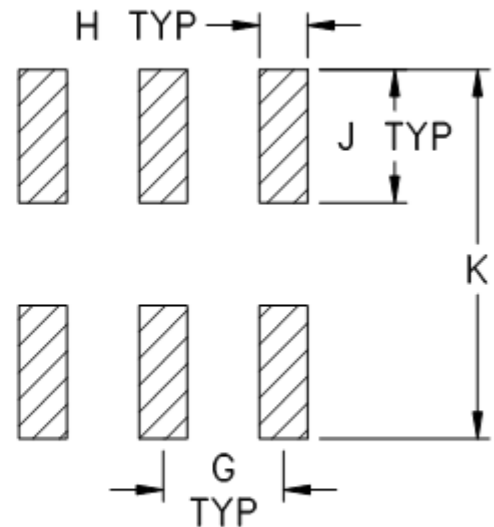


Outline Dimensions

JC0603C



PCB Land Pattern



Suggested Layout,
Tolerance to be within $\pm .002$

CASE #	A	B	C	D	E	F	G	H	J	K	WT. GRAM
JC0603C	.063 (1.60)	.031 (0.80)	.024 (0.60)	.012 (0.30)	.008 (0.20)	.006 (0.15)	.020 (0.50)	.010 (0.25)	.022 (0.55)	0.053 (1.35)	.005

Dimensions are in inches (mm). Tolerances: 2 Pl. $\pm .01$; 3 Pl. $\pm .005$

Notes:

1. Open style, ceramic base.
2. Termination finish:
For RoHS Case Styles: Tin plate over Nickel plate. All models, (+) suffix.



P.O. Box 350166, Brooklyn, New York 11235-0003 (718) 934-4500 Fax (718) 332-4661 For detailed performance specs & shopping online see Mini-Circuits web site



The Design Engineers Search Engine Provides ACTUAL Data Instantly From MINI-CIRCUITS At: www.minicircuits.com

RF/IF MICROWAVE COMPONENTS

Tape & Reel Packaging TR-F114

DEVICE ORIENTATION IN T&R



ILLUSTRATION 1

Applicable Case Styles	
GE0805C	JC0603C
GE0805C-1	JC0603C-4
GE0805C-1AP	JC0603C-6
GE0805C-7	
GE0805C-9	
GE0805C-10	
GE0805C-11	
GE0805C-12	

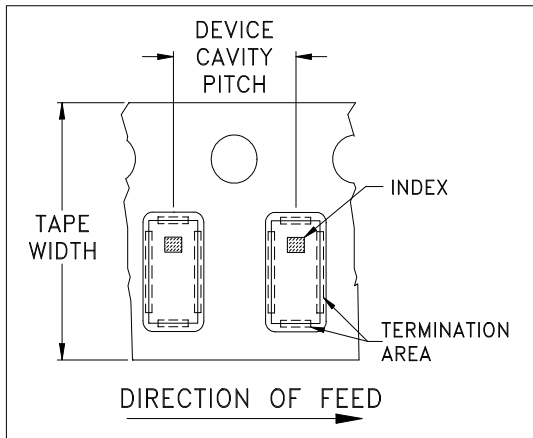


ILLUSTRATION 2

Applicable Case Styles	
GE0805C-2	JC0603C-1
GE0805C-3	JC0603C-2
GE0805C-4	JC0603C-3
GE0805C-5	JC0603C-5
GE0805C-6	JC0603C-7
GE0805C-8	JV1210C-1
GE0805C-15	

Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel	
8	4	7	Small quantity standards (see note)	20
				50
				100
				200
				500
				1000
			Standard	4000

Note: Please Consult individual model data sheet to determine device per reel availability.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

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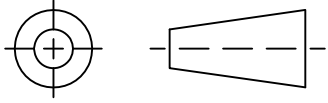
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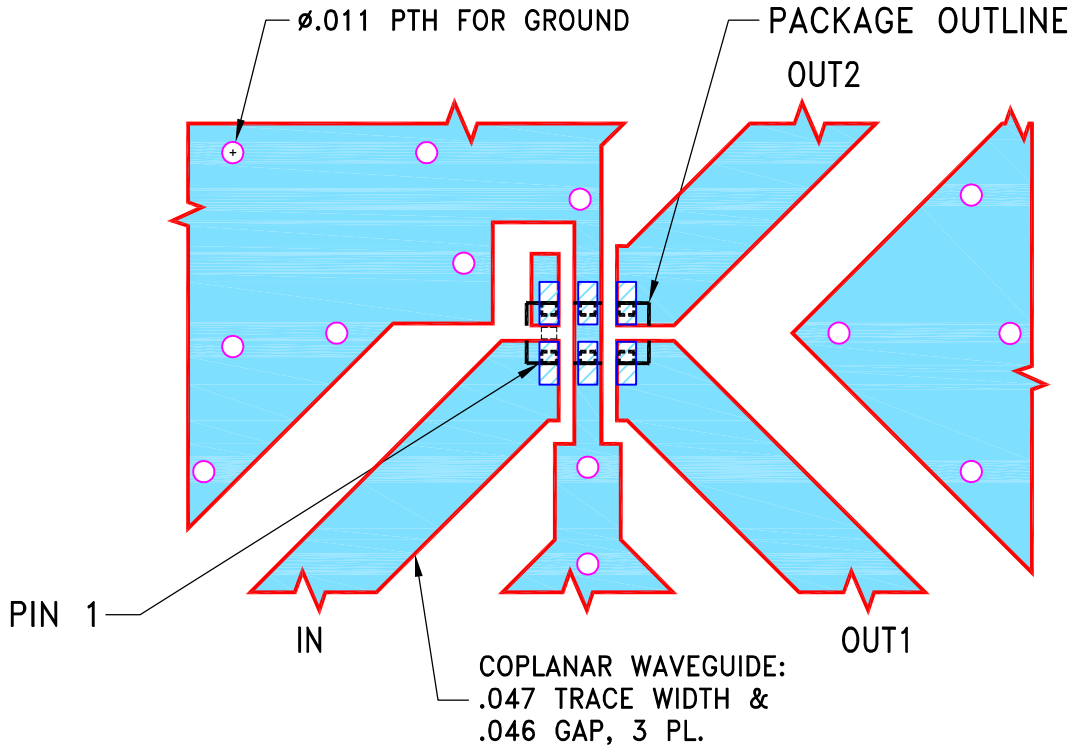
THIRD ANGLE PROJECTION



REVISIONS

REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
OR	M166452	NEW RELEASE	02/22/18	ITG	BK

SUGGESTED MOUNTING CONFIGURATION
FOR JC0603C CASE STYLE, "06TR01" PIN CODE

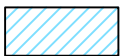


NOTES:

1. TRACE WIDTH & GAP PARAMETERS ARE SHOWN FOR ROGERS R04350B WITH DIELECTRIC THICKNESS $.020 \pm .0015$. COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH & GAP MAY NEED TO BE MODIFIED.
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.



DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER).



DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK.

UNLESS OTHERWISE SPECIFIED	INITIALS	DATE
DIMENSIONS ARE IN INCHES TOLERANCES ON: 2 PL DECIMALS ± 3 PL DECIMALS ± .005 ANGLES ± FRACTIONS ±	DRAWN	ITG 02/20/18
	CHECKED	GF 02/21/18
	APPROVED	BK 02/22/18



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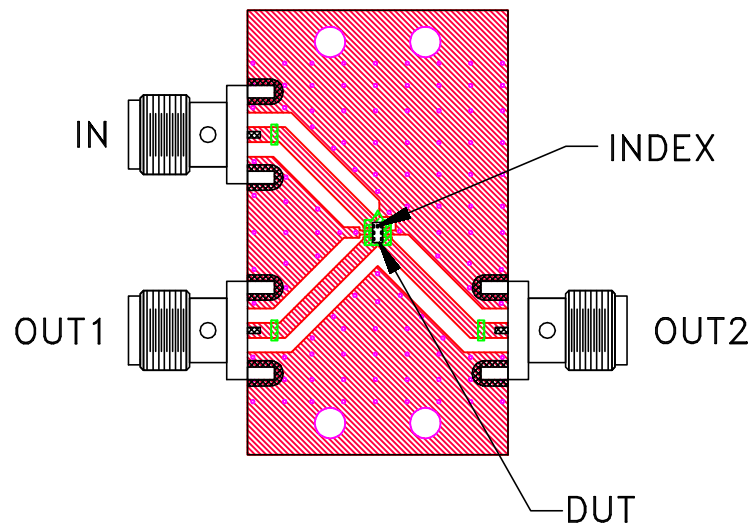
13 Neptune Avenue
 Brooklyn NY 11235

PL, 06TR01, JC0603C, TB-912+

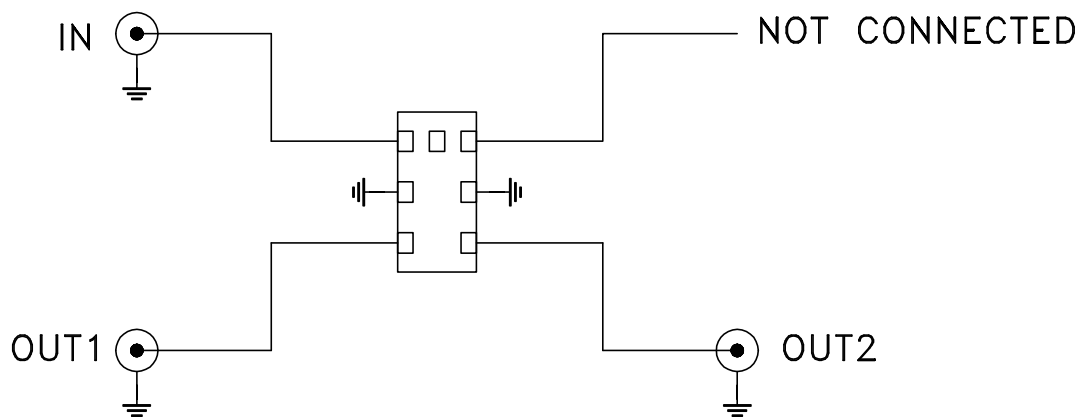
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SIZE	CODE IDENT	DRAWING NO:	REV:
A	15542	98-PL-574	OR
FILE:	98PL574	SCALE: 10:1	SHEET: 1 OF 1

Evaluation Board and Circuit




TB-912+



Schematic Diagram

Notes:

1. 50 Ohm SMA Female connectors.
2. PCB Material: R04350 or equivalent,
Dielectric Constant=3.5,
Thickness=.020 inch.

 **Mini-Circuits®**

All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-55° to 100°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Solder Reflow Heat	Sn-Pb Eutetic Process: 225°C peak Pb-Free Process 245° - 250°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, Para 4.2.5, Test S, 95% Coverage
Vibration (High Frequency)	20g peak, 10-2000 Hz, 12 times in each of three perpendicular directions (total 36)	MIL-STD-202, Method 204, Condition D
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A