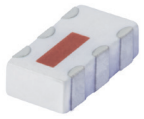
Ceramic Balun

RF Transformer

50Ω 3300 to 4500 MHz

TCN2-45+



Generic photo used for illustration purposes only

CASE STYLE: FV1206-1

+RoHS Compliant

The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

Available Tape and Reel at no extra cost

Reel Size	Devices/Reel
7"	20, 50, 100, 200, 500,1000, 3000

Maximum Ratings

Operating Temperature	-55°C to 100°C
Storage Temperature	-55°C to 100°C
Input RF Power**	5W

** From 85°C derate linearly to 2.5W at 100°C
Permanent damage may occur if any of these limits are exceeded.

Pin Connections

PRIMARY DOT	1
PRIMARY (GND), SECONDARY CT	2,5
SECONDARY DOT	6
SECONDARY	4
NOT USED	3

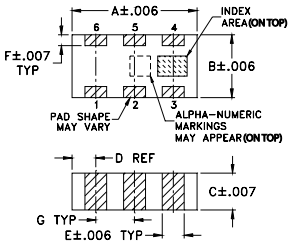
Features

- wideband, 3300 to 4500 MHz
- miniature size, 0.12"x.06"x.037"
- LTCC construction
- low cost
- aqueous washable
- patent pending

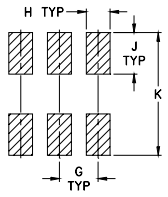
Applications

- WIMAX

Outline Drawing



PCB Land Pattern



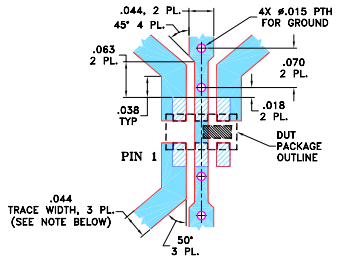
Suggested Layout,
Tolerance to be within ±.002

Outline Dimensions (inch / mm)

A	B	C	D	E	F
.126	.063	.035	.024	.022	.011
3.20	1.60	0.89	0.61	0.56	0.28

G	H	J	K		wt
.039	.024	.042	.123		grams
0.99	0.61	1.07	3.12		.020

Demo Board MCL P/N: TB-380
Suggested PCB Layout (PL-242)



NOTE:
1. TRACE WIDTH IS SHOWN FOR ROGERS RO4350B WITH DIELECTRIC THICKNESS .020" ± .0015". COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.

DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER).

DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

Electrical Specifications (T_{AMB}=25°C)

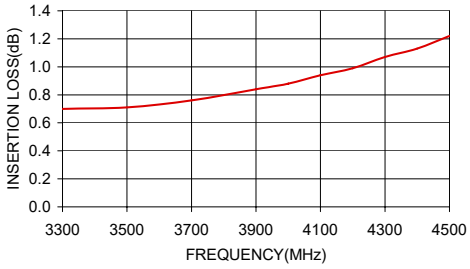
Ω RATIO (Secondary/Primary)	FREQUENCY (MHz)	INSERTION* LOSS (dB)	PHASE UNBALANCE† (Deg.) Typ.	AMPLITUDE UNBALANCE (dB) Typ.
2	3300-4500	1.5	5	1.0

* Insertion Loss is referenced to mid-band loss, 0.8 dB.
† Relative to 180°

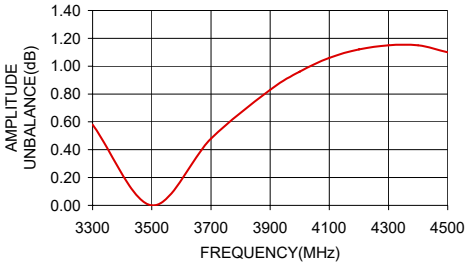
Typical Performance Data

FREQUENCY (MHz)	INSERTION LOSS (dB)	INPUT R. LOSS (dB)	AMPLITUDE UNBALANCE (dB)	PHASE UNBALANCE (Deg.)
3300.00	0.70	18.85	0.58	7.07
3500.00	0.71	17.39	0.00	6.09
3700.00	0.76	16.15	0.48	5.03
3900.00	0.84	14.98	0.83	3.78
4000.00	0.88	14.34	0.96	3.08
4100.00	0.94	13.71	1.06	2.29
4200.00	0.99	13.05	1.12	1.44
4300.00	1.07	12.38	1.15	0.48
4400.00	1.13	11.76	1.15	0.62
4500.00	1.22	11.15	1.10	1.82

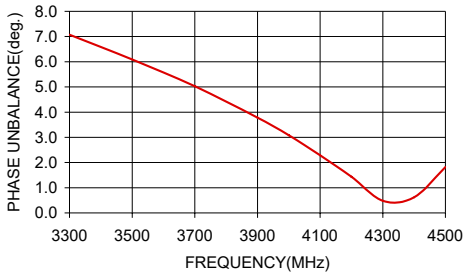
TCN2-45+
INSERTION LOSS



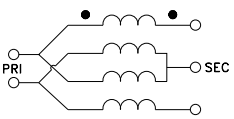
TCN2-45+
AMPLITUDE UNBALANCE



TCN2-45+
PHASE UNBALANCE



configuration H



Notes
A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
C. The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the Standard Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/MCLStore/terms.jsp

RF Transformer TCN2-45+

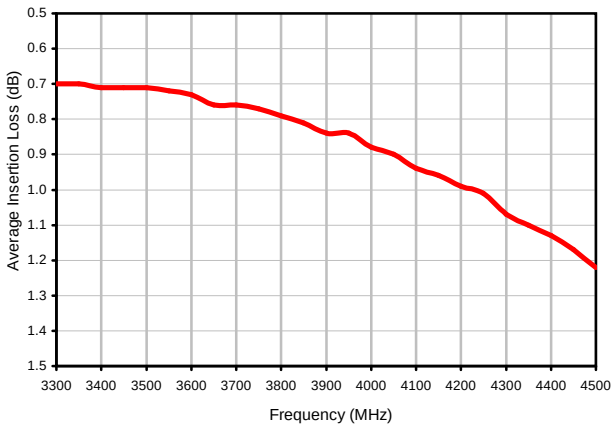
Typical Performance Data

FREQUENCY (MHz)	AVERAGE INSERTION LOSS* (dB)	INPUT RETURN LOSS (dB)	AMPLITUDE UNBALANCE (dB)	PHASE UNBALANCE** (deg)
3300	0.70	18.85	0.58	7.07
3350	0.70	18.46	0.42	6.86
3400	0.71	18.06	0.28	6.62
3450	0.71	17.73	0.13	6.35
3500	0.71	17.39	0.00	6.09
3550	0.72	17.09	0.13	5.86
3600	0.73	16.75	0.26	5.58
3650	0.76	16.47	0.37	5.32
3700	0.76	16.15	0.48	5.03
3750	0.77	15.89	0.57	4.76
3800	0.79	15.54	0.67	4.44
3850	0.81	15.28	0.75	4.12
3900	0.84	14.98	0.83	3.78
3950	0.84	14.69	0.89	3.46
4000	0.88	14.34	0.96	3.08
4050	0.90	14.03	1.01	2.70
4100	0.94	13.71	1.06	2.29
4150	0.96	13.36	1.09	1.85
4200	0.99	13.05	1.12	1.44
4250	1.01	12.72	1.14	0.97
4300	1.07	12.38	1.15	0.48
4350	1.10	12.05	1.16	0.06
4400	1.13	11.76	1.15	0.62
4450	1.17	11.43	1.14	1.19
4500	1.22	11.15	1.10	1.82

* Insertion Loss is referenced to mid-band loss , 0.8 dB.
**Phase Unbalance is relative to 180˚

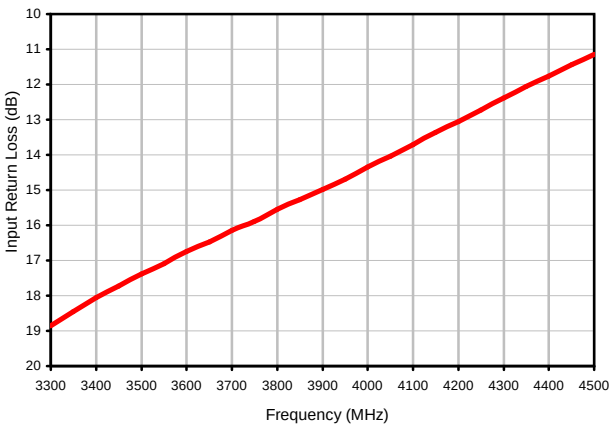
Typical Performance Curves

Average Insertion Loss*

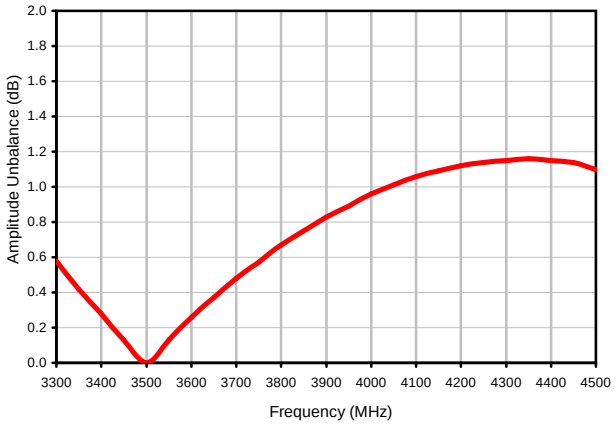


* Insertion Loss is referenced to mid-band loss , 0.8 dB.

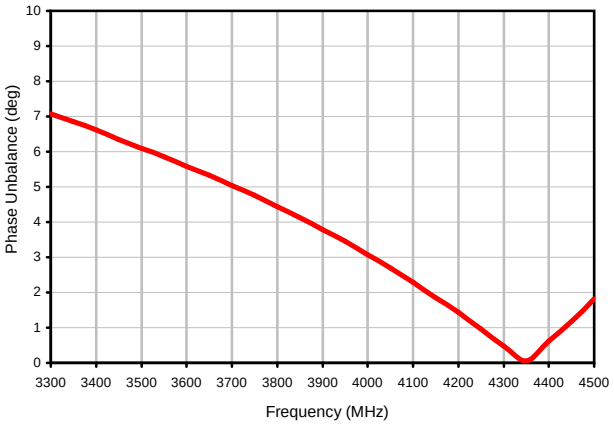
Input Return Loss



Amplitude Unbalance



Phase Unbalance**

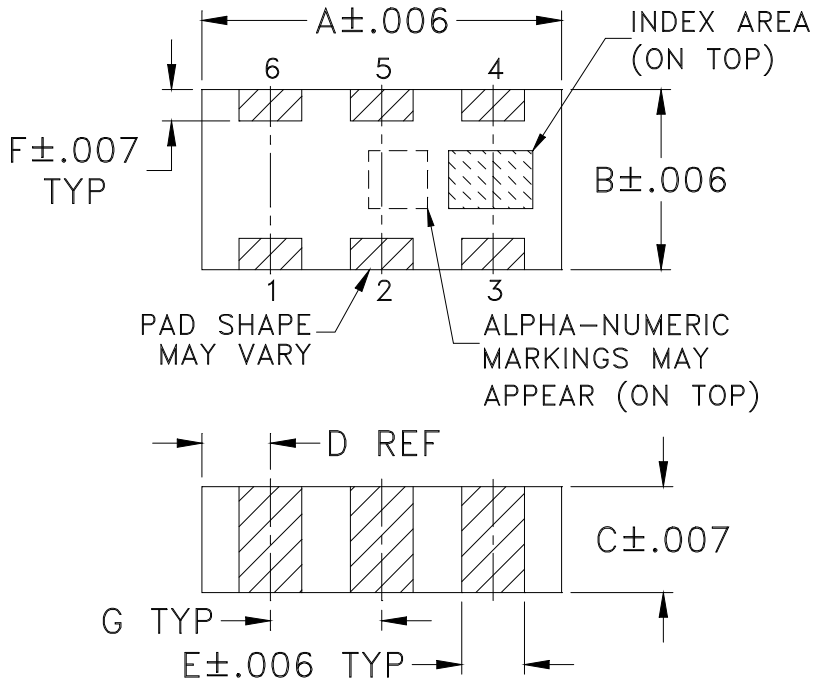


**Phase Unbalance is relative to 180˚

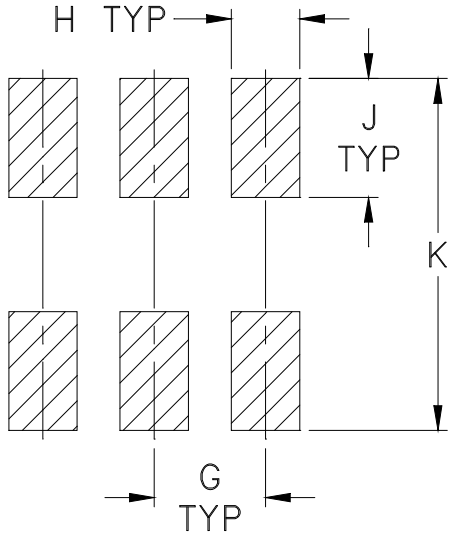
Case Style FV

FV1206-1

Outline Dimensions



PCB Land Pattern



Suggested Layout,
Tolerance to be within ±.002

CASE #	A	B	C	D	E	F	G	H	J	K	L	M	N	P	WT. GRAM
FV1206-1	.126 (3.20)	.063 (1.60)	.035 (0.89)	.024 (0.61)	.022 (0.56)	.011 (0.28)	.039 (0.99)	.024 (0.61)	.042 (1.07)	.123 (3.12)	-- --	-- --	-- --	-- --	.020

Dimensions are in inches (mm). Tolerances: 2 Pl. ± .01; 3 Pl. ± .005

Notes:

1. Open style, ceramic base.
2. Termination finish: **as shown below or indicated on Data Sheet.**
   For RoHS Case Styles: Tin plate over Nickel plate. All models, (+) suffix.
   For RoHS-5 Case Styles: Tin-Lead plate. All models, no (+) suffix.

Tape & Reel Packaging TR-F75

DEVICE ORIENTATION IN T&R

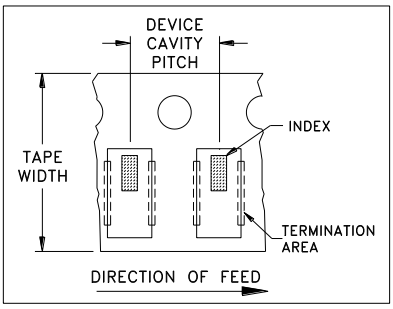


ILLUSTRATION 1

Applicable Case Styles
FV1206-1
FV1206-3

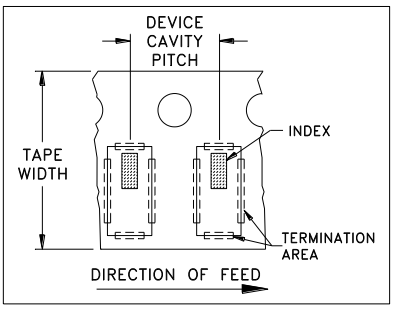


ILLUSTRATION 2

Applicable Case Styles
FV1206-4
FV1206-5
FV1206-6
FV1206-7
FV1206-9

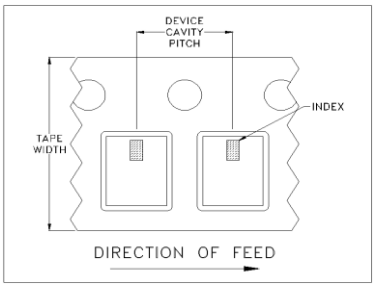


ILLUSTRATION 3

Applicable Case Styles
FV1206-12
GE0805C-18
NL1008C-6
NL1008C-7
NL1008C-9
NL1008C-10

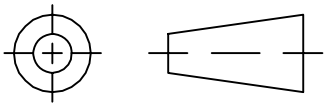
Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel	
8	4	7	Small quantity standards (see note)	20
				50
				100
				200
				500
				1000
			Standard	3000

Note: Please consult individual model data sheet to determine device per reel availability.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

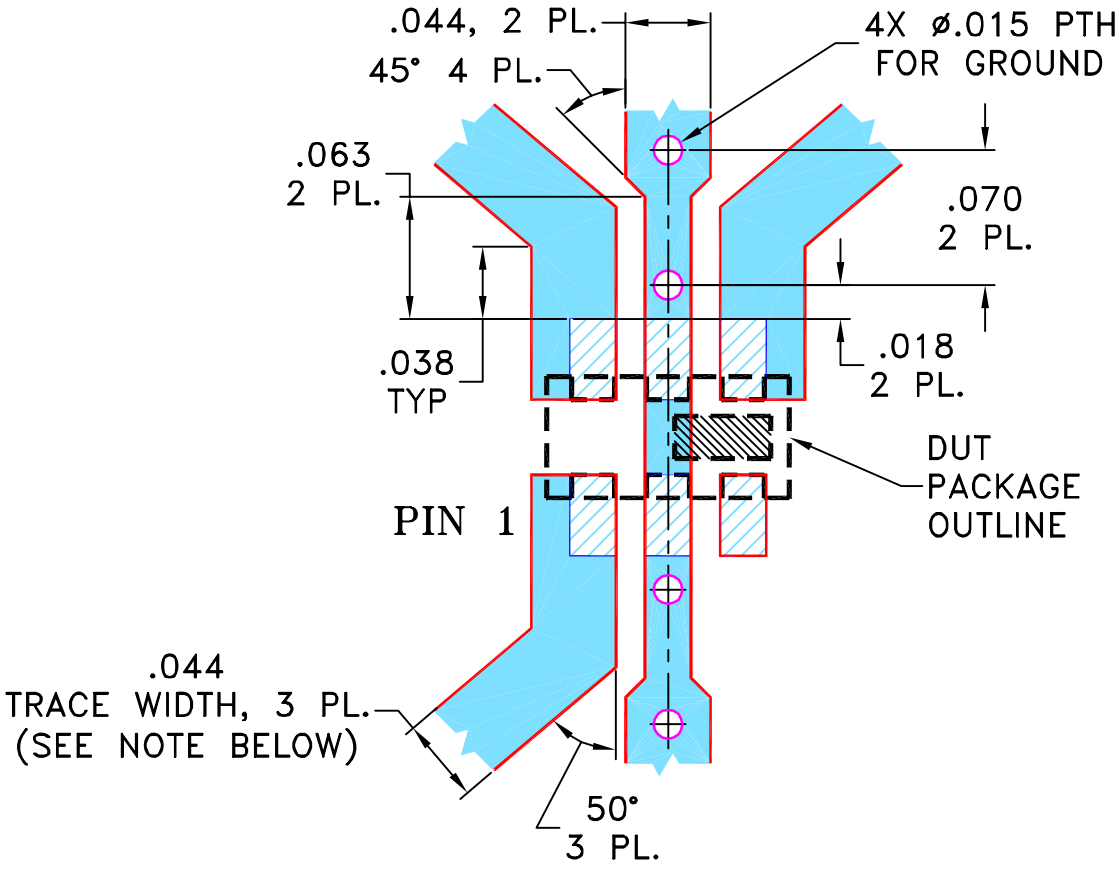
Go to: www.minicircuits.com/pages/pdfs/tape.pdf

THIRD ANGLE PROJECTION



REVISIONS					
REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
OR	M105988	NEW RELEASE	07/06/06	AV	ABD

SUGGESTED MOUNTING CONFIGURATION
FOR FV1206-1 CASE STYLE, "px" PIN CONNECTION



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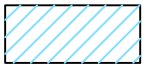
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.

DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER).



DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

UNLESS OTHERWISE SPECIFIED		INITIALS	DATE
DIMENSIONS ARE IN INCHES	DRAWN	AV	06/26/06
TOLERANCES ON:	CHECKED	IL	07/05/06
2 PL DECIMALS ±	APPROVED	ABD	07/06/06
3 PL DECIMALS ± .005			
ANGLES ±			
FRACTIONS ±			

ASHEETA1.DWG REV:A DATE:01/12/95

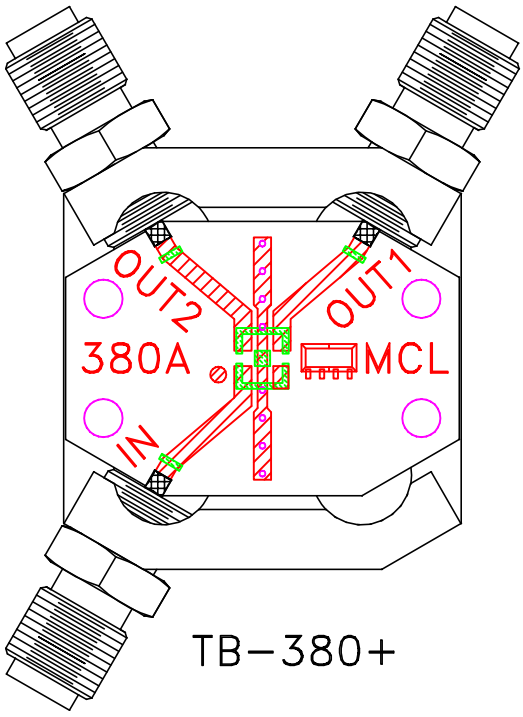
Mini-Circuits® 13 Neptune Avenue Brooklyn NY 11235

PL, px, FV1206-1, TCN2, TB-380

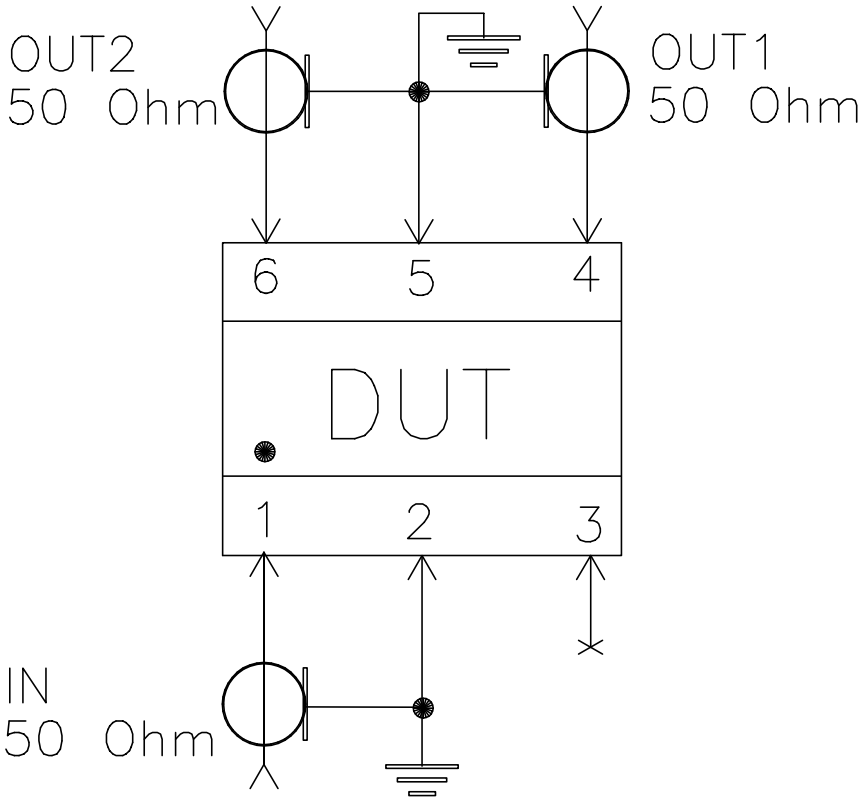
SIZE	CODE IDENT	DRAWING NO:	REV:
A	15542	98-PL-242	OR

FILE:	SCALE:	SHEET:
98PL242	10:1	1 OF 1

Evaluation Board and Circuit



TB-380+



Schematic Diagram

Notes:

1. SMA Female connectors.
2. PCB Material: Rogers RO4350 or equivalent, Dielectric Constant=3.5, Thickness=.020 inch.

Mini-Circuits®

Mini-Circuits

Environmental Specifications ENV06

All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-55° to 100°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Solder Reflow Heat	Sn-Pb Eutetic Process: 225°C peak Pb-Free Process 245° - 250°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, Para 4.2.5, Test S, 95% Coverage
Vibration (High Frequency)	20g peak, 10-2000 Hz, 12 times in each of three perpendicular directions (total 36)	MIL-STD-202, Method 204, Condition D
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A