

Monolithic Amplifier

RAM-3+

50Ω DC to 2 GHz

FEATURES

- · Wideband, DC to 2 GHz
- Cascadable Ceramic Package
- Internally Matched to 50Ω
- Low Noise Figure, 6.0 dB Typ.
- Excellent Repeatability
- Aqueous Washable
- Protected Under US Patent 6,943,629



Generic photo used for illustration purposes only

CASE STYLE: AF190

+RoHS Compliant
The +Suffix identifies RoHS Compliance.
See our website for methodologies and qualifications

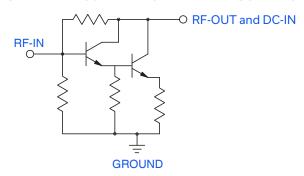
APPLICATIONS

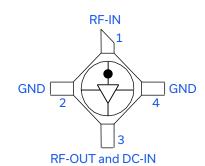
- Cellular
- UHF/VHF
- Communication Systems
- · Transmission Receivers

PRODUCT OVERVIEW

RAM-3+ (RoHS compliant) is a wideband amplifier offering high dynamic range. It has repeatable performance from lot to lot. It is enclosed in a ceramic surface-mount package. RAM-3+ uses Darlington configuration and is fabricated using InGaP HBT technology. Expected MTBF is 900 years at +100°C case temperature.

SIMPLIFIED SCHEMATIC AND PIN DESCRIPTION





Function	Pin Number	Description
RF-IN	1	RF input pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation.
RF-OUT and DC-IN	3	RF output and bias pin. DC voltage is present on this pin; therefore a DC blocking capacitor is necessary for proper operation. An RF choke is needed to feed DC bias without loss of RF signal due to the bias connection, as shown in "Recommended Application Circuit".
GND	2,4	Connections to ground. Use via holes as shown in "Suggested Layout for PCB Design" to reduce ground path inductance for best performance.



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ELECTRICAL SPECIFICATIONS AT +25°C AND 35 mA UNLESS NOTED OTHERWISE

Parameter	Conditions (GHz)	Min.	Тур.	Max.	Units
Frequency Range ¹		DC		2	GHz
	0.1		12.5		
Gain	1		12		dB
	2	8.0 ²	10.5		
Input Return Loss	DC - 2		12.5		dB
Output Return Loss	DC - 2		11.5		dB
Output Power @ 1 dB Compression	1		+10		dBm
Output IP3	1		+23		dBm
Noise Figure	1		6.0		dB
Recommended Device Operating Current			35		mA
Device Operating Voltage			+5.0		V
Device Voltage Variation vs. Temperature at 35 mA			-2.6		mV/°C
Device Voltage Variation vs. Current at +25°C			15.5		mV/mA
Thermal Resistance, Junction-to-Case ³			150		°C/W

^{1.} Guaranteed specification DC-2 GHz. Low frequency cut off determined by external coupling capacitors.

ABSOLUTE MAXIMUM RATINGS

Parameter	Ratings		
Operating Temperature	-54°C to +100°C		
Storage Temperature	-65°C to +150°C		
Operating Current	80 mA		
Power Dissipation	425 mW		
Input Power	+13 dBm		

Permanent damage may occur if any of these limits are exceeded. These ratings are not intended for continuous normal operation.

^{2.} Full temperature range.

^{3.} Case is defined as ground leads.

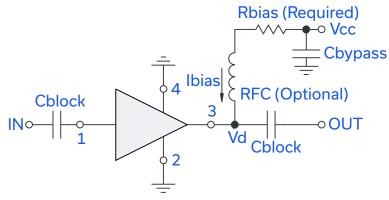


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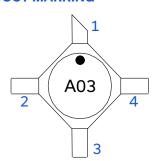
RECOMMENDED APPLICATION CIRCUIT



Test Board includes case, connectors, and components (in bold) soldered to PCB

R BIAS					
Vcc	"1%" Res. Values (Ohms) for Optimum Biasing				
7	57.6				
8	86.6				
9	115				
10	143				
11	169				
12	200				
13	226				
14	255				
15	287				

PRODUCT MARKING



 $Markings\ in\ addition\ to\ model\ number\ designation\ may\ appear\ for\ internal\ quality\ control\ purposes.$



Monolithic Amplifier

RAM-3+

DC to 2 GHz 50Ω

ADDITIONAL DETAILED TECHNICAL INFORMATION IS AVAILABLE ON OUR DASHBOARD. CLICK HERE

	Data Table
Performance Data & Graphs	Swept Graphs
	S-Parameter Data Set (.zip file)
Case Style	AF190 Ceramic surface-mount, 0.083 body diameter
Suggested Layout for PCB Design	PL-254
Evaluation Board	TB-414-3+
Environmental Ratings	ENV08T6

ESD RATING

Human Body Model (HBM): Class 1B (500 V to < 1,000 V) in accordance with ANSI/ESD STM 5.1 - 2001 Machine Model (MM): Class M1 (< 100 V) in accordance with ESD STM 5.2 - 1999

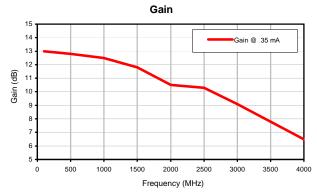
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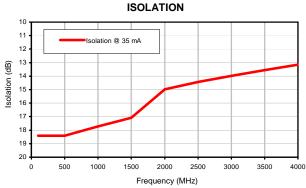


Typical Performance Data

FREQUENCY	GAIN	ISOLATION	RETURN LOSS IN	RETURN LOSS OUT
(MHz)	(dB)	(dB)	(dB)	(dB)
	35 mA	35 mA	35 mA	35 mA
100	13.00	18.42	23.10	16.48
500	12.80	18.42	24.44	15.92
1000	12.50	17.72	26.02	14.98
1500	11.80	17.08	27.96	13.56
2000	10.50	14.98	24.44	12.04
2500	10.30	14.42	15.39	11.70
3000	9.10	13.98	12.40	12.04
3500	7.80	13.56	9.90	12.04
4000	6.50	13.15	8.18	12.04

Typical Performance Curves



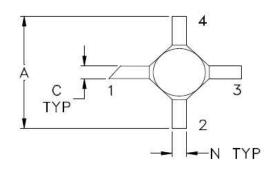


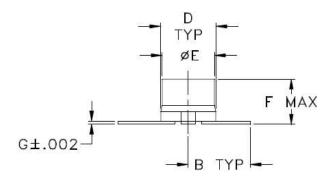




Outline Dimensions

AF190





H H K

PCB Land Pattern

Suggested Layout, Tolerance to be within ±.002

CASE#	A	В	C	D	Е	F	G	Н	J	K	L	M	N	WT. GRAM
AF190	.180 (4.57)	.090 (2.29)	.020 (0.51)	.100 (2.54)	.083 (2.11)	.072 (1.83)	.005 (0.13)	.060 (1.52)	.210 (5.33)	.060 (1.52)	.040 (1.02)	.040 (1.02)	.020 (0.51)	.04

Dimensions are in inches (mm). Tolerances: 2Pl. $\pm .03$; 3Pl. $\pm .015$

Notes:

1. Case material: Ceramic.

2. Termination material:

Nickel-Iron alloy 42.

3. Termination finish:

For RoHS Case Styles: Tin-Silver alloy plate over Nickel barrier or Matte-Tin Plate or

Matte-Tin Plate over Nickel barrier. See PCN# PCN20-035

For RoHS-5 Case Styles: Tin-Lead plate or Tin-Lead Plate over Nickel barrier.

See PCN# PCN20-035

4. Termination (1):

Identified by diagonally cut lead.

5. Special Tolerances: Termination width \pm .005 inch, termination thickness \pm .002 inch, cap diameter \pm .005 inch.





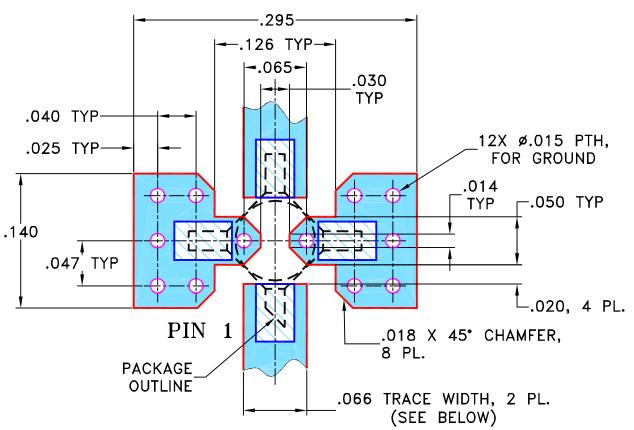
P.O. Box 350166, Brooklyn, New York 11235-0003 (718) 934-4500 Fax (718) 332-4661 For detailed performance specs & shopping online see Mini-Circuits web site

The Design Engineers Search Engine Provides ACTUAL Data Instantly From MINI-CIRCUITS At: www.minicircuits.com

THIRD ANGLE PROJECTION

	REVISIONS					
REV	ECN No.	DESCRIPTION	DATE	DR	AUTH	
OR	M108436	NEW RELEASE	11/14/06	PW	IG	
A	M108585	UPDATED DRAWING PER TB-414+	11/24/06	PW	MM	

SUGGESTED MOUNTING CONFIGURATION FOR AF190 CASE STYLE, "cb" PIN CONNECTION



NOTES:

- 1. TRACE WIDTH IS SHOWN FOR ROGERS RO4350B WITH DIELECTRIC THICKNESS .030" \pm .002"; COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.
- 2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.
- 3. IF PCB DESIGN RULES ALLOW, PLACE GROUND VIAS UNDER THE LAND PATTERN FOR BETTER RF PERFORMANCE. OTHERWISE PLACE GROUND VIAS AS CLOSE TO LAND PATTERN AS POSSIBLE.



DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)

DENOTES COPPER LAND PÁTTERN FREE OF SOLDER MASK

UNLESS OTHERWISE SPECIFIED		INITIALS	DATE				
DIMENSIONS ARE IN INCHES	DRAWN	PW	11/11/06				
TOLERANCES ON: 2 PL DECIMALS ±	CHECKED	IL	11/14/06				
3 PL DECIMALS ± .005 ANGLES ±	APPROVED	IG	11/14/06				
FRACTIONS ±							
∰ Mini−Circuits ®							

PL.	cb.	AF190.	RAM.	TB-414-X+
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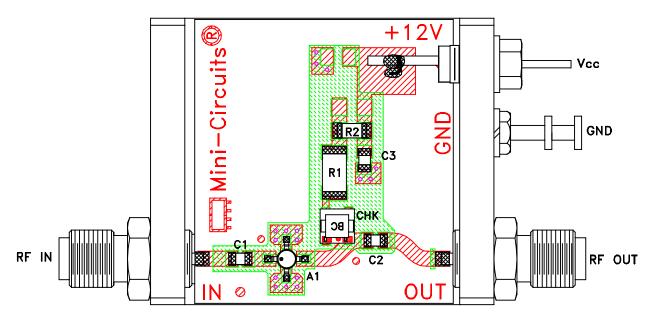
Mini-Circuits AND 13 Neptune Avenue Brooklyn NY 11235

	SIZE	CODE IDENT	DRAWING				REV:
	A	15542		98-PL	-254		A
_	FILE: 9	8PL254	SCALE:	10:1	SHEET:	1	OF 1

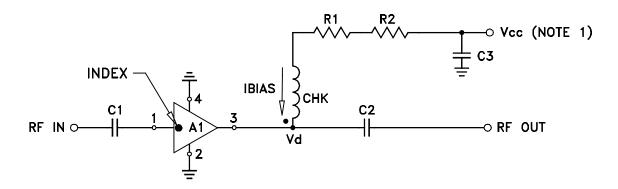
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ASHEETA1.DWG REV:A DATE:01/12/95

Evaluation Board and Circuit



TB-414-3+



COMPONENT	VALUE
A1	RAM-3(+)
C1 (NOTE 4)	2400 pF
C2 (NOTE 4)	2400 pF
C3 (bypass)	0.1 uF
R1	200 Ohms, 0.75W
R2	0 Ohm, 0.25W
CHK	Mini-Circuits TCCH-80+

Schematic Diagram

NOTE:

- 1. Vcc voltage: $+12\pm0.2V$.
- 2. SMA Female connectors.
- 3. PCB material: Rogers R04350 or equivalent, dielectric constant=3.5, dielectric thickness=.030 inch.
- 4. Capacitors, C1 & C2 should be free of resonance up to the highest frequency specified.

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Environmental Specifications

ENV47

All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-54° to 100°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-65° to 150° C Ambient Environment	Individual Model Data Sheet
HTOL	1000 hours at 125°C	MIL-STD-883, Method 1005, Condition B
Thermal Shock	-55° to 105°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Mechanical Shock	1500g, 0.5 ms, 5 shock pulses, Y1 direction only	MIL-STD-883, Method 2002, Condition B, except Y1 direction only.
Vibration (Variable Frequency)	50g peak 20-2000 Hz, 4 times in each of three perpendicular directions (total 12)	MIL-STD-883, Method 2007, Condition B
Autoclave	15 psig, 100% RH, 121°C, 96 hours	JEDEC-STD-22-B, Method A102
Solderability	10X Magnification	J-STD-002, Para 4.2.5, Test S, 95% Coverage
Solder Reflow Heat	Sn-Pb Eutetic Process: 240°C peak Pb-Free Process: 260°C peak	J-STD-020, Table 4-1, 4-2 and 5-2; Figure 5-1
Moisture Sensitivity: Level 1	Bake at 125°C for 24 hours. Soak at 85°C/85%RH for 168 hours Reflow 3 cycles at 260°C peak	J-STD-020
Marking Resistance to Solvents	Isopropyl alcohol + mineral spirits at 25°C; terpene defluxer at 25°C;	MIL-STD-202, Method 215

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ENV47

Rev: A



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All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
I	-	

distilled water + proylene glycol monomethyl ether + monoethanolamine at 63°C to 70°C

ENV47 Rev: A

03/18/11

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