



CERAMIC

Low Pass Filter

LFCN-3000D+

Mini-Circuits

50Ω DC to 3000 MHz

FEATURES

- Excellent power handling, 10W
- Small size
- 7 sections
- Temperature stable
- LTCC construction
- Protected by U.S. Patent 6,943,646



Generic photo used for illustration purposes only

CASE STYLE: FV1206

APPLICATIONS

- Harmonic rejection
- VHF/UHF transmitters/receivers
- Lab use

+RoHS Compliant

The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

ELECTRICAL SPECIFICATIONS^{1,2} AT 25°C

Parameter	F#	Frequency (MHz)	Min.	Typ.	Max.	Units	
Passband	Insertion Loss	DC-F1	DC-3000	—	—	1.2	dB
	Freq. Cut-Off	F2	3600	—	3.0	—	dB
	VSWR	DC-F1	DC-3000	—	1.2	—	:1
Stop Band	Rejection Loss	F3	4550	20	—	—	dB
		F4-F5	4780-7500	—	30	—	
	VSWR	F6	10000	—	20	—	:1
		F3-F6	4550-10000	—	20	—	

1. DC Resistance to ground is 100 Mohms min.

2. Measured on Mini-Circuits Characterization Test Board TB-270.

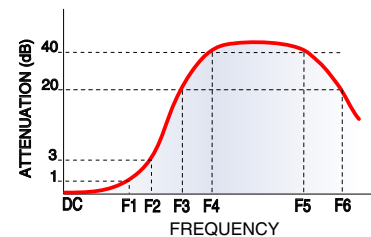
MAXIMUM RATINGS

Parameter	Ratings
Operating temperature	-55°C to 100°C
Storage temperature	-55°C to 100°C
RF Power Input ³	10 W max. at 25°C
Max. DC Voltage at pins 1&3	25 VDC
DC Current Input to Output	0.5A max. at 25°C

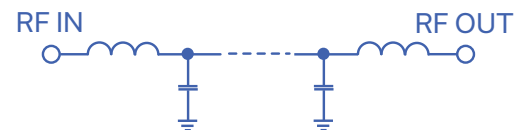
3. Derate linearly to 3.5W at 100°C ambient.

Permanent damage may occur if any of these limits are exceeded.

TYPICAL FREQUENCY RESPONSE



FUNCTIONAL SCHEMATIC



REV. J
ECO-011761
LFCN-3000D+
AD/CP/AM
220128

Mini-Circuits



CERAMIC

Low Pass Filter

LFCN-3000D+

PIN CONNECTIONS

RF IN	1
RF OUT	3
GROUND	2,4

PRODUCT MARKING: N/A

DEMO BOARD MCL P/N: TB-270
SUGGESTED PCB LAYOUT (PL-137)



- NOTES:**
1. COPLANAR WAVEGUIDE PARAMETERS ARE SHOWN FOR ROGERS RO4350B WITH THICKNESS .020" ± .0015".
COPPER: 1/2 OZ. EACH SIDE.
FOR OTHER MATERIALS TRACE WIDTH & GAP MAY NEED TO BE MODIFIED.
 2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.

- DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)
- DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

OUTLINE DRAWING



PCB Land Pattern



Suggested Layout,
Tolerance to be within ±.002

OUTLINE DIMENSIONS (Inches mm)

A	B	C	D	E	F	G	
.126	.063	.037	.020	.032	.009	.169	
3.20	1.60	0.94	0.51	0.81	0.23	4.29	
H	J	K	L	M	N	P	wt
.087	.024	.122	.024	.087	.012	.071	grams
2.21	0.61	3.10	0.61	2.21	0.30	1.80	.020

TAPE & REEL INFORMATION: F71



CERAMIC

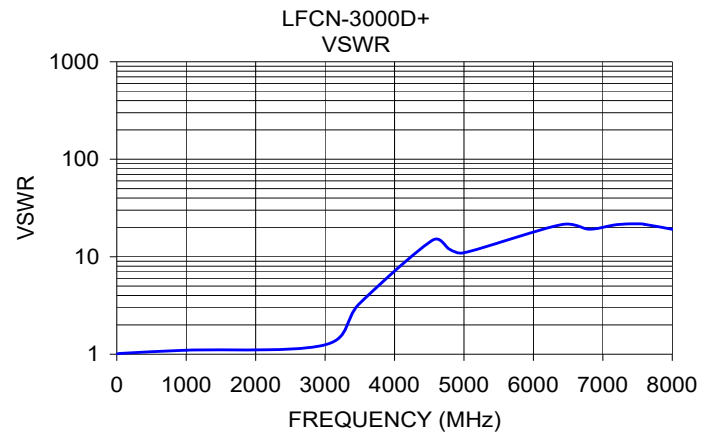
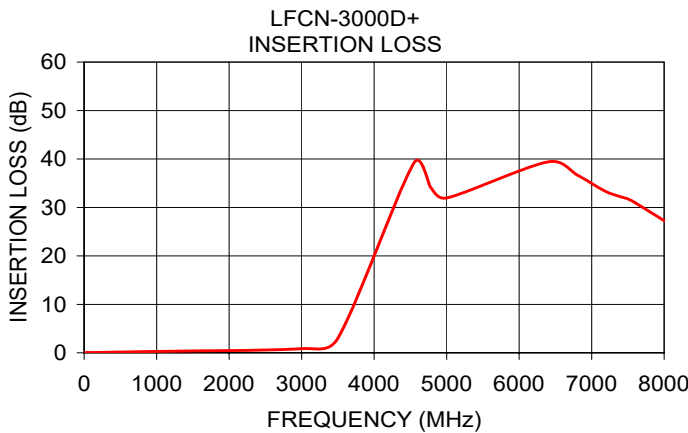
Low Pass Filter

LFCN-3000D+

Mini-Circuits

TYPICAL PERFORMANCE DATA AT 25°C

Frequency (MHz)	Insertion Loss (dB)	VSWR (:1)
10.00	0.04	1.01
50.00	0.06	1.02
1000.00	0.29	1.10
3000.00	0.86	1.25
3500.00	3.01	3.33
4550.00	39.19	14.83
4780.00	34.14	12.07
5000.00	31.96	10.97
6400.00	39.40	21.31
6800.00	36.70	19.16
7200.00	33.25	21.31
7500.00	31.75	21.77
7600.00	30.97	21.50
8400.00	23.58	16.99
8500.00	22.85	16.83



NOTES

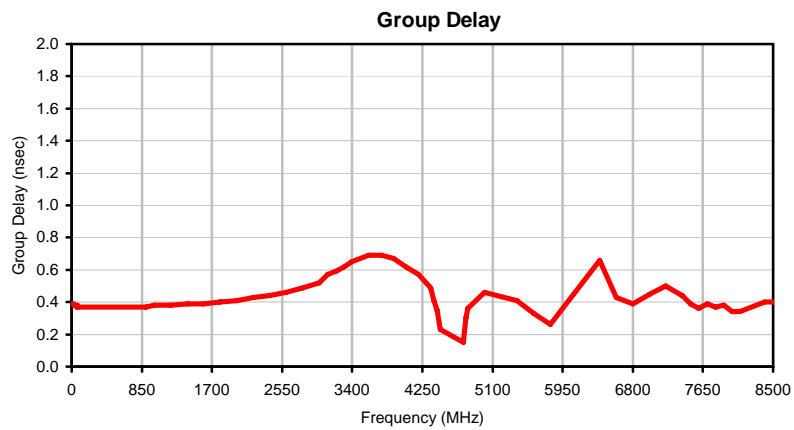
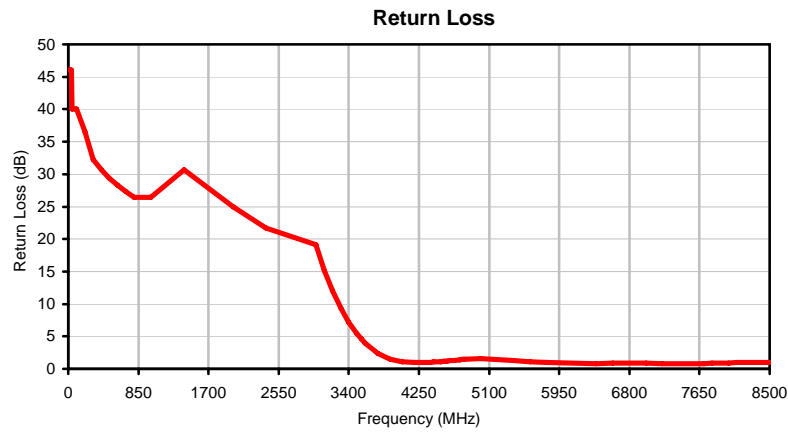
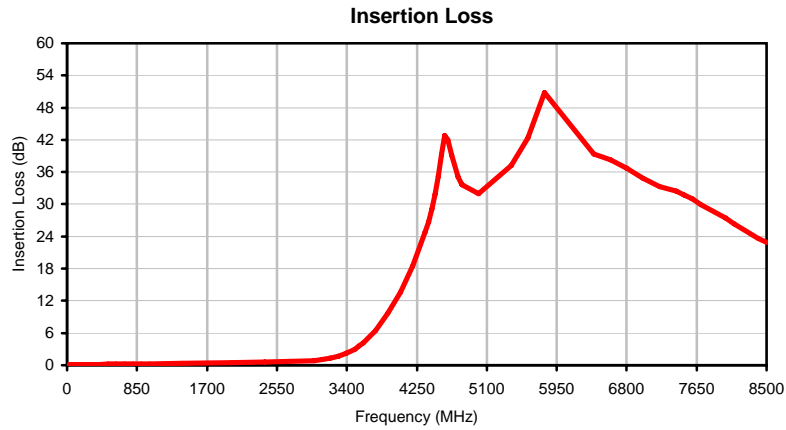
- A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
- B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
- C. The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the standard. Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/MCLStore/terms.jsp



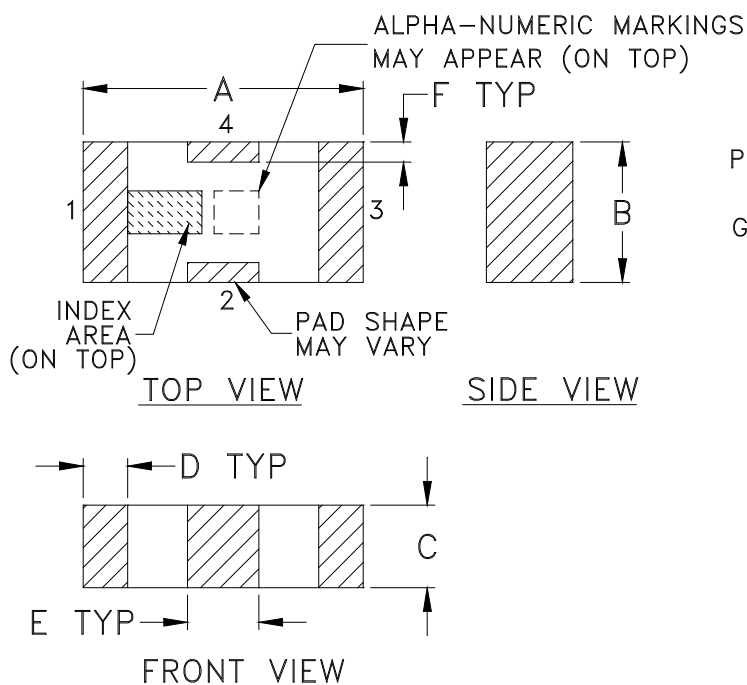
Typical Performance Data

FREQUENCY (MHz)	INSERTION LOSS (dB)	RETURN LOSS (dB)	FREQUENCY (MHz)	GROUP DELAY (nsec)
10.0	0.04	46.06	10.0	0.39
20.0	0.05	46.06	20.0	0.39
30.0	0.05	46.06	30.0	0.38
40.0	0.06	46.06	40.0	0.38
50.0	0.06	40.09	50.0	0.38
60.0	0.07	40.09	60.0	0.37
70.0	0.08	40.09	70.0	0.38
80.0	0.08	40.09	80.0	0.37
90.0	0.09	40.09	90.0	0.37
100.0	0.09	40.09	100.0	0.37
200.0	0.12	36.61	200.0	0.37
300.0	0.15	32.26	300.0	0.37
400.0	0.17	30.71	400.0	0.37
500.0	0.19	29.42	500.0	0.37
600.0	0.22	28.30	600.0	0.37
700.0	0.24	27.32	700.0	0.37
800.0	0.25	26.44	800.0	0.37
900.0	0.27	26.44	900.0	0.37
1000.0	0.29	26.44	1000.0	0.38
1400.0	0.35	30.71	1200.0	0.38
2000.0	0.46	24.94	1400.0	0.39
2400.0	0.57	21.66	1600.0	0.39
3000.0	0.86	19.08	1800.0	0.40
3100.0	1.02	15.21	2000.0	0.41
3200.0	1.26	12.01	2200.0	0.43
3300.0	1.65	9.37	2400.0	0.44
3400.0	2.21	7.20	2600.0	0.46
3500.0	3.01	5.38	2800.0	0.49
3550.0	3.52	4.62	3000.0	0.52
3600.0	4.12	3.94	3100.0	0.57
3750.0	6.46	2.40	3200.0	0.59
3900.0	9.63	1.53	3300.0	0.62
4050.0	13.61	1.11	3400.0	0.65
4200.0	18.46	0.99	3500.0	0.67
4350.0	24.71	1.01	3600.0	0.69
4390.0	26.77	1.03	3750.0	0.69
4430.0	29.10	1.06	3900.0	0.67
4470.0	31.84	1.09	4050.0	0.62
4510.0	35.16	1.13	4200.0	0.57
4550.0	39.19	1.17	4350.0	0.49
4590.0	42.77	1.22	4390.0	0.41
4630.0	42.01	1.26	4430.0	0.35
4670.0	39.11	1.31	4470.0	0.23
4750.0	35.07	1.41	4750.0	0.15
4780.0	34.14	1.44	4780.0	0.30
4800.0	33.64	1.47	4800.0	0.36
5000.0	31.96	1.59	5000.0	0.46
5400.0	37.25	1.30	5400.0	0.41
5600.0	42.40	1.10	5600.0	0.33
5800.0	50.85	0.95	5800.0	0.26
6400.0	39.40	0.82	6400.0	0.66
6600.0	38.31	0.88	6600.0	0.43
6800.0	36.70	0.91	6800.0	0.39
7000.0	34.89	0.85	7000.0	0.45
7200.0	33.25	0.82	7200.0	0.50
7400.0	32.41	0.80	7400.0	0.44
7500.0	31.75	0.80	7500.0	0.39
7600.0	30.97	0.81	7600.0	0.36
7700.0	29.93	0.83	7700.0	0.39
7800.0	29.12	0.84	7800.0	0.37
7900.0	28.29	0.87	7900.0	0.38
8000.0	27.44	0.89	8000.0	0.34
8100.0	26.36	0.94	8100.0	0.34
8400.0	23.58	1.02	8400.0	0.40
8500.0	22.85	1.03	8500.0	0.40

Typical Performance Curves



Outline Dimensions



PCB Land Pattern



Suggested Layout,
Tolerance to be within $\pm .002$

CASE #	A	B	C	D	E	F	G	H	J	K	L	M	N	P	WT. GRAM
FV1206	.126 (3.20)	.063 (1.60)	.037 (0.94)	.020 (0.51)	.032 (0.81)	.009 (0.23)	.169 (4.29)	.087 (2.21)	.024 (0.61)	.122 (3.10)	.024 (0.61)	.087 (2.21)	.012 (0.30)	.071 (1.80)	.020

Dimensions are in inches (mm). Tolerances: 2 Pl. $\pm .01$; 3 Pl. $\pm .005$

Notes:

1. Open style, ceramic base.
2. Termination finish: **as shown below or indicated on Data Sheet.**
For RoHS Case Styles: Tin plate over Nickel plate. All models, (+) suffix.
For RoHS-5 Case Styles: Tin-Lead plate. All models, no (+) suffix.



P.O. Box 350166, Brooklyn, New York 11235-0003 (718) 934-4500 Fax (718) 332-4661 For detailed performance specs & shopping online see Mini-Circuits web site



The Design Engineers Search Engine Provides ACTUAL Data Instantly From MINI-CIRCUITS At: www.minicircuits.com

RF/IF MICROWAVE COMPONENTS

Tape & Reel Packaging TR-F71



ILLUSTRATION 1

Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel	
8	4	7	Small quantity standards (see note)	20
				50
				100
				200
				500
				1000
			Standard	3000

Note: Please Consult individual model data sheet to determine device per reel availability.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

Go to: www.minicircuits.com/pages/pdfs/tape.pdf



P.O. Box 350166, Brooklyn, New York 11235-0003 (718) 934-4500 Fax (718) 332-4661 For detailed performance specs & shopping online see Mini-Circuits web site



The Design Engineers Search Engine Provides ACTUAL Data Instantly From MINI-CIRCUITS At: www.minicircuits.com

RF/IF MICROWAVE COMPONENTS

THIRD ANGLE PROJECTION



REVISIONS

REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
OR	M88634	NEW RELEASE	08/28/03	GF	ABD
A	M102713	ADDED "...WITH SMOBC"	01/17/06	MMG	IL

SUGGESTED MOUNTING CONFIGURATION
FOR FV1206 CASE STYLE, "nx" PIN CONNECTION



- NOTES:**
- COPLANAR WAVEGUIDE PARAMETERS ARE SHOWN FOR ROGERS R04350B WITH THICKNESS .020" ± .0015".
 COPPER: 1/2 OZ. EACH SIDE.
 FOR OTHER MATERIALS TRACE WIDTH & GAP MAY NEED TO BE MODIFIED.

2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.



DENOTES PCB COPPER LAYOUT WITH SMOBC
 (SOLDER MASK OVER BARE COPPER)



DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

UNLESS OTHERWISE SPECIFIED

INITIALS

DATE

DIMENSIONS ARE IN INCHES

DRAWN

GF

08/27/03

TOLERANCES ON:

CHECKED

AV

08/28/03

2 PL DECIMALS ±

APPROVED

ABD

08/28/03

3 PL DECIMALS ± .005

ANGLES ±

FRACTIONS ±



Mini-Circuits®

13 Neptune Avenue
 Brooklyn NY 11235

PL, nx, FV1206, LFCN/HFCN, TB-270

THIS DOCUMENT AND ITS CONTENTS ARE THE PROPERTY OF MINI-CIRCUITS. EXCEPT FOR USE EXPRESSLY GRANTED, IN WRITING, TO ITS VENDORS, VENDEE AND THE UNITED STATES GOVERNMENT, MINI-CIRCUITS RESERVES ALL PROPRIETARY DESIGN, USE, MANUFACTURING AND REPRODUCTION RIGHTS THERETO. THESE CONTENTS SHALL NOT BE USED, DUPLICATED OR DISCLOSED TO ANY OUTSIDE PARTY, IN WHOLE OR IN PART, WITHOUT WRITTEN PERMISSION OF MINI-CIRCUITS.

SIZE

CODE IDENT

DRAWING NO:

REV:

A

15542

98-PL-137

A

FILE: 98PL137

SCALE:

10:1

SHEET:

1 OF 1

ASHEETA1.DWG REV:A DATE:01/12/95

Evaluation Board and Circuit



Schematic Diagram

Notes:

1. SMA Female connectors.
2. PCB Material: ROGERS R04350 or equivalent, Dielectric Constant=3.5, Thickness=.020 inch.

 **Mini-Circuits®**

All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-55° to 100°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Solder Reflow Heat	Sn-Pb Eutetic Process: 225°C peak Pb-Free Process 245° - 250°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, Para 4.2.5, Test S, 95% Coverage
Vibration (High Frequency)	20g peak, 10-2000 Hz, 12 times in each of three perpendicular directions (total 36)	MIL-STD-202, Method 204, Condition D
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A