



LTCC SURFACE MOUNT

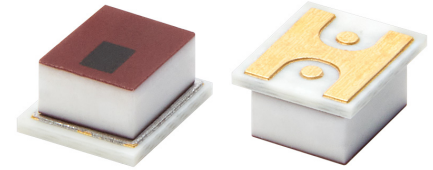
High Pass Filter

HFHKI-3100+

50Ω 3500 to 12500 MHz

THE BIG DEAL

- Insertion Loss, Typ. 1 dB
- Strong Stopband Rejection
- Ultra-Wide Passband, 3500 to 12500 MHz
- 1210 SMT Footprint
- Integrated CPWG Interposer for Easy SMT Integration
- Power Handling: 6 W
- Protected by US Patents 11,638,370 and 11,744,057

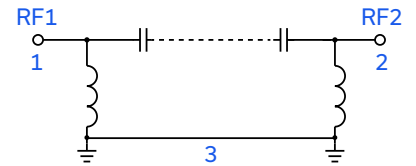


Generic photo used for illustration purposes only

APPLICATIONS

- 5G Sub-6 GHz and Emerging Mid-band systems
- X-Band Radar Systems
- Test and Measurement Equipment
- Ultra-Wideband (UWB) Systems
- Satellite Communications

FUNCTIONAL DIAGRAM



PRODUCT OVERVIEW

The HFHKI-3100+ is a 3500 to 12500 MHz LTCC high pass filter incorporating an integrated interposer for CPWG interface capability. The architecture enables direct connection to planar transmission lines. The filter provides broad passband performance with effective suppression of signals below cutoff. Its SMT implementation supports compact layouts, and the ceramic construction maintains consistent electrical behavior across environmental conditions.

ELECTRICAL SPECIFICATIONS^{1,2,3} AT +25 °C, Z₀ = 50 Ω

Parameter		F#	Frequency (MHz)	Min.	Typ.	Max.	Units
Passband	Insertion Loss	F4-F5	3500-5000	—	1.8	2.4	dB
		F5-F6	5000-10500	—	1	1.6	
		F6-F7	10500-12500	—	1.3	2.6	
Passband	Return Loss	F4-F5	3500-5000	—	11	—	dB
		F5-F6	5000-10500	—	13	—	
		F6-F7	10500-12500	—	13	—	
Stopband	Rejection	DC-F1	DC-950	60	70	—	dB
		F1-F2	950-1700	37	47	—	
		F2-F3	1700-2400	—	23	—	
Stopband	Freq. Cut-Off ⁴	F _c	3175	—	3	—	dB

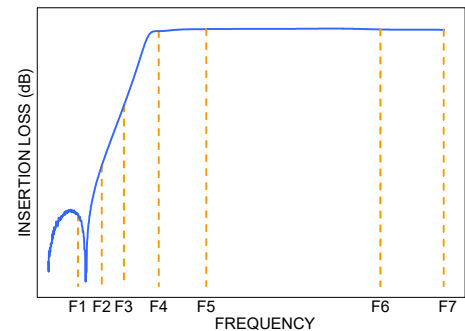
1. Tested on Evaluation Board P/N TB-HFHKI-3100C+ with the connector and feedline effects de-embedded using the 2X Thru IEEE P370 method.
2. This filter is bi-directional RF1 and RF2 ports may be interchanged, see S-Parameters for actual performance.
3. This component should not be used as a DC-block. In applications where DC voltage and/or current is present at either the input or output ports, external DC blocking capacitors are required.
4. Typical variation ± 5 %

ABSOLUTE MAXIMUM RATINGS⁵

Operating Temperature	-55 °C to +125 °C
Storage Temperature	-55 °C to +125 °C
Input Power ⁶	6 W at 25 °C

5. Permanent damage may occur if any of these limits are exceeded.
6. Power rating applies only to signals within the passband. Power rating above +25°C operating temperature decreases linearly to 1 W at +125°C.

TYPICAL FREQUENCY RESPONSE





LTCC SURFACE MOUNT

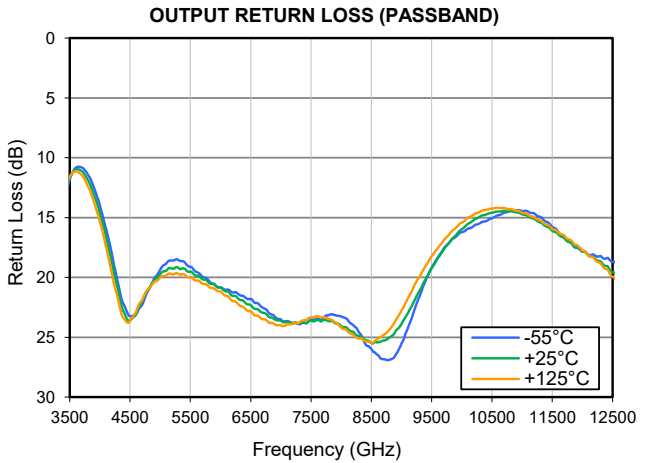
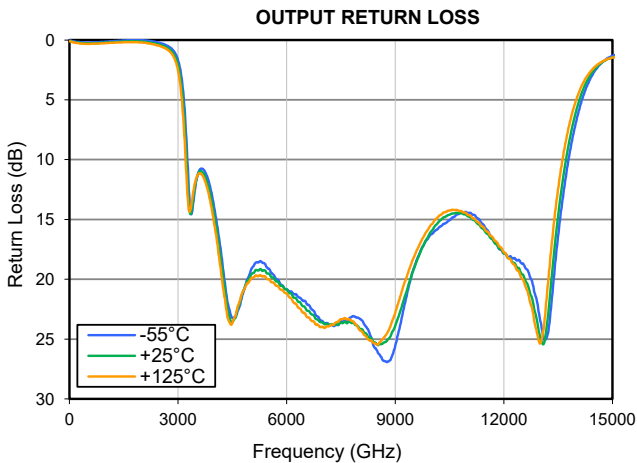
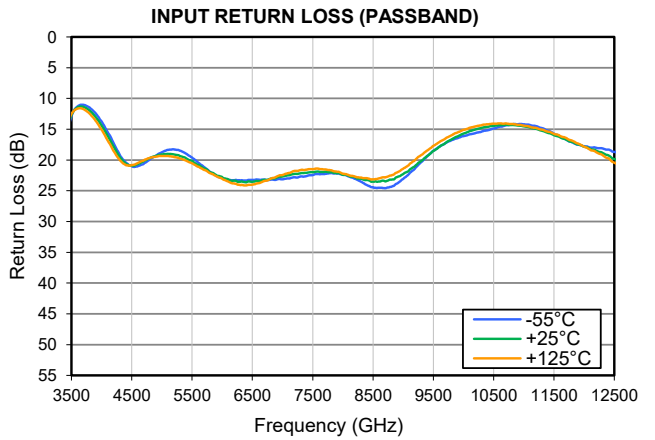
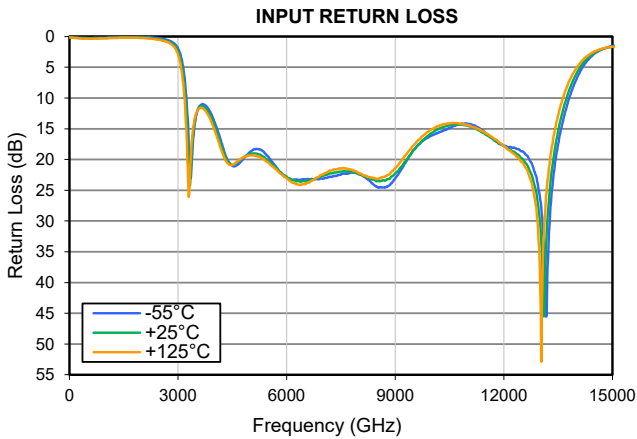
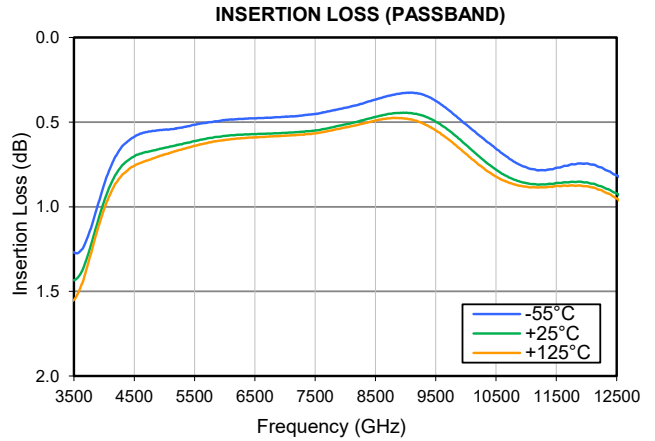
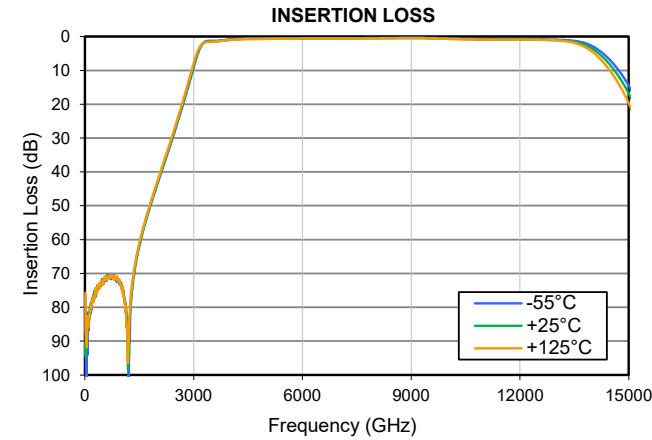
High Pass Filter

HFHKI-3100+

Mini-Circuits

50Ω 3500 to 12500 MHz

TYPICAL PERFORMANCE GRAPHS





LTCC SURFACE MOUNT

High Pass Filter

HFHKI-3100+

Mini-Circuits

50Ω 3500 to 12500 MHz

FUNCTIONAL DIAGRAM

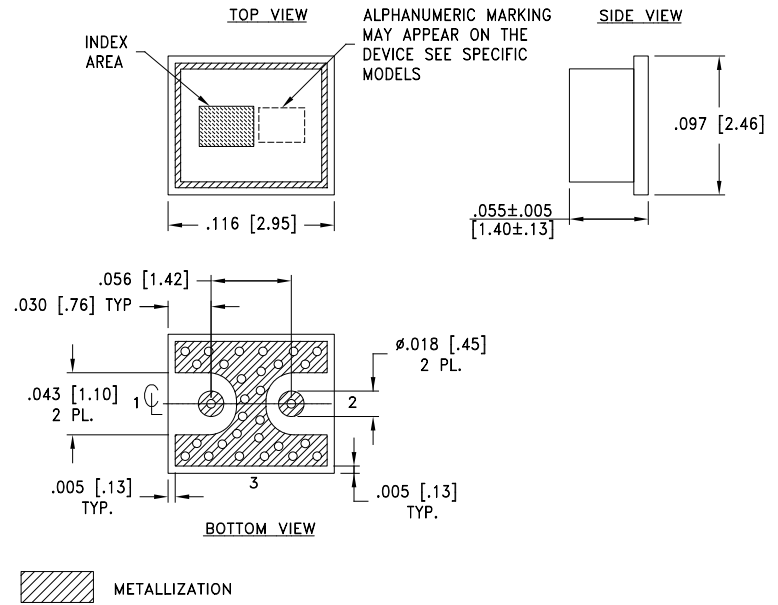


Figure 1. HFHKI-3100+ Functional Diagram

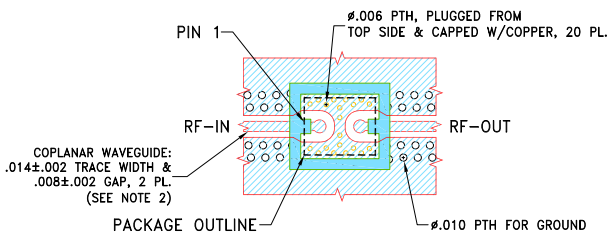
PAD DESCRIPTION

Function	Pad Number	Description
RF1 ²	1	Connects to RF Input Port
RF2 ²	2	Connects to RF Output Port
GROUND	3	Connects to Ground on PCB, (See drawing PL-837)

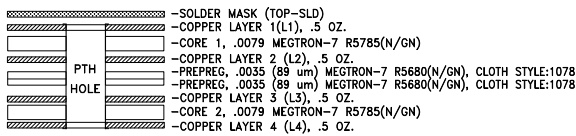
CASE STYLE DRAWING



SUGGESTED PCB LAYOUT (PL-837)



STACK-UP DIAGRAM



- TOTAL FINISHED THICKNESS 0.026±.004.
- PTH PRESENT FROM COPPER LAYER 1 TO COPPER LAYER 4.
- INDICATED ON TOP VIEW PTH'S ARE PLUGGED WITH EPOXY AND CAPPED WITH COPPER FROM TOP SIDE.
- L2, L3, AND L4 ARE CONTINUOUS GROUND PLANES.

NOTES:

- PCB IS MULTILAYER PCB, SEE STACK-UP DIAGRAM.
- TRACE WIDTH & GAP ARE SHOWN FOR MEGTRON-7 R5785(N/GN) WITH DIELECTRIC THICKNESS .0079". COPPER: .5 OZ. EACH LAYER. FOR OTHER MATERIALS TRACE WIDTH & GAP MAY NEED TO BE MODIFIED.
- LAYERS L2, L3 & L4 OF PCB ARE CONTINUOUS GROUND PLANES.

- DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER).
- DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK.

Figure 2. Suggested PCB Layout

PRODUCT MARKING*: K6

*Marking may contain other features or characters for internal lot control.



LTCC SURFACE MOUNT

High Pass Filter

HFHKI-3100+

Mini-Circuits

50Ω 3500 to 12500 MHz

ADDITIONAL DETAILED INFORMATION IS AVAILABLE ON OUR DASHBOARD

[CLICK HERE](#)

Performance Data & Graphs	Data Graphs S-Parameter (S2P Files) Data Set (.zip file) De-embedded to device pads
Case Style	NM3723-1 Lead Finish: Gold Plate over Nickel Plate
RoHS/REACH Status	Compliant
Tape and Reel	F66-3
Suggested Layout for PCB Design	PL-837
Evaluation Board	TB-HFHKI-3100C+ Gerber File
Environmental Rating	ENV06T10

NOTES

- A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
- B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuits' applicable established test performance criteria and measurement instructions.
- C. The parts covered by this specification document are subject to Mini-Circuits' standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the standard terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/terms/viewterm.html



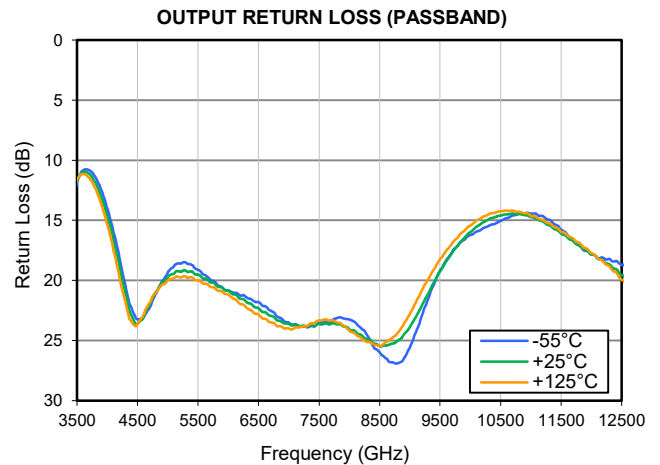
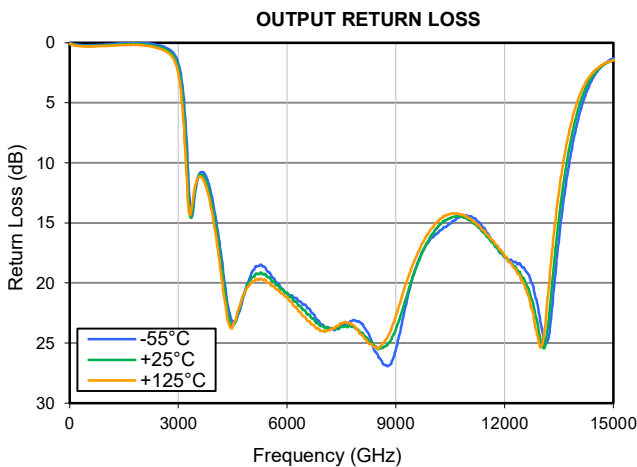
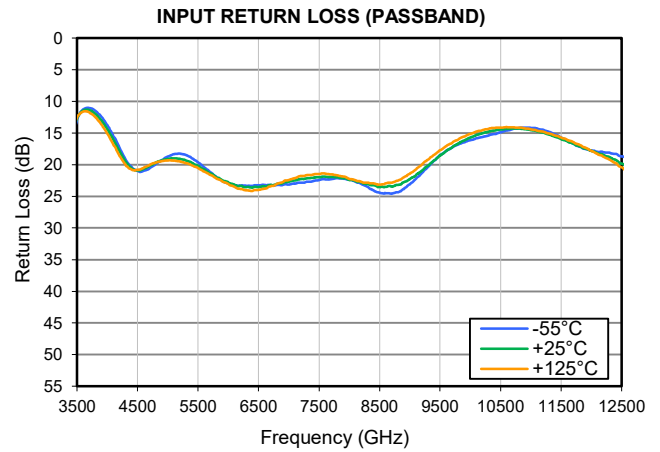
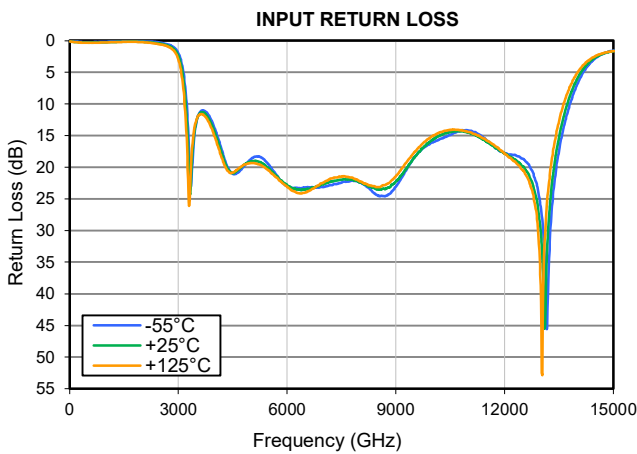
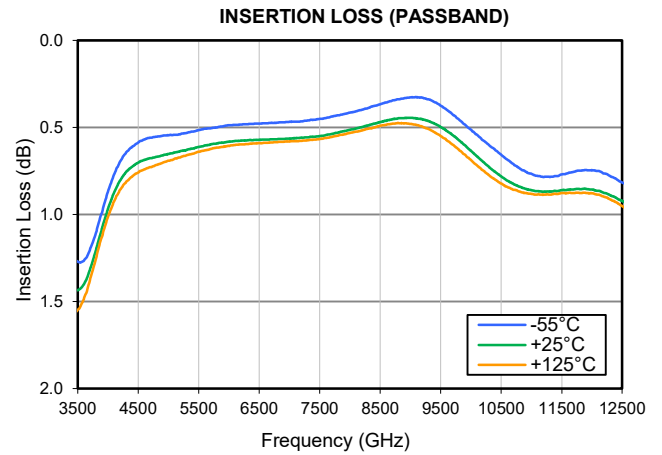
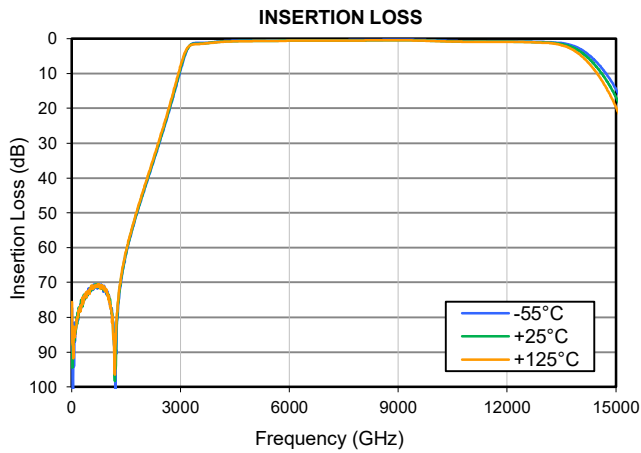
LTCC High pass Filter

HFHKI-3100+

Typical Performance Data

FREQUENCY (MHz)	INSERTION LOSS (dB)			RETURN LOSS (dB)			OUTPUT RETURN LOSS (dB)		
	@-55°C	@+25°C	@+125°C	@-55°C	@+25°C	@+125°C	@-55°C	@+25°C	@+125°C
10.00	77.42	90.48	75.66	0.08	0.13	0.15	0.09	0.15	0.05
50.00	92.68	88.61	91.82	0.10	0.15	0.17	0.11	0.17	0.06
100.00	83.65	84.52	83.56	0.13	0.19	0.22	0.15	0.22	0.11
500.00	72.25	72.62	73.33	0.23	0.30	0.34	0.27	0.34	0.19
1000.00	73.44	73.86	74.03	0.18	0.26	0.29	0.21	0.29	0.14
1500.00	61.97	61.56	61.05	0.07	0.16	0.21	0.12	0.21	0.03
2000.00	43.55	43.20	42.75	0.06	0.18	0.25	0.12	0.25	0.01
2500.00	27.25	26.82	26.24	0.32	0.48	0.58	0.40	0.58	0.25
3000.00	9.11	8.56	7.84	1.94	2.38	2.93	2.08	2.93	1.69
3500.00	1.27	1.43	1.55	12.89	12.70	12.53	11.72	12.53	11.83
4000.00	0.87	0.97	1.02	13.77	14.41	15.09	14.64	15.09	14.02
4500.00	0.59	0.70	0.76	20.99	20.90	20.83	23.60	20.83	23.24
5000.00	0.55	0.65	0.69	18.73	19.09	19.34	19.72	19.34	19.35
5500.00	0.52	0.61	0.64	19.61	20.16	20.52	19.52	20.52	19.11
6000.00	0.49	0.58	0.61	22.94	22.79	23.05	20.84	23.05	20.83
6500.00	0.48	0.57	0.59	23.21	23.54	23.99	22.31	23.99	21.79
7000.00	0.47	0.56	0.58	23.13	22.68	22.44	23.62	22.44	23.52
7500.00	0.45	0.55	0.57	22.44	21.98	21.48	23.53	23.62	23.34
8000.00	0.42	0.51	0.53	22.39	22.42	22.24	24.06	22.24	23.26
8500.00	0.37	0.47	0.49	24.44	23.51	23.15	25.47	23.15	25.99
9000.00	0.33	0.45	0.48	23.08	22.28	21.42	23.88	21.42	25.56
9500.00	0.37	0.50	0.55	18.49	18.52	17.70	19.24	17.70	19.20
10000.00	0.51	0.63	0.69	16.11	15.70	15.11	15.98	15.11	16.22
10500.00	0.66	0.78	0.82	14.88	14.44	14.11	14.60	14.11	15.06
11000.00	0.77	0.86	0.88	14.20	14.53	14.41	14.73	14.41	14.46
11500.00	0.77	0.86	0.88	15.72	15.97	15.75	16.10	15.75	15.78
12000.00	0.74	0.86	0.88	17.76	17.82	17.84	17.84	17.84	17.86
12500.00	0.81	0.92	0.95	18.66	19.83	20.43	19.58	20.43	18.70

Typical Performance Data

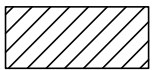
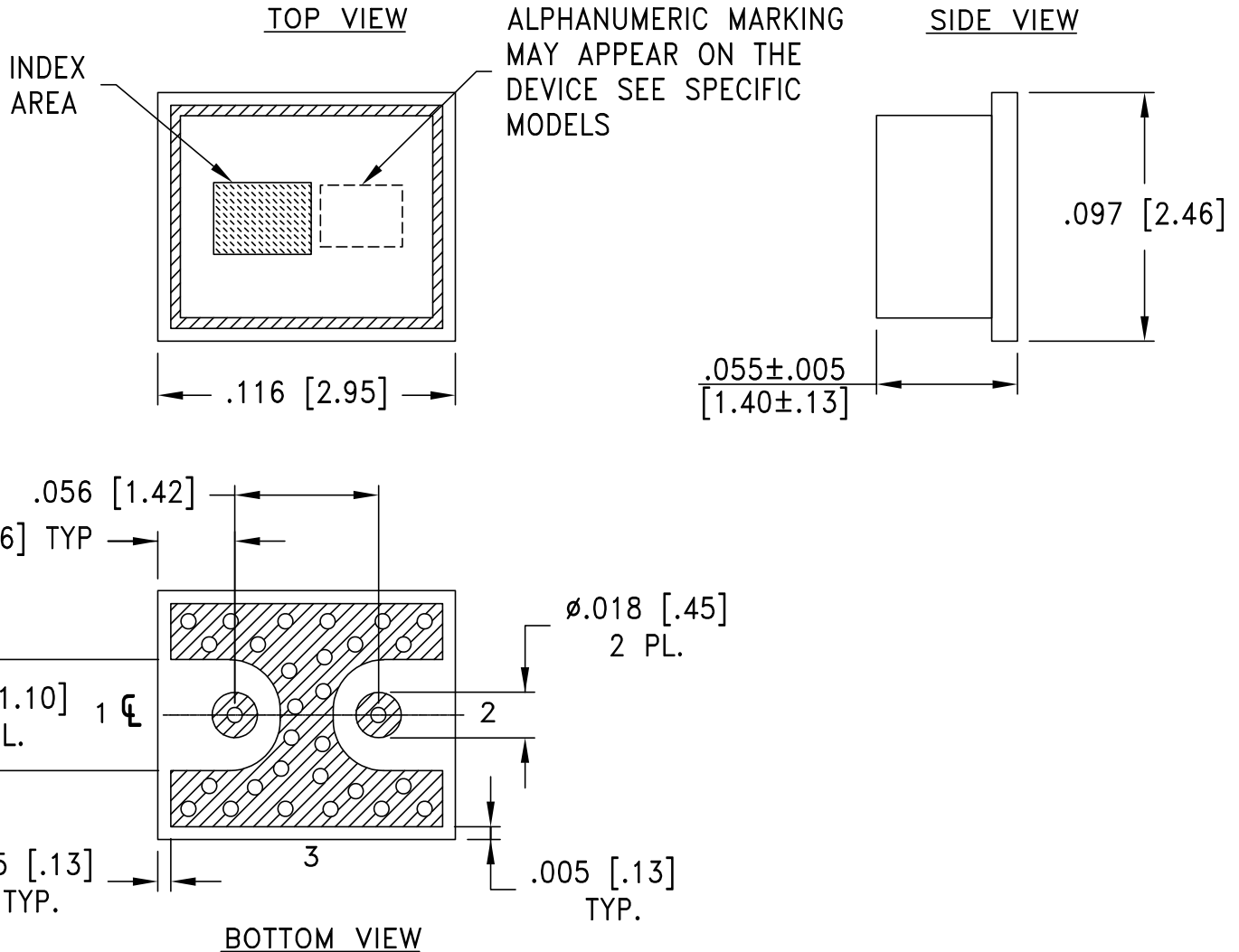


Case Style

NM

Outline Dimensions

NM3723-1



METALLIZATION

Weight: .021 grams

Dimensions are in inches [mm]. Tolerances: 2 Pl.±.01; 3 Pl. ±.005 inches

Notes:

1. Case material: LTCC on printed circuit board base.
2. Termination Finish: **as shown below or indicated on Data Sheet.**
For RoHS Case Styles: Gold Plate over Nickel plate. All models, (+) suffix.

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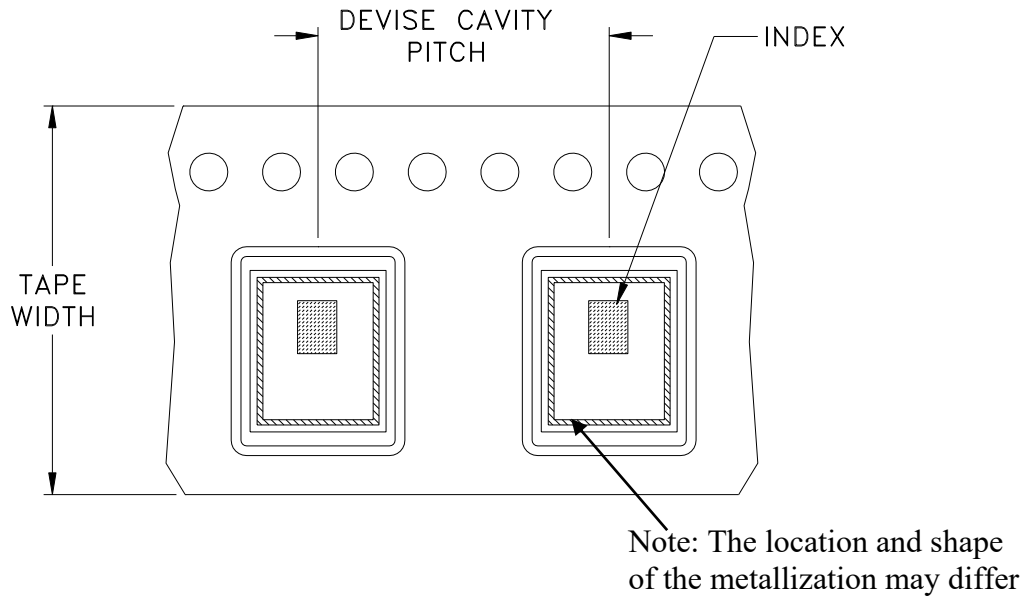
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Tape & Reel Packaging TR-F66-3



Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel see note	
8	4	7	Small quantity standard	20
				50
				100
				200
				500
		7	Standard	1000

Note: Please consult individual model data sheet to determine device per reel availability.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

Go to: www.minicircuits.com/pages/pdfs/tape.pdf

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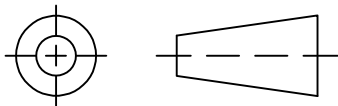
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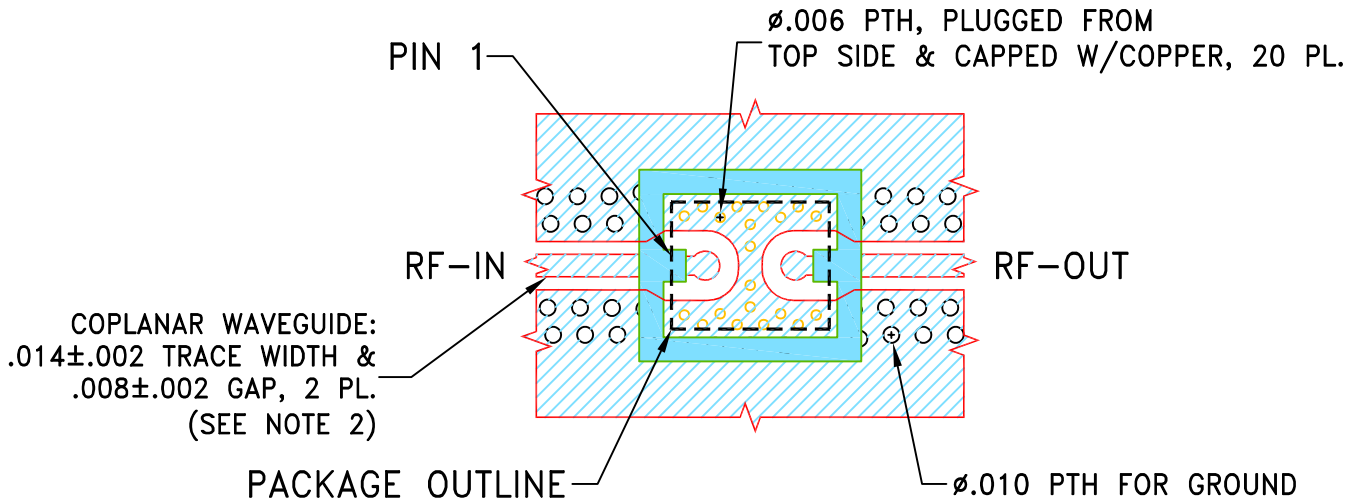
THIRD ANGLE PROJECTION



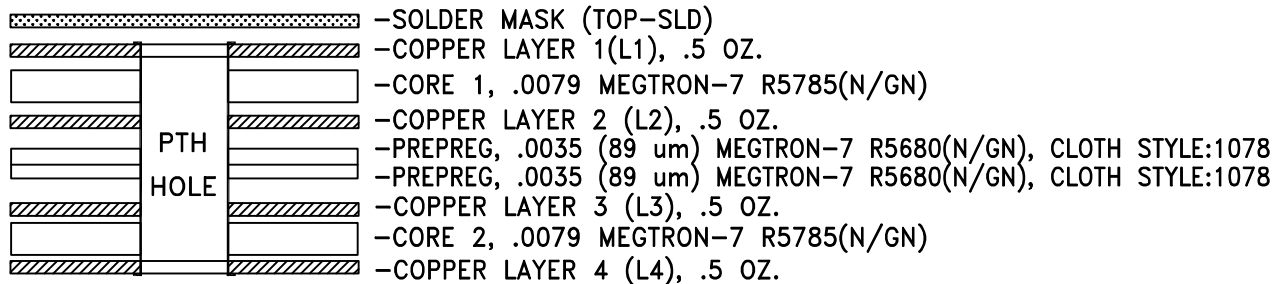
REVISIONS

REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
OR	ECO-026948	NEW RELEASE	09/15/25	ITG	IL

SUGGESTED MOUNTING CONFIGURATION
FOR NM3723-1 CASE STYLE



STACK-UP DIAGRAM



- TOTAL FINISHED THICKNESS 0.026±.004.
- PTH PRESENT FROM COPPER LAYER 1 TO COPPER LAYER 4.
- INDICATED ON TOP VIEW PTH'S ARE PLUGGED WITH EPOXY AND CAPPED WITH COPPER FROM TOP SIDE.
- L2, L3, AND L4 ARE CONTINUOUS GROUND PLANES.

NOTES:

- PCB IS MULTILAYER PCB, SEE STACK-UP DIAGRAM.
- TRACE WIDTH & GAP ARE SHOWN FOR MEGTRON-7 R5785(N/GN) WITH DIELECTRIC THICKNESS .0079", COPPER: 1 OZ. EACH LAYER. FOR OTHER MATERIALS TRACE WIDTH & GAP MAY NEED TO BE MODIFIED.
- LAYERS L2, L3 & L4 OF PCB ARE CONTINUOUS GROUND PLANES.



DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER).



DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK.

UNLESS OTHERWISE SPECIFIED	INITIALS	DATE
DIMENSIONS ARE IN INCHES	ITG	09/15/25
TOLERANCES ON:	GF	09/15/25
2 PL DECIMALS ±	APPROVED	09/15/25
3 PL DECIMALS ± .005	IL	09/15/25
ANGLES ±		
FRACTIONS ±		

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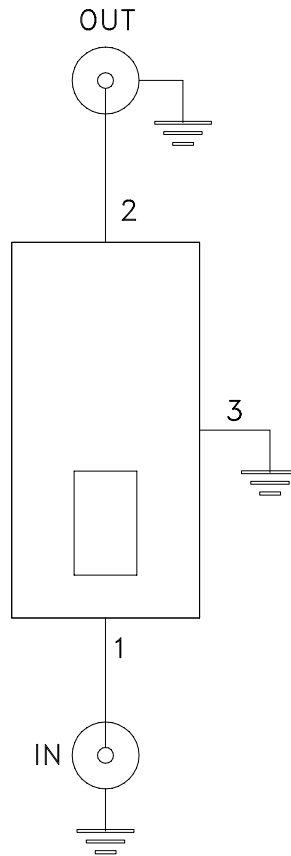
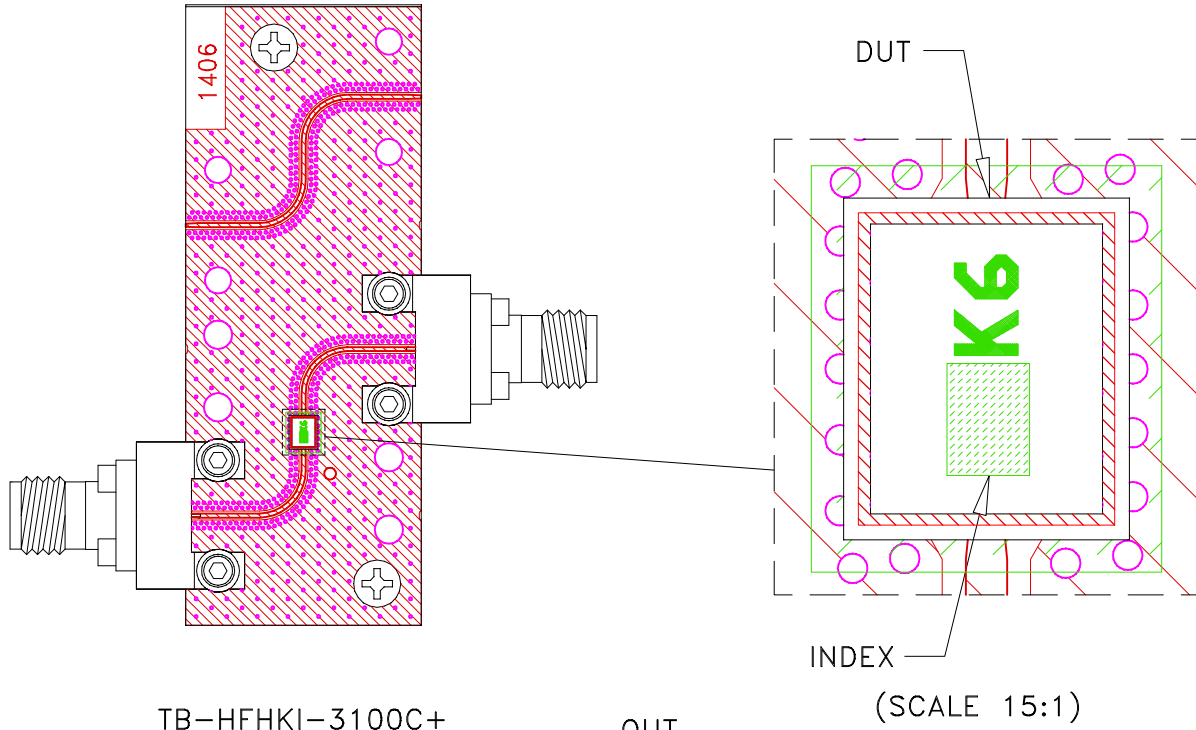
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Brooklyn NY 11235

PL, NM3723-1, TB-HFHKI-2200C+

SIZE	CODE IDENT	DRAWING NO:	REV:
A	15542	98-PL-837	OR
FILE:	98PL837	SCALE:	8:1
SHEET:	1	OF	1

Evaluation Board and Circuit



Schematic Diagram

1. 50 Ohm 2.4 mm Female end Launch connectors.
2. PCB Material: MEGTRON-7 R5785 (N or GN) or equivalent, Dielectric Constant= 3.37 Thickness=.0079 inch.

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All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-55° to 125° C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 125° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Solder Reflow Heat	Sn-Pb Eutectic Process 225°C peak Pb-Free Process 245° - 250°C peak	J-STD-020C, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, Test B,B1, 95% Coverage
Thermal Shock	-55° to +125°C, 15 min dwell,250 cycles	MIL-STD-202, Method 107
Bend Test	1mm, deflection for 5 seconds Span of bending: 2.75"	--
High Temp Storage	125°C to 1000 Hrs	---