

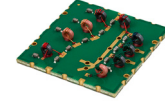
NON-CATALOG

Surface Mount

Diplexer

DPB6588-75+

75Ω DC to 1220 MHz
(DC-65, 88-1220 MHz)



CASE STYLE: PA2002

The Big Deal

- Low insertion loss, 1.2dB Typ.
- High rejection, > 45dB
- Very good return loss, 22dB Typ.
- 75Ω Impedance
- Used in DOCSIS 3.1 standard

Product Overview

DPB6588-75+ is a high performance diplexer with the lowpass port at DC-65 MHz and highpass port at 88-1220 MHz. Excellent return loss combined with high out of channel rejection makes it a ideal component in cable TV and multiband radio systems.

Key Features

Feature	Advantages
Low passband insertion loss	Passband insertion loss 1.2dB typical ensures low signal loss through both the channels.
Excellent Stopband rejection	Co-channel rejection of 45dB ensures unwanted spurious are eliminated.
Excellent return loss at DC-65 and 88-1220 MHz	This makes signal transmission with very less reflection and well-matched with the adjacent component used in the system.

Notes

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B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
C. The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the Standard Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/MCLStore/terms.jsp

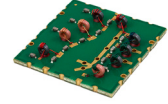


Surface Mount Diplexer

NON-CATALOG

DPB6588-75+

75Ω DC to 1220 MHz (DC-65, 88-1220 MHz)



CASE STYLE: PA2002

Maximum Ratings

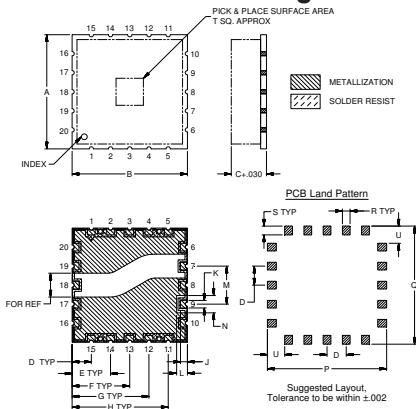
Operating Temperature	-40° to 85°C
Storage Temperature	-55°C to 100°C
RF Power Input	27dBm Max.

Permanent damage may occur if any of these limits are exceeded. These ratings are not intended for continuous normal operation

Pin Connections

HIGH PASS PORT	7
LOW PASS PORT	9
COMMON PORT	18
GROUND	1-6,8,10-17,19,20

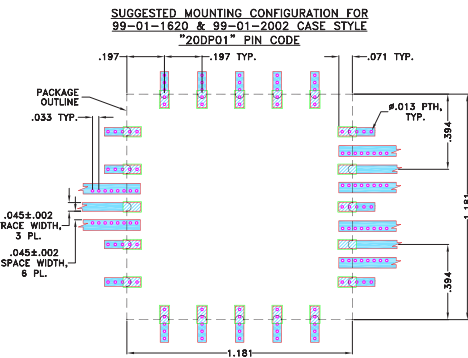
Outline Drawing



Outline Dimensions (inch/mm)

A	B	C	D	E	F	G	H	J	K
1.181	1.181	.300	.197	.394	.591	.787	.984	.071	.079
30.00	30.00	7.62	5.00	10.00	15.00	20.00	25.00	1.80	2.00
L	M	N	P	Q	R	S	T	U	Wt.
.111	.394	.179	1.221	1.221	.079	.091	.280	.178	grams
2.82	10.00	4.54	31.01	31.01	2.01	2.31	7.11	4.52	3.8

Demo Board MCL P/N: TB-786+ Suggested PCB Layout (PL-435)



- TRACE WIDTH IS SHOWN FOR OAK-602 WITH DIELECTRIC THICKNESS .031"±.002". COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.
 - BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.
- DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)
 DENOTES COPPER LAND PATTERN FREE OF SOLDERMASK

Features

- Low insertion loss
- 75Ω Impedance
- Excellent return loss
- High rejection

Applications

- Cable TV systems (DOCSIS 3.1 standard)
- Multiband radio systems

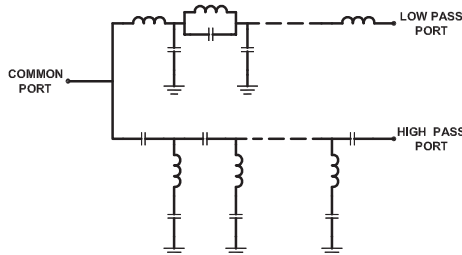
Electrical Specifications at 25°C

Parameter	Port	Frequency (MHz)	Min.	Typ.	Max.	Unit	
Pass Band	Insertion Loss	Low Pass	DC-65	-	0.9	1.5	dB
		High Pass	88-1220	-	1.2	1.6	
	Return Loss	Low Pass	DC-65	18	22	-	dB
		High Pass	88-1220	17	22	-	
Common		DC-65	18	22	-		
Stop Band Isolation	Low Pass	88-900	45	50	-	dB	
	High Pass	900-1220	43	45	-		
	High Pass	DC-65	45	50	-		

Typical Performance Data at 25°C

FREQUENCY (MHz)	INSERTION LOSS (dB)			RETURN LOSS (dB)	
	Low Pass Port	High Pass Port	Common Port	Low Pass Port	High Pass Port
1.0	0.04	85.70	51.54	51.03	0.03
10.0	0.08	75.18	34.87	34.81	0.01
50.0	0.30	79.72	28.11	27.57	0.25
60.0	0.50	71.47	35.00	38.45	0.45
65.0	0.71	72.36	29.02	27.15	0.59
70.0	1.13	46.18	28.91	37.15	0.77
74.0	2.87	32.11	9.96	9.24	1.03
75.0	4.28	26.87	7.10	6.07	1.16
76.0	6.45	20.73	5.18	3.87	1.36
78.0	13.24	10.96	4.02	1.70	2.37
79.5	20.35	6.32	5.25	1.12	4.27
80.0	23.05	5.24	6.05	1.01	5.22
81.5	31.33	3.12	9.48	0.81	8.99
85.0	45.19	1.54	21.56	0.61	22.52
88.0	56.57	1.20	31.45	0.53	40.52
100.0	73.85	0.78	32.40	0.42	26.41
250.0	63.55	0.43	31.93	0.32	24.72
300.0	62.79	0.43	53.49	0.30	26.76
500.0	58.93	0.46	22.84	0.26	21.95
900.0	54.17	0.56	19.64	0.37	21.78
1000.0	52.95	0.56	20.72	0.41	27.02
1220.0	49.63	0.61	21.03	0.53	25.18

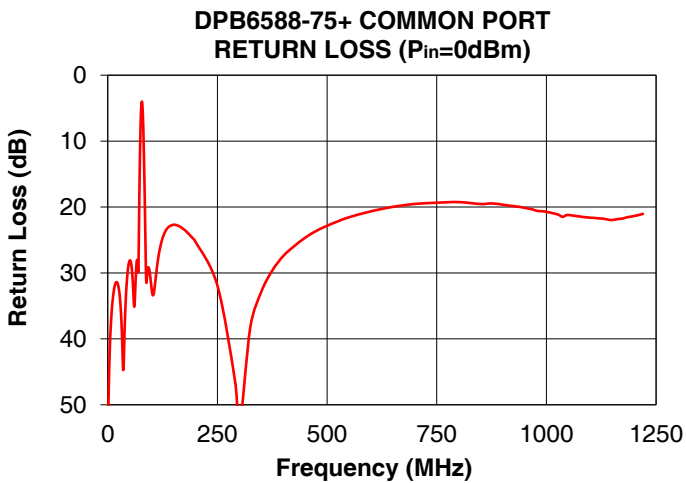
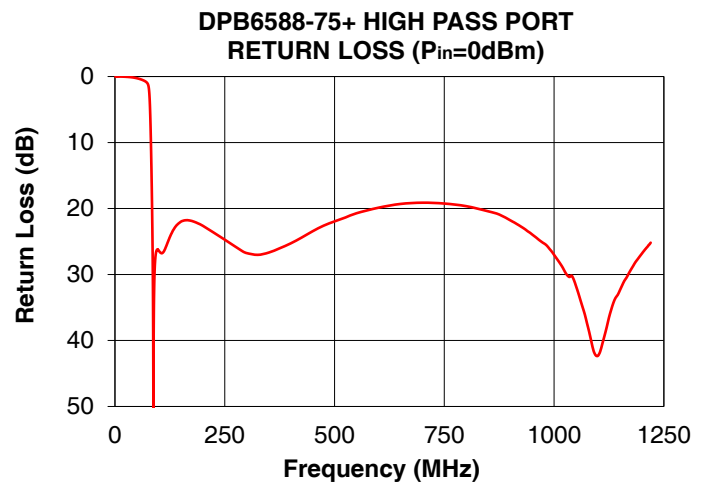
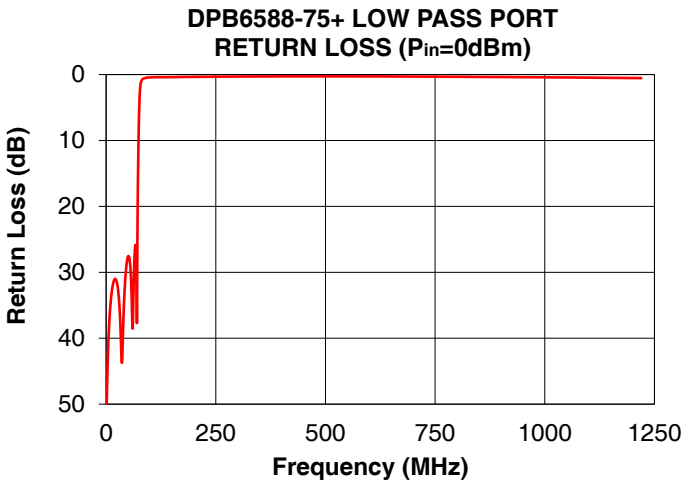
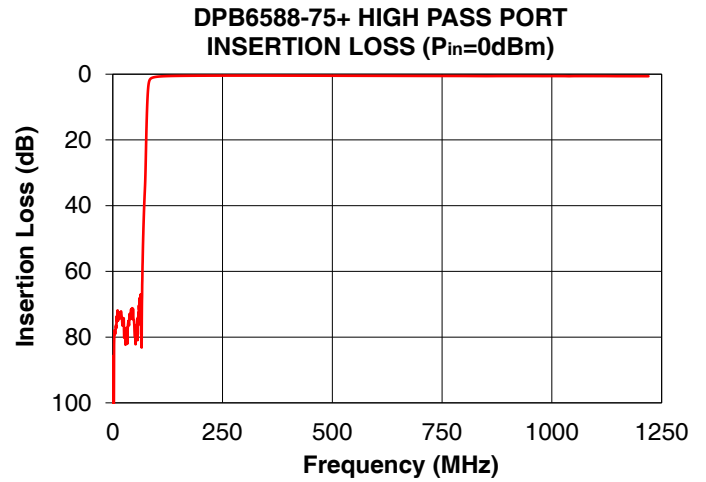
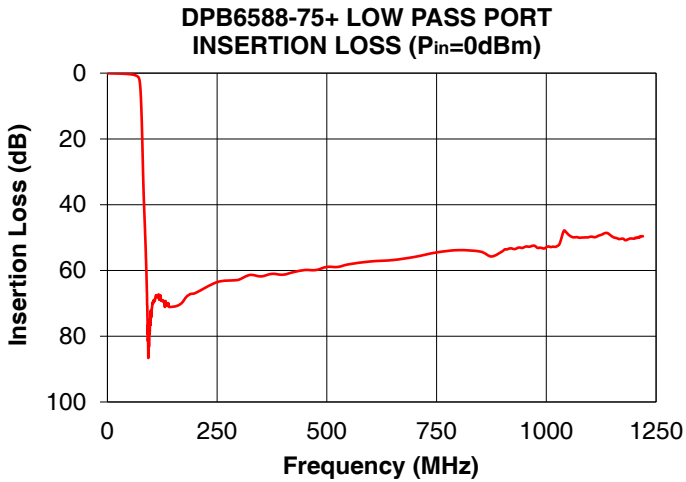
Functional Schematic



Notes

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Notes

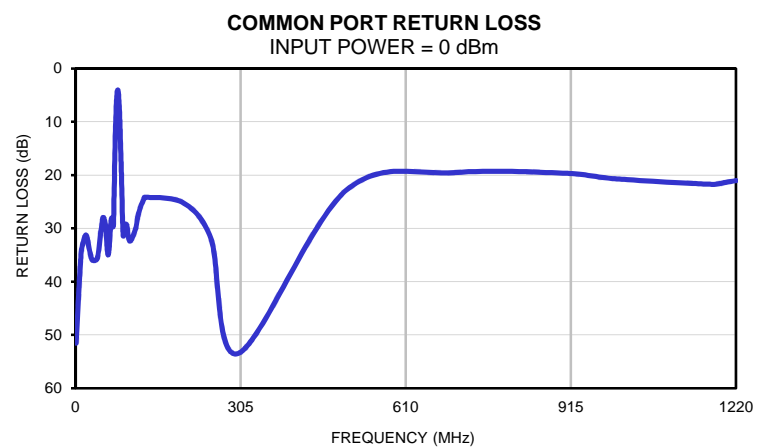
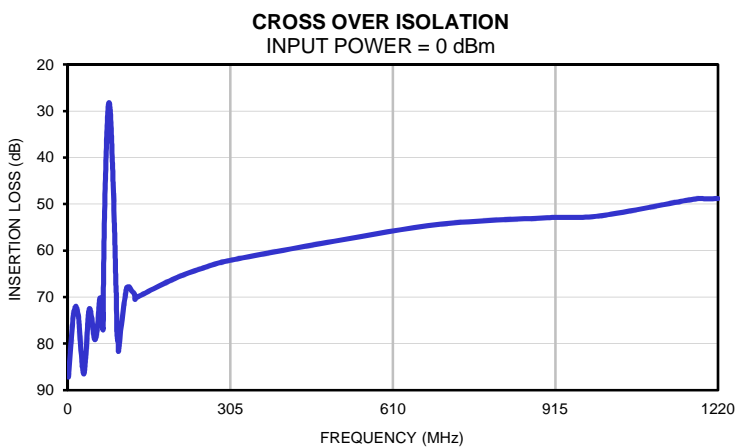
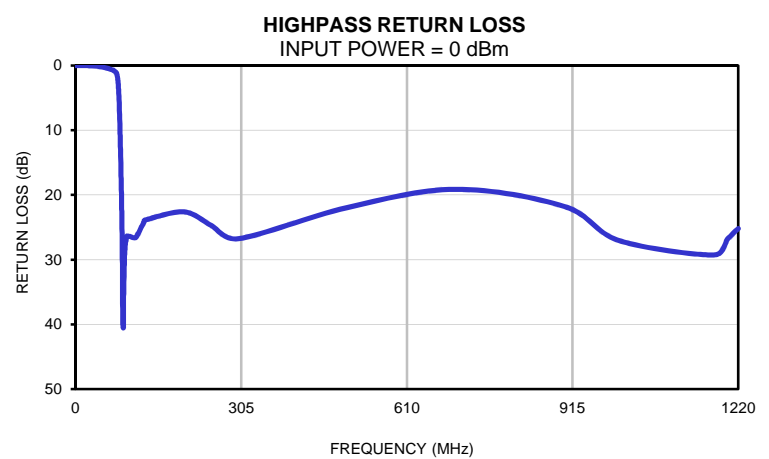
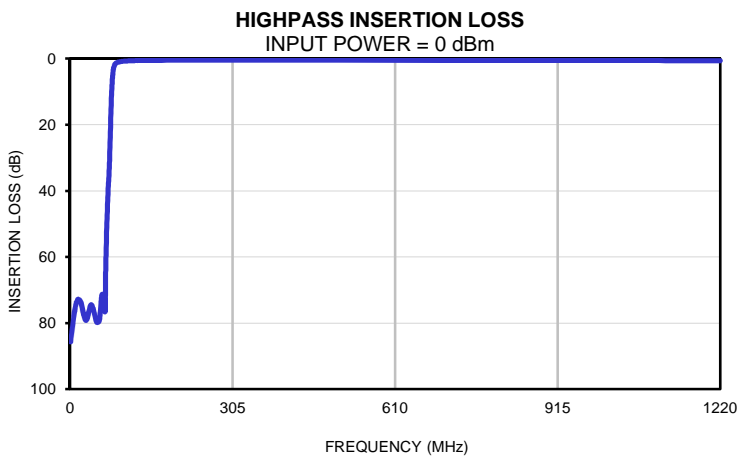
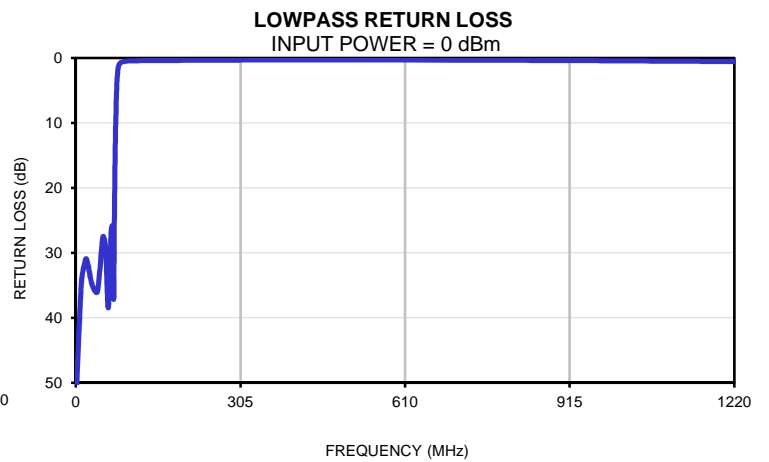
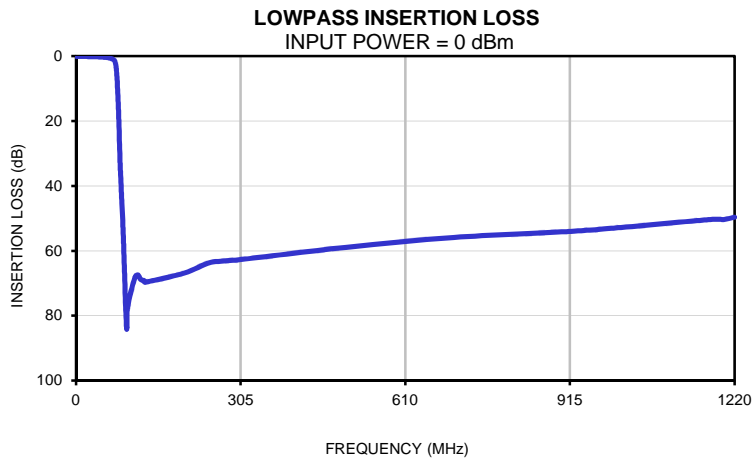
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Typical Performance Data

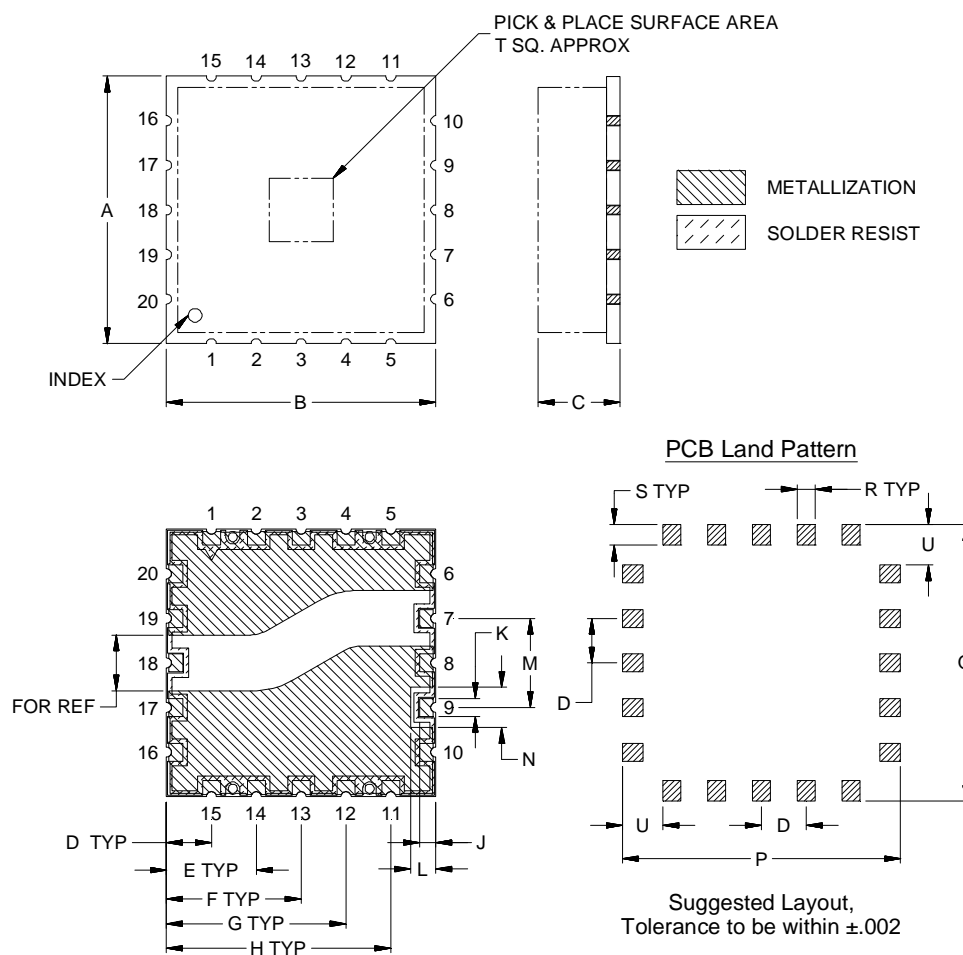
FREQUENCY (MHz)	INSERTION LOSS (dB)		Cross over isolation (dB) (between LFP and HPF)	RETURN LOSS (dB)		
	Lowpass port	Highpass port		Common port	Lowpass port	Highpass port
1	0.04	85.70	87.18	51.54	51.03	0.03
10	0.08	75.18	74.13	34.87	34.81	0.01
15	0.10	72.84	71.97	32.20	32.01	0.00
20	0.11	73.54	74.31	31.39	31.00	0.02
30	0.14	79.20	86.53	35.87	34.91	0.06
40	0.20	74.48	72.62	35.63	35.94	0.13
50	0.30	79.72	79.04	28.11	27.57	0.25
55	0.39	79.31	77.13	29.48	29.07	0.34
60	0.50	71.47	70.26	35.00	38.45	0.45
65	0.71	72.36	73.34	29.02	27.15	0.59
65.5	0.74	76.62	76.42	28.56	26.56	0.60
66	0.77	76.51	76.99	28.23	26.17	0.62
67	0.83	64.08	65.02	28.06	25.85	0.65
68	0.91	57.28	57.21	28.60	26.56	0.68
69	1.01	51.04	51.63	29.74	29.27	0.73
70	1.13	46.18	46.73	28.91	37.15	0.77
71	1.30	42.02	42.59	23.82	27.76	0.81
72	1.57	38.49	38.77	18.35	19.19	0.87
73	2.04	35.50	35.38	13.72	13.50	0.95
74	2.87	32.11	32.47	9.96	9.24	1.03
75	4.28	26.87	30.24	7.10	6.07	1.16
76	6.45	20.73	28.81	5.18	3.87	1.36
77	9.45	15.31	28.19	4.19	2.49	1.72
78	13.24	10.96	28.34	4.02	1.70	2.37
79	17.80	7.63	29.21	4.65	1.26	3.49
79.5	20.35	6.32	29.92	5.25	1.12	4.27
80	23.05	5.24	30.82	6.05	1.01	5.22
80.5	25.84	4.36	31.87	7.03	0.93	6.33
81	28.63	3.67	33.07	8.18	0.86	7.59
81.5	31.33	3.12	34.43	9.48	0.81	8.99
82	33.85	2.70	35.87	10.90	0.77	10.53
84	41.65	1.77	42.33	17.63	0.65	17.88
85	45.19	1.54	45.80	21.56	0.61	22.52
86	48.79	1.39	49.49	25.80	0.58	28.67
87	52.48	1.28	53.26	29.67	0.55	39.69
88	56.57	1.20	57.19	31.45	0.53	40.52
88.5	59.01	1.16	59.37	31.39	0.52	35.66
90	66.44	1.08	66.79	30.12	0.50	29.95
91	71.60	1.03	69.84	29.46	0.49	28.35
92	77.40	0.99	77.65	29.18	0.48	27.41
94	84.14	0.92	79.47	29.33	0.46	26.51
94.5	80.08	0.91	78.26	29.46	0.45	26.41
95	78.40	0.90	81.66	29.65	0.45	26.33
100	73.85	0.78	76.33	32.40	0.42	26.41
110	67.89	0.66	68.07	30.35	0.40	26.61
115	67.46	0.62	67.79	27.74	0.40	25.98
120	68.84	0.59	68.71	25.94	0.39	25.14
125	69.10	0.57	69.33	24.71	0.39	24.32
126	69.16	0.57	70.50	24.51	0.39	24.16
128	69.68	0.56	70.15	24.16	0.39	23.87
200	66.90	0.45	66.03	25.27	0.34	22.62
250	63.55	0.43	63.90	31.93	0.32	24.72
300	62.79	0.43	62.22	53.49	0.30	26.76
500	58.93	0.46	57.99	22.84	0.26	21.95
700	55.88	0.53	54.33	19.53	0.30	19.12
900	54.17	0.56	52.94	19.64	0.37	21.78
1000	52.95	0.56	52.51	20.72	0.41	27.02
1175	50.34	0.60	48.93	21.73	0.51	29.27
1200	50.34	0.61	48.90	21.38	0.52	26.83
1220	49.63	0.61	48.80	21.03	0.53	25.18

Typical Performance Curves



Outline Dimensions

PA2002



CASE#	A	B	C		D	E	F	G	H	J	K	L	M
			Max	Min									
PA2002	1.181 (30.00)	1.181 (30.00)	.280 (7.11)	.205 (5.21)	.197 (5.00)	.394 (10.00)	.591 (15.00)	.787 (20.00)	.984 (25.00)	.071 (1.80)	.079 (2.01)	.111 (2.82)	.394 (10.00)

CASE#	N	P	Q	R	S	T	U	WT.GRAMS
PA2002	.179 (4.54)	1.221 (31.01)	1.221 (31.01)	.079 (2.01)	.091 (2.31)	.280 (7.11)	.178 (4.52)	3.8

Dimensions are in inches (mm). Tolerances: 2Pl. ± .03; 3Pl. ± .015

Notes:

1. Base: Printed wiring laminate.
2. Termination finish:
 - For RoHS Case Styles: 3-5 μ inch Gold over 120-240 μ inch Nickel plate.
 - For RoHS-5 Case Styles: Tin-Lead plate.



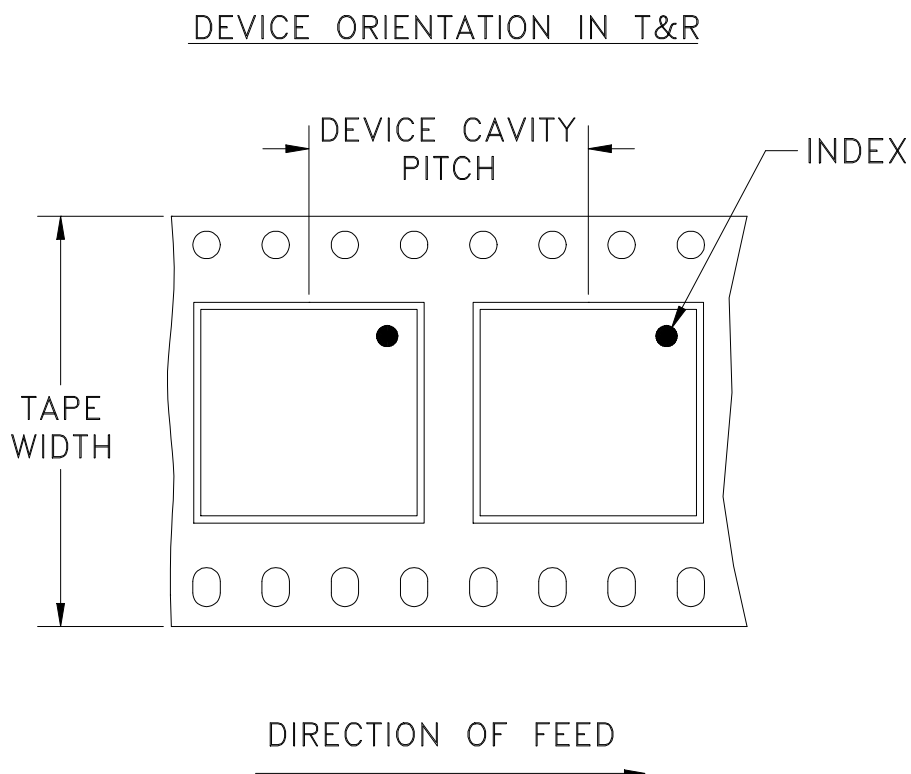
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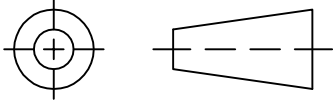
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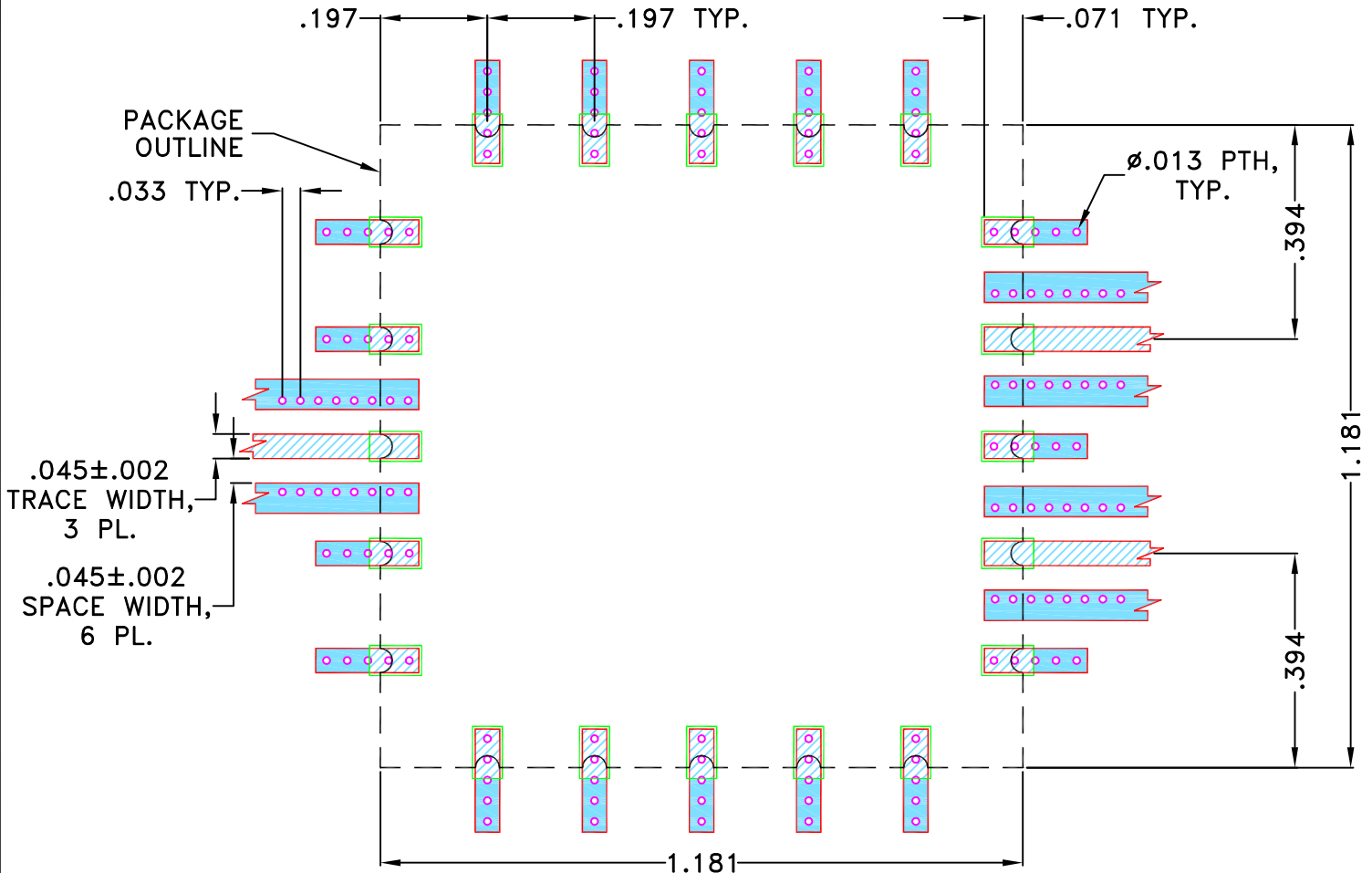
THIRD ANGLE PROJECTION



REVISIONS

REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
OR	M147697	NEW RELEASE	SEP 14	DDR	MD

SUGGESTED MOUNTING CONFIGURATION FOR NU1620, NV1998, NZ2001, PA2002 CASE STYLE "20DP01" PIN CODE



NOTES:

- TRACE WIDTH IS SHOWN FOR OAK-602 WITH DIELECTRIC THICKNESS .031"±.002". COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.
- BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.



DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)



DENOTES COPPER LAND PATTERN FREE OF SOLDERMASK

UNLESS OTHERWISE SPECIFIED	INITIALS	DATE
DIMENSIONS ARE IN INCHES	DRAWN DDR	09 SEP 14
TOLERANCES ON:	CHECKED MD	09 SEP 14
2 PL DECIMALS ±	APPROVED KR	09 SEP 14
3 PL DECIMALS ± .005"		
ANGLES ±		
FRACTIONS ±		



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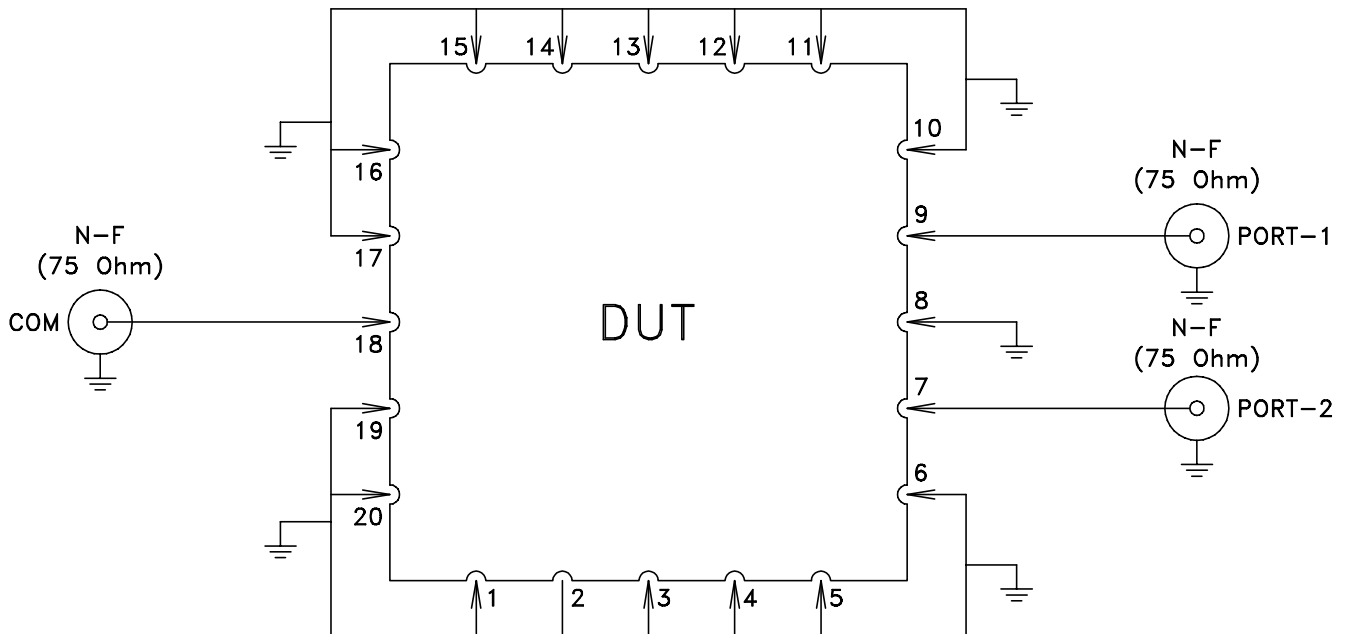
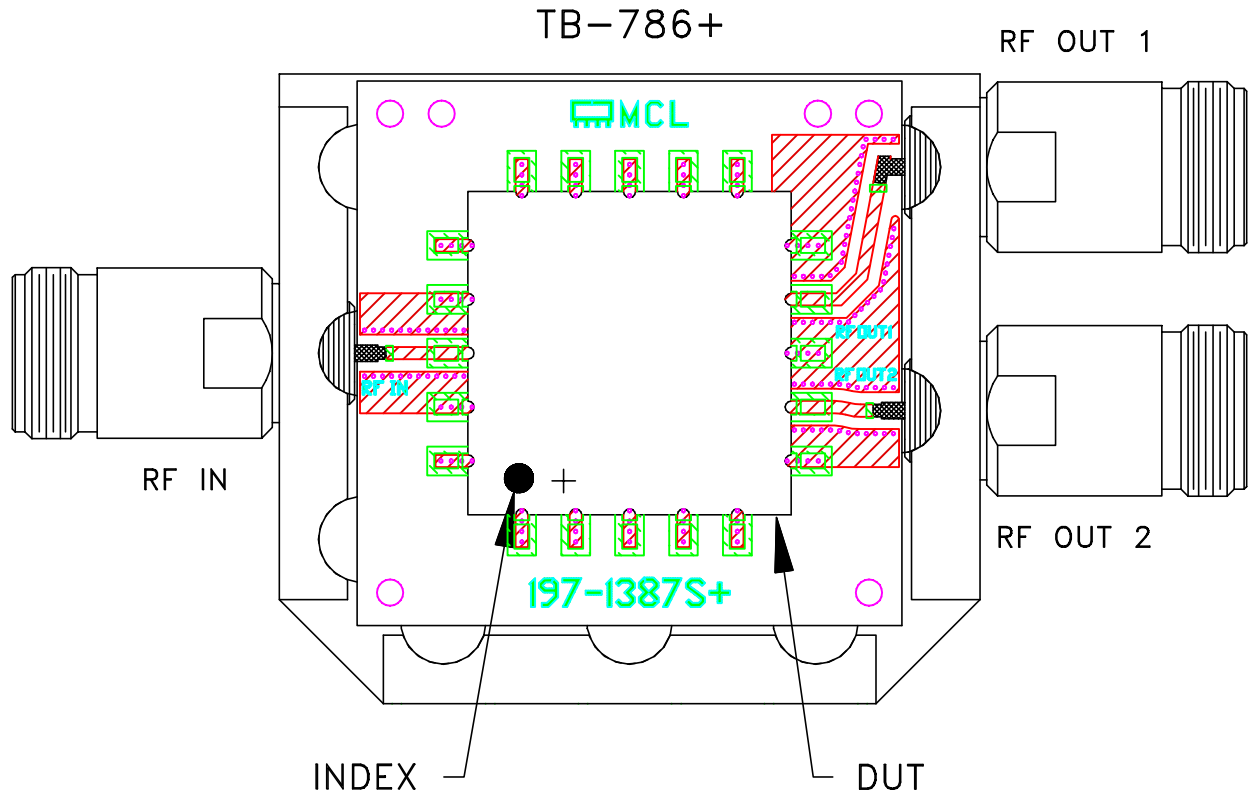
13 Neptune Avenue
Brooklyn NY 11235

PL, 20DP01, NU1620, NV1998, NZ2001, PA2002, TB-786+, 75 OHM

SIZE A	CODE IDENT 15542	DRAWING NO: 98-PL-435	REV: OR
FILE: 98PL435	SCALE: 3:1	SHEET: 1 OF 1	

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Evaluation Board and Circuit



Schematic Diagram

Notes:

1. 75 Ohm N Female connectors.
2. PCB Material: OAK-602 OR Equivalent
Dielectric Constant=2.50±.04, Thickness=.031 inch.

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All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-55° to 85°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
HAST	130°C, 85% RH, 96 hours	JESD22-A110
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Thermal Shock	-55° to 100°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Solder Reflow Heat	Sn-Pb Eutectic Process: 225°C peak Pb-Free Process, 245°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, Para 4.2.5, Test S, 95% Coverage
Vibration (High Frequency)	20g peak, 20-2000 Hz, 4 times in each of three axes (total 12)	MIL-STD-883, Method 2007.3, Condition A
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A
Marking Resistance to Solvents	Isopropyl alcohol + mineral spirits at 25°C; terpene defluxer at 25°C; distilled water + proylene glycol monomethyl ether + monoethanolamine at 63°C to 70°C	MIL-STD-202, Method 215