

# Surface Mount Digital Step Attenuator

## DAT-3175A Series

75Ω 0 to 31 dB, 1.0 dB Step 1MHz to 2.5 GHz

### The Big Deal

- Wideband, operates up to 2.5 GHz
- Glitchless attenuation transitions
- High IP3, 52 dBm



CASE STYLE: DG983-2

### Product Overview

The DAT-3175A+ series of 75Ω digital step attenuators provides adjustable attenuation from 0 to 31 dB in 1.0 dB steps. The control is a 5-bit serial/parallel interface, and the attenuators operate with either single positive or dual (positive and negative) supply voltage. DAT-3175A+ series models are produced by a unique CMOS process on silicon, offering the performance of GaAs with the advantages of conventional CMOS devices.

### Key Features

Feature	Advantages
Wideband operation, specified from 1MHz to 2.5 GHz	Can be used in multiple applications such as various versions of DOCSIS, satellite and defense, reducing part count.
Serial or parallel interface	Models available with serial or parallel interface mode to suit customer demand.
Good VSWR, 1.3:1 typ.	Eases interfacing with adjacent components and results in low amplitude ripple.
Single positive supply models: (Model suffixes: -SP+ and -PP+) +2.3 to +3.6V+	Use of single positive supply simplifies power supply design. An internal negative voltage generator supplies the desired negative voltage. Single positive supply results in excellent spurious performance, -140 dBm typical.
Dual supply models: (Model suffixes: -SN+ and -PN+) +2.7 to +3.6V (Positive) and -3.6 to -3.2V (Negative)	Dual supply provides spurious-free operation. It also allows fast switching up to 1 MHz (vs. 25 kHz for single supply).
Useable over a wide range of supply voltages, +2.3/2.7 to 5.2V	Wide range of positive operating voltages allows the DAT-3175A+ Series of models to be used in a wide range of applications. See Application Note AN-70-032 for operation above +3.6V
Footprint compatible to DAT-3175-XX+ Series (XX=SN/SP/PN/PP)	Can fit into existing footprint and provide wideband performance, to 2.5 GHz instead of 2.0 GHz.
Glitchless Attenuation Transitions, 0.26 typical	Compared to previous generation of digital attenuators which is a vast improvement.



# Digital Step Attenuator

75Ω 1-2500 MHz

31 dB, 1.0 dB Step  
5 Bit, Serial Control Interface, Dual Supply Voltages

## Product Features

- Dual Supply (Positive & Negative) Voltages
- Immune to latch up
- Glitchless attenuation transitions
- Excellent accuracy, 0.1 dB Typ
- Low Insertion Loss
- High IP3, +55-59 dBm Typ
- Very low DC power consumption
- Excellent return loss, 18 dB Typ
- Small size 4.0 x 4.0 mm

## Typical Applications

- DOCSIS® 3.1
- Portable Wireless
- CATV & DBS
- MMDS & Wireless LAN
- Wireless Local Loop
- UNII & Hiper LAN
- Power amplifier distortion canceling loops



Generic photo used for illustration purposes only

## DAT-3175A-SN+

CASE STYLE: DG983-2

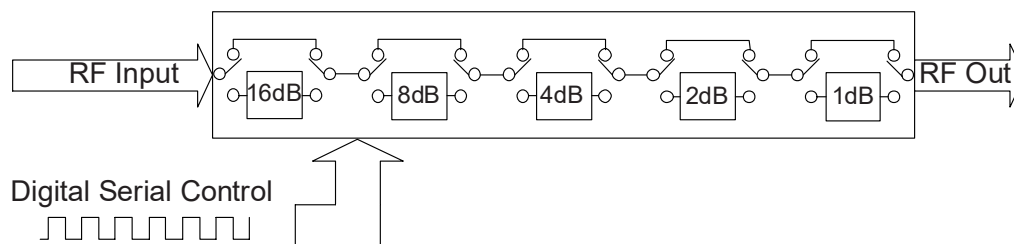
**+RoHS Compliant**

The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

## General Description

The DAT-3175A-SN+ is a 75Ω RF digital step attenuator that offers an attenuation range up to 31 dB in 1.0 dB steps. The control is a 5-bit serial interface, operating dual (positive and negative) supply voltages. The DAT-3175A-SN+ is produced using a unique CMOS process on silicon, offering the performance of GaAs, with the advantages of conventional CMOS devices.

## Simplified Schematic



## RF Electrical Specifications, 1-2500 MHz, $T_{AMB}=25^{\circ}C$ , $V_{DD}=+3V$ , $V_{SS}=-3.2V$ , $75\Omega$

Parameter	Freq. Range (GHz)	Min.	Typ.	Max.	Units
Accuracy @ 1 dB Attenuation Setting	0.001-1.2	—	0.03	0.18	dB
	1.2-2.0	—	0.1	0.20	
	2.0-2.5	—	0.1	0.23	
Accuracy @ 2 dB Attenuation Setting	0.001-1.2	—	0.07	0.21	dB
	1.2-2.0	—	0.15	0.26	
	2.0-2.5	—	0.15	0.31	
Accuracy @ 4 dB Attenuation Setting	0.001-1.2	—	0.05	0.27	dB
	1.2-2.0	—	0.15	0.36	
	2.0-2.5	—	0.2	0.47	
Accuracy @ 8 dB Attenuation Setting	0.001-1.2	—	0.1	0.39	dB
	1.2-2.0	—	0.24	0.60	
	2.0-2.5	—	0.35	0.79	
Accuracy @ 16 dB Attenuation Setting	0.001-1.2	—	0.23	0.63	dB
	1.2-2.5	—	0.8	1.0	
	2.0-2.5	—	0.8	1.43	
Insertion Loss <sup>1</sup> @ all attenuator set to 0dB	0.001-1.2	—	1.2	1.8	dB
	1.2-2.5	—	1.6	1.9	
VSWR	0.001-1.2	—	1.3	—	:1
	1.2-2.5	—	1.4	—	
Input IP3 (at Min. and Max. Attenuation)	.005-2.5	—	55-69	—	dBm
Input IP2	.005-2.5	—	See Fig. 1	—	dBm
Input Power @ 0.1dB Compression (at Min. and Max. Attenuation)	0.030-2.5	—	+30	—	dBm
Input Operating Power	1 MHz to 30 MHz	—	—	See Fig. 2	dBm
	>30 MHz	—	—	+24	
Thermal Resistance (Junction to case)	—	—	25	—	$^{\circ}C/W$

## DC Electrical Specifications

Parameter	Min.	Typ.	Max.	Units
$V_{DD}$ , Supply Voltage	2.7	3	3.6 <sup>2</sup>	V
$I_{DD}$ Supply Current	—	—	80	$\mu A$
Control Input Low	-0.3	—	0.6 <sup>3</sup>	V
$V_{SS}$ , Supply Voltage	-3.6	—	-3.2	V
$I_{SS}$ , Supply Current	-40	—	—	$\mu A$
Control Input High	1.17	—	3.6	V
Control Current	—	—	20	$\mu A$

1. Loss values are de-embedded from test board Loss (test board's Insertion Loss: 0.10dB @ 100MHz, 0.40dB @ 1200MHz, 0.55dB @ 2000MHz, 0.75dB @ 4000MHz).
2. For operation above +3.6V see application note, AN-70-032
3. 0V during power-up.

## Absolute Maximum Ratings<sup>4</sup>

Parameter	Ratings
Operating Temperature	-40 $^{\circ}C$ to 105 $^{\circ}C$
Storage Temperature	-65 $^{\circ}C$ to 150 $^{\circ}C$
$V_{DD}$	-0.3V Min., 5.5V Max.
$V_{SS}$	-3.8V Min.
Voltage on any input	-0.3V Min., 3.6V Max.
Input Power	1-30 MHz
	30-2500MHz
	Figure 2
	+30dBm

4. Permanent damage may occur if any of these limits are exceeded.
5. Operation between max operating and absolute max input power will result in reduced reliability.

## Switching Specifications

Parameter	Min.	Typ.	Max.	Units
Switching Speed, 50% Control to 0.5dB of Attenuation Value	—	0.4	0.7	$\mu Sec$
Switching Control Frequency	—	1.0	—	MHz

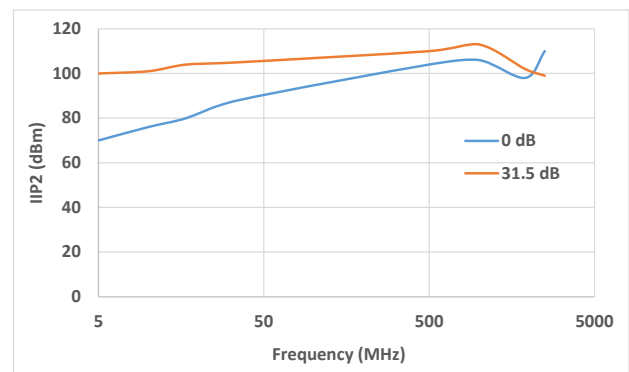
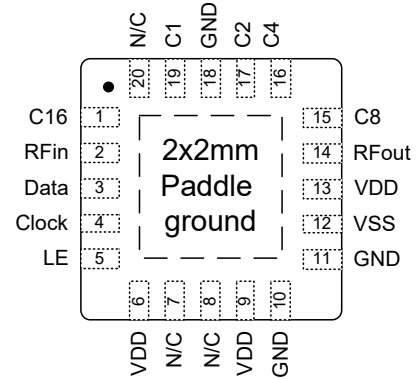


Figure 1. IP2 vs. frequency and attenuation

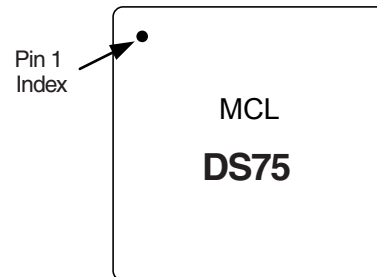
## Pin Description

Function	Pin Number	Description
C16	1	Control for Attenuation bit, 16dB (Notes 4,7)
RF in	2	RF in port (Note 1)
Data	3	Serial Interface data input (Note 3)
Clock	4	Serial Interface clock input
LE	5	Latch Enable Input (Note 2)
V <sub>DD</sub>	6	Power Supply
N/C	7	Not connected
N/C	8	Not connected
V <sub>DD</sub>	9	Power Supply
GND	10	Ground connection
GND	11	Ground connection
V <sub>SS</sub>	12	Negative supply voltage
V <sub>DD</sub>	13	Power Supply
RF out	14	RF out port (Note 1)
C8	15	Control for attenuation bit, 8 dB (Note 4)
C4	16	Control for attenuation bit, 4 dB (Note 4)
C2	17	Control for attenuation bit, 2 dB (Note 4)
GND	18	Ground Connection
C1	19	Control for attenuation bit, 1 dB (Note 4)
N/C	20	Not connected (Note 7,8)
GND	Paddle	Paddle ground (Note 6)

## Pin Configuration (Top View)

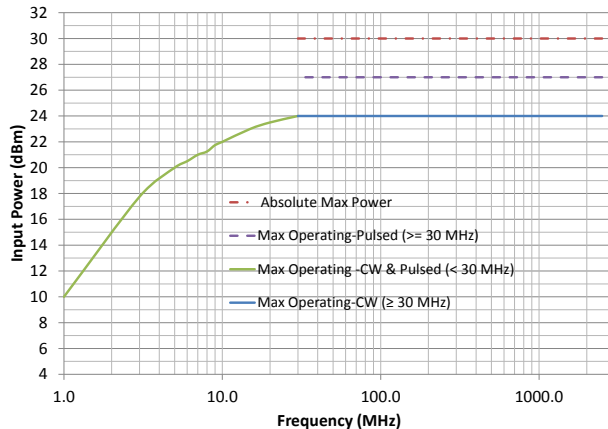


## Device Marking



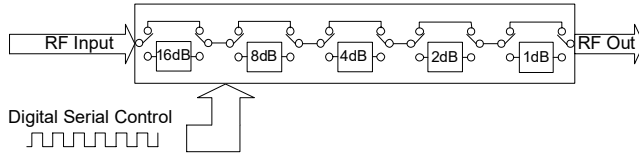
### Notes:

- Both RF ports must be held at 0VDC or DC blocked with an external series capacitor.
- Latch Enable (LE) has an internal 2MΩ resistor to V<sub>DD</sub>.
- Place a 10KΩ resistor in series, as close to pin as possible to avoid freq. resonance.
- Refer to Power-up Control Settings.
- The exposed solder pad on the bottom of the package (See Pin configuration) must be grounded for proper device operation.
- Ground must be less than 80 mil (0.08") from Pin 12 for proper device operation.
- This pin has internal 1MΩ pull down resistor to ground.
- Place 10KΩ resistor to ground externally.



Pulsed Power: 5% duty cycle, 4620 μs period

## Simplified Schematic



The DAT-3175A-SN+ serial interface consists of 5 control bits that select the desired attenuation state, as shown in Table 1: Truth Table

Attenuation State	C16	C8	C4	C2	C1
Reference	0	0	0	0	0
1 (dB)	0	0	0	0	1
2 (dB)	0	0	0	1	0
4 (dB)	0	0	1	0	0
8 (dB)	0	1	0	0	0
16 (dB)	1	0	0	0	0
31 (dB)	1	1	1	1	1

Note: Not all 32 possible combinations of C1 - C16 are shown in table

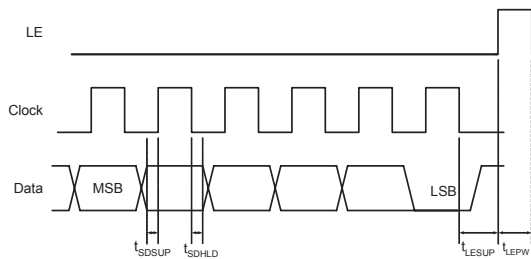
The serial interface is a 5-bit serial in, parallel-out shift register buffered by a transparent latch.

It is controlled by three CMOS-compatible signals: Data, Clock, and Latch Enable (LE). The Data and Clock inputs allow data to be serially entered into the shift register, a process that is independent of the state of the LE input.

The LE input controls the latch. When LE is HIGH, the latch is transparent and the contents of the serial shift register control the attenuator. When LE is brought LOW, data in the shift register is latched.

The shift register should be loaded while LE is held LOW to prevent the attenuator value from changing as data is entered. The LE input should then be toggled HIGH and brought LOW again, latching the new data. The timing for this operation is defined by **Figure 3** (Serial Interface Timing Diagram) and **Table 2** (Serial Interface AC Characteristics).

**Figure 3: Serial Interface Timing Diagram**



Symbol	Parameter	Min.	Max.	Units
$f_{clk}$	Serial data clock frequency (Note 1)		10	MHz
$t_{clkH}$	Serial clock HIGH time	30		ns
$t_{clkL}$	Serial clock LOW time	30		ns
$t_{LESUP}$	LE set-up time after last clock falling edge	10		ns
$t_{LEPW}$	LE minimum pulse width	30		ns
$t_{SDSUP}$	Serial data set-up time before clock rising edge	10		ns
$t_{SDHLD}$	Serial data hold time after clock falling edge	10		ns

Note 1. fclk verified during the functional pattern test. Serial programming sections of the functional pattern are clocked at 10MHz to verify fclk specification.

The DAT-3175A-SN+, uses a common 5-bit serial word format, as shown in Table 3: 5-Bit attenuator Serial Programming Register Map.

The first bit, the MSB, corresponds to the 16-dB Step and B1 bit corresponds to the 1dB step.

B5	B4	B3	B2	B1	B0
C16	C8	C4	C2	C1	0

↑  
MSB  
(first in)

**Note: The stop bit (B0) must always be low to prevent the attenuator from entering an unknown state.**

↑  
LSB  
(last in)

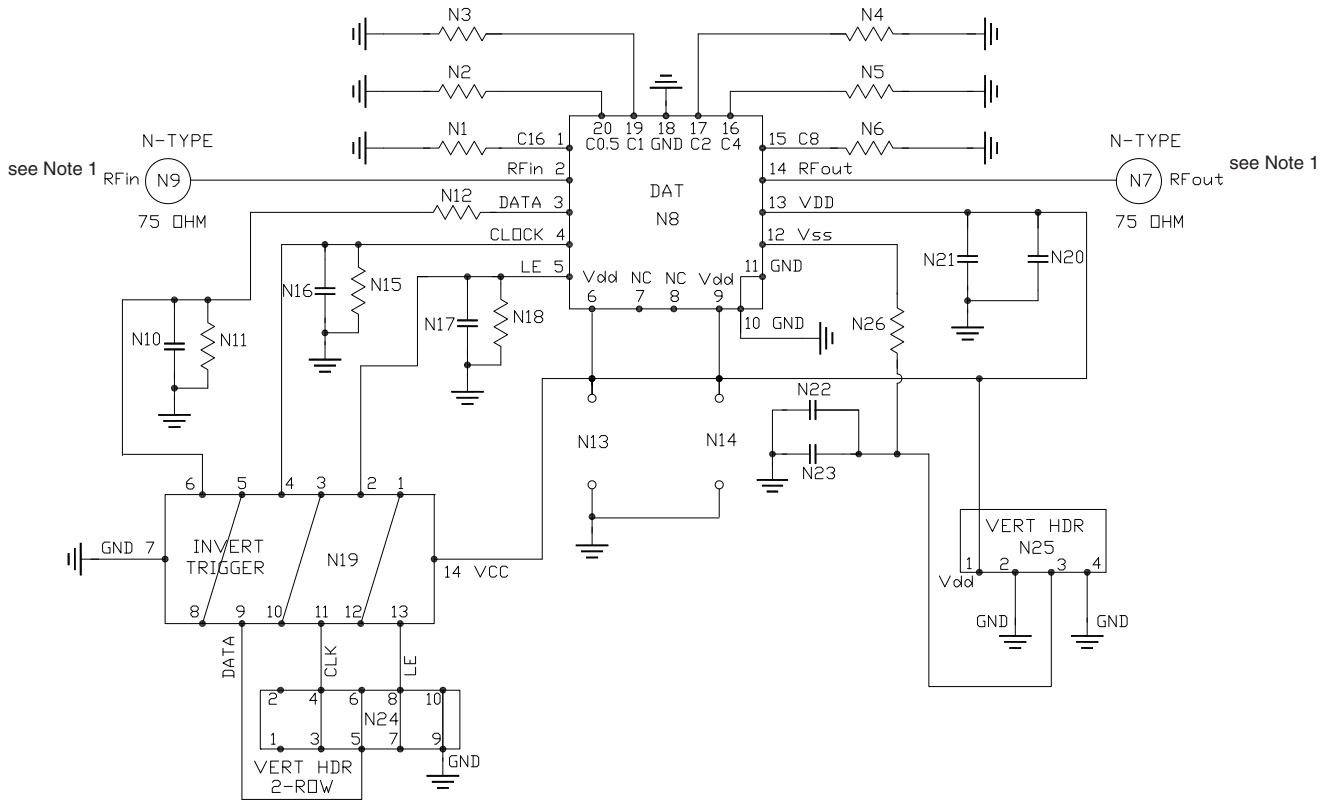
### Power-up Control Settings

The DAT-3175A-SN+ always assumes a specifiable attenuation setting on power-up, allowing a known attenuation state to be established before an initial serial control word is provided.

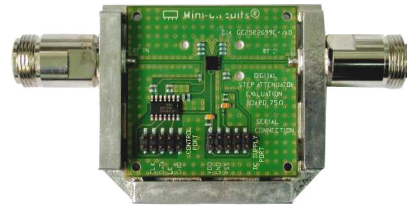
When the attenuator powers up, the five control bits are set to whatever data is present on the five data inputs (C1 to C16).

This allows any one of the 32 attenuation settings to be specified as the power-up state.

## TB-343 Evaluation Board Schematic Diagram



Note 1: Both RF ports must be held at 0VDC or DC blocked with an external series capacitor.



**TB-343**

Bill of Materials	
N1-N6, N11, N12, N15, N18	Resistor 0603 10 KOhm +/- 1%
N10, N16, N17, N20	NPO Capacitor 0603 100pF +/- 5%
N21	Tantalum Capacitor 0805 100nF +/- 10%
N19	Hex Invert Schmitt Trigger MSL1

\*\*N12 can be reduced to 0 Ohms

<b>Additional Detailed Technical Information</b>	
<i>additional information is available on our dash board. To access this information <a href="#">click here</a></i>	
<b>Performance Data</b>	Data Table
	Swept Graphs
	S-Parameter (S2P Files) Data Set (.zip file)
<b>Case Style</b>	DG983-2 <i>Plastic package, exposed paddle, lead finish: NiPdAu</i>
<b>Tape &amp; Reel</b> Standard quantities available on reel	F87 <i>7" reels with 20, 50, 100, 200, 500 Or 1000 devices 13" reels with 3K devices</i>
<b>Suggested Layout for PCB Design</b>	PL-194
<b>Evaluation Board</b>	TB-343
<b>Environmental Ratings</b>	ENV33T1

## ESD Rating

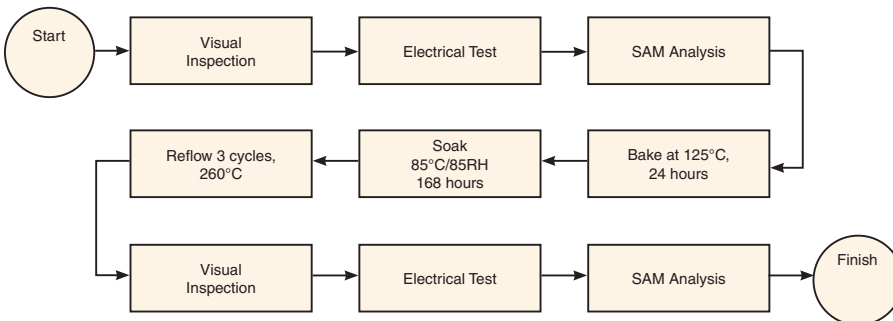
Human Body Model (HBM): Class 1C (1000 to <2000V) in accordance with MIL-STD-883 method 3015 (pass 1500V)

Charge Device Model class C2 (500 to <1000V) per JESD22-C101

## MSL Rating

Moisture Sensitivity: MSL1 in accordance with IPC/JEDEC J-STD-020D

## MSL Test Flow Chart



## Additional Notes

- Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
- Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
- The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the Standard Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at [www.minicircuits.com/MCLStore/terms.jsp](http://www.minicircuits.com/MCLStore/terms.jsp)



# Digital Step Attenuator

# DAT-3175A-SN+

## Typical Performance Data

TEST CONDITIONS: INPUT POWER=-10dBm, Vdd=+3V, Vss = -3.2V, TEMPERATURE=-45degC

FREQUENCY (MHz)	STEP ATTENUATION* AT TTL CONTROL STATE (dB)						
	00000 THRU LOSS	00001 1.0 dB	00010 2.0 dB	00100 4.0 dB	01000 8.0 dB	10000 16 dB	11111 31dB
10	0.98	1.04	2.05	4.11	8.17	16.17	31.20
50	0.98	1.05	2.05	4.11	8.17	16.20	31.35
100	0.99	1.04	2.05	4.11	8.16	16.20	31.27
150	1.00	1.04	2.04	4.10	8.16	16.19	31.29
200	1.00	1.05	2.05	4.10	8.16	16.20	31.23
250	1.01	1.04	2.05	4.10	8.16	16.20	31.29
300	1.01	1.04	2.05	4.11	8.16	16.21	31.23
350	1.02	1.04	2.05	4.11	8.17	16.21	31.31
400	1.02	1.04	2.05	4.11	8.17	16.21	31.30
450	1.03	1.04	2.04	4.10	8.16	16.20	31.25
500	1.05	1.04	2.04	4.10	8.15	16.19	31.30
550	1.07	1.03	2.03	4.09	8.14	16.17	31.29
600	1.08	1.03	2.03	4.09	8.13	16.17	31.27
700	1.12	1.03	2.03	4.07	8.11	16.14	31.24
800	1.17	1.03	2.02	4.06	8.09	16.12	31.15
900	1.18	1.03	2.02	4.05	8.10	16.14	31.27
1000	1.21	1.03	2.02	4.05	8.10	16.17	31.51
1100	1.24	1.02	2.02	4.05	8.10	16.16	31.41
1200	1.26	1.02	2.01	4.04	8.08	16.12	31.06
1300	1.25	1.02	2.01	4.04	8.08	16.08	30.65
1400	1.24	1.02	2.01	4.04	8.06	16.00	30.28
1500	1.28	1.02	2.02	4.05	8.10	16.14	31.09
1600	1.32	1.02	2.02	4.07	8.13	16.22	31.44
1700	1.32	1.02	2.02	4.08	8.13	16.20	31.04
1800	1.33	1.03	2.02	4.08	8.13	16.16	30.67
1900	1.34	1.03	2.02	4.09	8.12	16.09	30.07
2000	1.36	1.02	2.02	4.09	8.13	16.04	29.68
2100	1.38	1.02	2.02	4.10	8.16	16.06	29.56
2200	1.39	1.03	2.03	4.11	8.17	16.03	29.35
2300	1.47	1.04	2.05	4.14	8.23	16.15	29.97
2400	1.57	1.04	2.06	4.18	8.31	16.34	30.86
2500	1.34	1.04	2.05	4.17	8.27	16.09	29.22
2600	1.21	1.05	2.07	4.23	8.35	16.19	29.45
2700	1.17	1.06	2.08	4.28	8.43	16.28	29.52
2800	1.32	1.05	2.08	4.32	8.51	16.51	30.72
2900	1.63	1.03	2.05	4.29	8.48	16.56	31.00
3000	2.09	0.99	1.99	4.24	8.45	16.81	33.66

\* Step Attenuation above Thru Loss (TTL Logic 00000).



REV. OR  
DAT-3175A-SN+

10/12/2016

Page 1 of 9

P.O. Box 350166, Brooklyn, New York 11235-0003 (718) 934-4500 • Fax (718) 332-4661 For detailed performance specs & shopping online see Mini-Circuits web site  
The Design Engineers Search Engine Provides ACTUAL Data Instantly From MINI-CIRCUITS At: www.minicircuits.com



IF/RF MICROWAVE COMPONENTS

# Digital Step Attenuator

# DAT-3175A-SN+

## Typical Performance Data

TEST CONDITIONS: INPUT POWER=-10dBm, Vdd=+3V, Vss = -3.2V, TEMPERATURE=-45degC

FREQUENCY (MHz)	INPUT RETURN LOSS AT TTL CONTROL STATE (dB)						
	0000 0 dB	00001 1.0 dB	00010 2.0 dB	00100 4.0 dB	01000 8.0 dB	10000 16 dB	11111 31dB
10	19.90	22.62	24.20	37.05	29.27	24.73	23.22
50	19.52	22.11	23.57	40.52	30.44	25.60	23.88
100	19.65	22.23	23.70	38.36	30.30	25.67	23.92
150	19.34	21.78	23.12	40.42	31.89	26.66	24.74
200	19.48	21.90	23.28	40.22	31.60	26.53	24.66
250	19.26	21.66	23.02	43.20	32.44	26.90	24.97
300	19.37	21.88	23.30	39.35	30.95	26.16	24.38
350	19.23	21.73	23.16	38.01	30.83	26.08	24.31
400	19.25	21.77	23.20	33.77	29.38	25.47	23.87
450	19.17	21.61	22.99	32.66	29.41	25.68	24.05
500	19.16	21.50	22.81	30.62	28.57	25.46	23.95
550	19.16	21.43	22.69	30.54	28.94	25.91	24.40
600	19.14	21.35	22.51	29.56	28.48	25.91	24.46
700	19.27	21.36	22.42	29.16	28.41	26.24	24.84
800	19.12	21.11	22.05	28.32	27.82	26.19	25.00
900	18.82	20.72	21.58	27.30	27.00	25.87	24.88
1000	18.40	20.24	21.06	26.63	26.55	25.80	25.00
1100	17.94	19.68	20.46	26.06	26.39	26.26	25.64
1200	17.57	19.22	19.95	25.76	26.48	27.05	26.78
1300	17.20	18.79	19.46	25.71	26.97	28.63	28.90
1400	16.73	18.25	18.88	25.36	27.13	30.04	31.54
1500	16.49	17.99	18.62	25.55	27.98	32.73	36.17
1600	16.66	18.15	18.77	26.34	29.76	38.44	49.76
1700	17.54	19.01	19.55	27.63	30.95	32.98	30.94
1800	18.73	19.99	20.44	26.58	27.79	26.51	24.95
1900	19.68	20.59	20.92	23.77	24.17	22.85	21.64
2000	20.01	20.45	20.78	21.21	21.36	20.23	19.26
2100	20.22	20.16	20.43	19.17	19.12	18.12	17.29
2200	19.86	19.50	19.72	17.81	17.64	16.75	16.04
2300	21.46	20.95	21.07	18.03	17.47	16.44	15.72
2400	25.55	23.45	22.70	17.70	16.57	15.48	14.81
2500	31.40	24.90	22.83	16.81	15.37	14.32	13.79
2600	25.84	22.87	20.87	15.62	14.01	13.06	12.64
2700	19.54	19.43	18.47	14.69	13.10	12.25	11.91
2800	15.02	15.81	15.75	13.73	12.46	11.80	11.53
2900	12.33	13.33	13.68	12.92	12.09	11.62	11.42
3000	10.55	11.66	12.21	12.36	12.01	11.82	11.71

# Digital Step Attenuator

# DAT-3175A-SN+

## Typical Performance Data

TEST CONDITIONS: INPUT POWER=-10dBm, Vdd=+3V, Vss = -3.2V, TEMPERATURE=-45degC

FREQUENCY (MHz)	OUTPUT RETURN LOSS AT TTL CONTROL STATE (dB)						
	0000 0 dB	00001 1.0 dB	00010 2.0 dB	00100 4.0 dB	01000 8.0 dB	10000 16 dB	11111 31dB
10	19.74	22.14	24.03	27.77	29.67	24.76	23.29
50	19.38	21.61	23.41	26.75	30.95	25.66	23.90
100	19.33	21.49	23.26	26.41	31.23	25.88	24.11
150	18.91	20.99	22.63	25.49	33.29	27.04	25.04
200	19.06	21.15	22.83	25.78	32.89	26.80	24.85
250	18.99	21.08	22.77	25.79	33.27	26.90	24.92
300	19.16	21.34	23.13	26.32	31.42	25.98	24.23
350	18.98	21.14	22.94	26.04	31.33	26.06	24.26
400	18.87	21.03	22.81	25.66	29.72	25.49	23.84
450	18.57	20.64	22.33	24.97	30.08	25.92	24.26
500	18.26	20.21	21.81	24.14	29.53	26.06	24.48
550	18.15	20.03	21.58	23.77	30.28	26.89	25.16
600	18.00	19.83	21.27	23.29	30.21	27.33	25.64
700	18.09	19.85	21.24	23.11	31.09	28.40	26.56
800	18.38	20.09	21.45	23.12	31.03	28.76	26.95
900	19.13	20.90	22.29	23.79	29.73	27.43	25.82
1000	19.34	21.04	22.33	23.50	28.86	27.20	25.78
1100	18.94	20.45	21.57	22.36	27.65	27.19	26.19
1200	18.73	20.09	21.09	21.63	26.79	27.08	26.52
1300	18.93	20.26	21.20	21.51	26.36	26.74	26.32
1400	19.08	20.39	21.27	21.47	26.35	26.79	26.52
1500	18.84	20.09	20.94	21.17	27.43	28.57	28.63
1600	17.94	19.04	19.79	20.04	26.40	28.69	29.97
1700	17.76	18.85	19.63	20.03	27.53	31.06	33.57
1800	17.64	18.78	19.63	20.48	30.43	39.49	50.18
1900	17.78	18.98	19.90	21.51	32.17	33.48	30.32
2000	17.73	18.89	19.80	22.30	26.88	25.50	23.87
2100	17.35	18.54	19.48	22.39	24.73	23.19	21.81
2200	18.72	19.97	20.94	24.86	22.27	20.37	19.23
2300	20.43	21.40	21.86	25.17	19.62	18.00	17.07
2400	23.35	24.34	24.35	27.15	19.09	17.37	16.46
2500	32.82	32.58	28.44	25.16	17.67	16.17	15.41
2600	33.07	32.66	27.15	21.43	16.16	14.81	14.19
2700	21.41	22.75	22.22	18.18	14.85	13.65	13.15
2800	15.53	16.90	17.58	15.68	14.14	13.21	12.81
2900	11.84	13.04	13.89	13.20	13.07	12.59	12.35
3000	9.64	10.71	11.56	11.48	12.29	12.29	12.23

# Digital Step Attenuator

# DAT-3175A-SN+

## Typical Performance Data

TEST CONDITIONS: INPUT POWER=-10dBm, Vdd=+3V, Vss= -3.2V, TEMPERATURE=+25degC

FREQUENCY (MHz)	STEP ATTENUATION* AT TTL CONTROL STATE (dB)						
	00000 THRU LOSS	00001 1.0 dB	00010 2.0 dB	00100 4.0 dB	01000 8.0 dB	10000 16 dB	11111 31dB
10	1.10	1.02	2.03	4.04	8.00	16.00	30.91
50	1.11	1.02	2.01	4.01	8.00	15.97	30.88
100	1.12	1.02	2.02	4.01	8.01	15.98	30.92
150	1.13	1.01	2.02	4.02	8.00	15.98	30.89
200	1.14	1.02	2.02	4.01	8.00	15.97	30.89
250	1.15	1.01	2.01	4.01	8.00	15.96	30.88
300	1.16	1.01	2.01	4.01	7.99	15.96	30.90
350	1.18	1.02	2.01	4.01	8.00	15.98	30.97
400	1.20	1.01	2.01	4.01	7.99	15.97	30.88
450	1.21	1.01	2.01	4.00	7.98	15.96	30.88
500	1.22	1.01	2.01	4.00	7.98	15.95	30.94
550	1.24	1.01	2.00	4.00	7.97	15.94	30.92
600	1.26	1.01	2.00	3.99	7.97	15.94	30.93
700	1.29	1.01	2.00	3.99	7.96	15.93	30.91
800	1.34	1.00	2.00	3.98	7.95	15.92	30.83
900	1.36	1.00	2.00	3.98	7.96	15.94	31.00
1000	1.41	1.00	2.00	3.97	7.96	15.96	31.15
1100	1.44	1.00	2.00	3.97	7.96	15.96	31.06
1200	1.47	1.00	1.99	3.97	7.95	15.93	30.76
1300	1.48	0.99	1.99	3.96	7.94	15.87	30.30
1400	1.49	0.99	1.99	3.96	7.93	15.82	30.14
1500	1.54	1.00	1.99	3.98	7.97	15.97	30.94
1600	1.57	1.00	2.00	4.00	8.00	16.03	31.10
1700	1.58	1.00	2.00	4.00	8.00	15.99	30.69
1800	1.60	1.00	2.00	4.01	8.01	15.99	30.34
1900	1.62	1.00	1.99	4.01	8.01	15.91	29.85
2000	1.64	1.00	1.99	4.01	8.00	15.86	29.54
2100	1.63	0.99	1.99	4.01	8.02	15.88	29.48
2200	1.60	1.00	1.99	4.02	8.04	15.86	29.25
2300	1.59	1.00	1.99	4.04	8.09	15.97	29.88
2400	1.52	1.00	2.00	4.07	8.15	16.07	29.97
2500	1.29	1.00	2.00	4.08	8.12	15.88	28.90
2600	1.24	1.00	2.01	4.12	8.19	15.96	29.05
2700	1.28	1.01	2.02	4.15	8.24	16.03	29.30
2800	1.47	1.00	2.01	4.16	8.28	16.19	30.32
2900	1.80	0.98	1.98	4.14	8.25	16.27	30.82
3000	2.25	0.95	1.94	4.11	8.24	16.51	33.27

\* Step Attenuation above Thru Loss (TTL Logic 00000).



REV. OR  
DAT-3175A-SN+

10/12/2016

Page 4 of 9

P.O. Box 350166, Brooklyn, New York 11235-0003 (718) 934-4500 • Fax (718) 332-4661 For detailed performance specs & shopping online see Mini-Circuits web site  
The Design Engineers Search Engine Provides ACTUAL Data Instantly From MINI-CIRCUITS At: www.minicircuits.com



IF/RF MICROWAVE COMPONENTS

# Digital Step Attenuator

# DAT-3175A-SN+

## Typical Performance Data

TEST CONDITIONS: INPUT POWER=-10dBm, Vdd=+3V, Vss= -3.2V, TEMPERATURE=+25degC

FREQUENCY (MHz)	INPUT RETURN LOSS AT TTL CONTROL STATE (dB)						
	0000 0 dB	00001 1.0 dB	00010 2.0 dB	00100 4.0 dB	01000 8.0 dB	10000 16 dB	11111 31dB
10	18.79	20.35	20.96	31.48	36.58	48.35	36.54
50	18.49	19.95	20.56	30.32	34.85	58.89	39.15
100	18.63	20.14	20.79	30.94	35.85	48.82	37.48
150	18.34	19.81	20.45	29.72	33.88	46.04	39.82
200	18.43	19.90	20.53	29.75	33.77	42.37	38.30
250	18.23	19.65	20.28	28.90	32.38	41.03	40.71
300	18.27	19.68	20.29	28.68	31.95	38.64	38.92
350	18.12	19.50	20.08	28.13	31.12	38.22	41.50
400	18.16	19.54	20.11	27.99	30.78	36.94	39.38
450	18.18	19.52	20.08	27.92	30.71	37.43	42.61
500	18.29	19.63	20.17	27.94	30.49	36.66	40.79
550	18.38	19.73	20.22	28.05	30.62	37.47	44.41
600	18.49	19.82	20.32	28.01	30.42	36.62	41.84
700	18.69	20.01	20.47	28.17	30.25	36.03	40.84
800	18.64	19.93	20.36	27.56	29.26	33.80	37.26
900	18.43	19.70	20.10	26.68	27.94	31.48	33.94
1000	18.00	19.24	19.63	25.60	26.68	29.75	32.13
1100	17.36	18.54	18.91	24.38	25.36	28.27	30.89
1200	16.72	17.84	18.15	23.21	24.14	26.88	29.50
1300	16.19	17.21	17.49	22.22	23.08	25.50	27.97
1400	15.82	16.74	16.97	21.46	22.16	24.22	26.22
1500	15.77	16.64	16.82	21.29	21.95	23.77	25.47
1600	16.09	16.87	17.00	21.42	22.02	23.40	24.57
1700	16.76	17.41	17.45	21.54	21.99	22.63	23.04
1800	17.43	17.86	17.85	21.11	21.51	21.51	21.33
1900	17.92	18.14	18.14	20.33	20.80	20.41	19.89
2000	18.14	18.20	18.31	19.44	20.04	19.46	18.77
2100	18.40	18.32	18.54	18.66	19.27	18.54	17.77
2200	18.43	18.34	18.68	18.24	18.84	18.08	17.25
2300	20.19	20.20	20.69	19.43	19.83	18.76	17.76
2400	24.65	24.57	25.20	20.83	20.38	18.89	17.78
2500	38.26	34.82	31.78	20.49	18.98	17.63	16.76
2600	26.93	27.40	25.46	18.73	16.95	15.91	15.32
2700	19.83	20.93	20.57	17.28	15.60	14.78	14.37
2800	15.55	16.74	16.96	15.86	14.63	14.11	13.84
2900	12.92	14.13	14.62	14.75	14.05	13.83	13.68
3000	11.10	12.28	12.91	13.90	13.74	13.91	13.93

# Digital Step Attenuator

# DAT-3175A-SN+

## Typical Performance Data

TEST CONDITIONS: INPUT POWER=-10dBm, Vdd=+3V, Vss= -3.2V, TEMPERATURE=+25degC

FREQUENCY (MHz)	OUTPUT RETURN LOSS AT TTL CONTROL STATE (dB)						
	00000 0 dB	00001 1.0 dB	00010 2.0 dB	00100 4.0 dB	01000 8.0 dB	10000 16 dB	11111 31dB
10	18.77	20.00	20.91	21.82	35.76	45.95	36.34
50	18.41	19.65	20.55	21.52	34.18	59.31	38.84
100	18.48	19.73	20.65	21.68	34.69	47.71	37.58
150	18.14	19.36	20.27	21.26	32.93	45.25	40.09
200	18.15	19.38	20.28	21.29	32.65	41.83	38.53
250	17.89	19.08	19.98	20.92	31.42	40.95	41.58
300	17.86	19.05	19.92	20.86	30.89	38.39	39.49
350	17.68	18.83	19.67	20.55	30.09	37.70	42.53
400	17.67	18.81	19.67	20.54	29.81	36.54	40.08
450	17.60	18.72	19.56	20.37	29.56	36.80	44.15
500	17.56	18.67	19.50	20.28	29.10	35.84	42.12
550	17.59	18.67	19.50	20.22	29.05	36.03	45.82
600	17.56	18.63	19.43	20.13	28.69	35.20	43.65
700	17.70	18.74	19.53	20.13	28.69	35.30	44.98
800	17.93	18.97	19.73	20.22	28.69	35.20	44.70
900	18.46	19.53	20.31	20.68	29.67	37.12	48.16
1000	18.61	19.62	20.36	20.56	29.09	35.47	43.22
1100	18.36	19.29	19.97	19.98	27.25	31.88	36.50
1200	18.04	18.87	19.48	19.30	25.51	29.00	32.26
1300	17.88	18.63	19.15	18.84	24.47	27.43	30.24
1400	17.89	18.54	18.98	18.55	24.01	26.73	29.42
1500	17.63	18.15	18.49	18.08	23.34	25.73	28.14
1600	17.09	17.57	17.86	17.52	22.44	24.48	26.56
1700	17.06	17.53	17.86	17.66	22.83	24.80	26.60
1800	16.99	17.49	17.85	17.96	23.12	24.75	25.72
1900	17.12	17.66	18.10	18.63	23.43	24.24	24.17
2000	17.14	17.66	18.08	19.20	22.53	22.42	21.81
2100	17.40	17.99	18.44	20.09	22.51	21.89	21.02
2200	18.75	19.26	19.69	22.23	22.07	20.80	19.68
2300	19.44	19.88	20.17	23.60	20.76	19.38	18.32
2400	21.25	21.98	22.43	27.59	21.41	19.60	18.42
2500	25.96	28.08	28.74	36.27	20.76	18.75	17.65
2600	27.11	34.15	41.08	25.87	19.38	17.61	16.69
2700	20.63	22.77	24.03	20.00	17.62	16.29	15.64
2800	15.48	16.88	17.96	16.27	16.18	15.45	15.05
2900	12.18	13.32	14.28	13.54	14.55	14.43	14.28
3000	10.06	11.06	11.93	11.68	13.39	13.80	13.91

# Digital Step Attenuator

# DAT-3175A-SN+

## Typical Performance Data

TEST CONDITIONS: INPUT POWER=-10dBm, Vdd=+3V, Vss =-3.2V, TEMPERATURE=+85degC

FREQUENCY (MHz)	STEP ATTENUATION* AT TTL CONTROL STATE (dB)						
	00000 THRU LOSS	00001 1.0 dB	00010 2.0 dB	00100 4.0 dB	01000 8.0 dB	10000 16 dB	11111 31dB
10	1.30	0.99	1.99	3.91	7.82	15.72	30.76
50	1.30	0.99	1.99	3.92	7.84	15.72	30.47
100	1.30	0.99	1.99	3.92	7.84	15.74	30.46
150	1.30	0.99	1.99	3.92	7.84	15.73	30.52
200	1.32	0.99	1.99	3.92	7.84	15.73	30.52
250	1.34	0.99	1.99	3.92	7.84	15.72	30.50
300	1.37	0.98	1.99	3.92	7.83	15.71	30.50
350	1.40	0.99	1.99	3.92	7.82	15.72	30.48
400	1.43	0.99	1.98	3.91	7.82	15.72	30.49
450	1.45	0.98	1.98	3.91	7.81	15.70	30.54
500	1.46	0.99	1.98	3.91	7.81	15.71	30.53
550	1.48	0.98	1.98	3.91	7.81	15.71	30.52
600	1.49	0.98	1.98	3.91	7.82	15.70	30.52
700	1.52	0.98	1.98	3.91	7.82	15.71	30.43
800	1.57	0.98	1.98	3.90	7.82	15.71	30.50
900	1.60	0.98	1.98	3.91	7.83	15.74	30.62
1000	1.65	0.98	1.98	3.90	7.83	15.77	30.82
1100	1.70	0.97	1.98	3.90	7.83	15.76	30.67
1200	1.74	0.97	1.97	3.90	7.82	15.71	30.39
1300	1.76	0.97	1.97	3.89	7.81	15.66	29.93
1400	1.78	0.97	1.97	3.90	7.81	15.66	29.95
1500	1.84	0.98	1.98	3.91	7.85	15.78	30.66
1600	1.86	0.98	1.98	3.93	7.87	15.82	30.71
1700	1.86	0.98	1.98	3.94	7.88	15.78	30.26
1800	1.85	0.98	1.98	3.94	7.89	15.75	29.83
1900	1.83	0.98	1.98	3.94	7.90	15.75	29.68
2000	1.82	0.98	1.98	3.95	7.91	15.72	29.47
2100	1.76	0.97	1.98	3.96	7.93	15.74	29.39
2200	1.72	0.97	1.97	3.96	7.95	15.74	29.26
2300	1.69	0.97	1.97	3.99	8.01	15.88	29.97
2400	1.54	0.97	1.96	4.00	8.00	15.75	29.03
2500	1.42	0.97	1.96	4.00	8.00	15.67	28.64
2600	1.42	0.96	1.96	4.01	8.02	15.72	28.85
2700	1.51	0.95	1.95	4.02	8.05	15.80	29.22
2800	1.72	0.94	1.94	4.03	8.07	15.89	29.90
2900	2.06	0.92	1.91	4.01	8.05	16.00	30.69
3000	2.48	0.90	1.89	3.99	8.05	16.21	32.93

\* Step Attenuation above Thru Loss (TTL Logic 00000).



REV. OR  
DAT-3175A-SN+

P.O. Box 350166, Brooklyn, New York 11235-0003 (718) 934-4500 • Fax (718) 332-4661 For detailed performance specs & shopping online see Mini-Circuits web site  
The Design Engineers Search Engine Provides ACTUAL Data Instantly From MINI-CIRCUITS At: www.minicircuits.com

10/12/2016  
Page 7 of 9



# Digital Step Attenuator

# DAT-3175A-SN+

## Typical Performance Data

TEST CONDITIONS: INPUT POWER=-10dBm, Vdd=+3V, Vss =-3.2V, TEMPERATURE=+85degC

FREQUENCY (MHz)	INPUT RETURN LOSS AT TTL CONTROL STATE (dB)						
	00000 0 dB	00001 1.0 dB	00010 2.0 dB	00100 4.0 dB	01000 8.0 dB	10000 16 dB	11111 31dB
10	17.47	18.18	18.17	22.85	22.90	24.57	26.13
50	17.26	17.88	17.94	22.47	22.63	24.19	25.76
100	17.37	18.03	18.13	22.86	23.12	24.84	26.51
150	17.14	17.82	17.94	22.54	22.89	24.58	26.24
200	17.20	17.91	18.03	22.69	23.05	24.82	26.53
250	17.01	17.69	17.80	22.23	22.52	24.13	25.71
300	17.02	17.67	17.76	22.05	22.28	23.84	25.30
350	16.88	17.48	17.53	21.61	21.71	23.08	24.43
400	16.91	17.50	17.50	21.47	21.51	22.79	24.08
450	16.95	17.47	17.45	21.32	21.23	22.39	23.56
500	17.10	17.59	17.53	21.40	21.25	22.35	23.49
550	17.22	17.68	17.60	21.44	21.23	22.25	23.34
600	17.39	17.86	17.75	21.66	21.44	22.45	23.54
700	17.79	18.26	18.15	22.30	22.04	23.08	24.24
800	18.02	18.57	18.48	22.95	22.73	23.97	25.28
900	18.00	18.62	18.57	23.25	23.11	24.61	26.22
1000	17.52	18.22	18.22	22.75	22.72	24.35	26.07
1100	16.75	17.44	17.47	21.62	21.65	23.17	24.74
1200	15.93	16.59	16.62	20.36	20.40	21.73	23.08
1300	15.29	15.89	15.88	19.25	19.20	20.26	21.34
1400	14.96	15.45	15.37	18.45	18.27	19.08	19.95
1500	14.96	15.35	15.20	18.08	17.78	18.36	19.05
1600	15.33	15.55	15.31	17.96	17.55	17.84	18.33
1700	15.87	15.92	15.58	17.86	17.41	17.45	17.69
1800	16.35	16.21	15.86	17.68	17.37	17.16	17.20
1900	16.63	16.38	16.12	17.44	17.40	17.04	16.87
2000	16.85	16.58	16.46	17.34	17.62	17.16	16.80
2100	17.25	16.98	17.01	17.41	17.99	17.41	16.85
2200	17.80	17.59	17.78	17.94	18.79	18.09	17.38
2300	19.86	19.78	20.13	20.03	21.16	20.02	18.92
2400	24.16	24.56	25.29	23.78	24.83	22.58	20.83
2500	28.95	34.08	37.73	25.19	24.04	21.98	20.47
2600	24.72	28.31	28.40	23.27	21.28	20.08	19.17
2700	19.46	21.43	21.75	21.02	19.23	18.65	18.16
2800	15.93	17.48	17.97	18.89	17.76	17.67	17.47
2900	13.48	14.84	15.43	17.17	16.69	17.08	17.16
3000	11.63	12.88	13.50	15.74	15.89	16.79	17.21



# Digital Step Attenuator

# DAT-3175A-SN+

## Typical Performance Data

TEST CONDITIONS: INPUT POWER=-10dBm, Vdd=+3V, Vss =-3.2V, TEMPERATURE=+85degC

FREQUENCY (MHz)	OUTPUT RETURN LOSS AT TTL CONTROL STATE (dB)						
	0000 0 dB	00001 1.0 dB	00010 2.0 dB	00100 4.0 dB	01000 8.0 dB	10000 16 dB	11111 31dB
10	17.48	17.82	18.10	17.95	22.77	24.68	26.30
50	17.25	17.68	17.95	17.78	22.52	24.32	25.93
100	17.43	17.89	18.21	18.08	23.07	25.04	26.83
150	17.23	17.73	18.07	17.98	22.92	24.90	26.65
200	17.18	17.69	18.05	17.98	22.94	24.98	26.78
250	16.80	17.29	17.64	17.58	22.22	24.07	25.65
300	16.58	17.06	17.38	17.32	21.72	23.47	24.98
350	16.32	16.74	17.05	16.92	21.06	22.61	23.92
400	16.30	16.70	16.99	16.83	20.85	22.33	23.59
450	16.30	16.68	16.93	16.73	20.62	21.94	23.10
500	16.42	16.78	16.99	16.78	20.63	21.93	23.06
550	16.61	16.94	17.14	16.89	20.69	21.88	22.97
600	16.81	17.11	17.30	17.01	20.88	22.05	23.12
700	17.19	17.50	17.67	17.35	21.38	22.54	23.59
800	17.53	17.87	18.07	17.71	22.02	23.28	24.45
900	17.96	18.35	18.58	18.13	22.86	24.33	25.63
1000	17.92	18.33	18.56	18.05	22.89	24.48	25.96
1100	17.47	17.88	18.14	17.55	22.18	23.84	25.46
1200	16.90	17.29	17.53	16.88	21.14	22.69	24.22
1300	16.48	16.79	16.99	16.27	20.15	21.47	22.84
1400	16.26	16.47	16.57	15.80	19.31	20.35	21.48
1500	15.92	15.98	15.98	15.21	18.27	19.00	19.86
1600	15.85	15.80	15.74	14.97	17.81	18.39	19.11
1700	16.12	16.00	15.88	15.18	17.85	18.24	18.77
1800	16.40	16.23	16.10	15.58	18.04	18.22	18.52
1900	16.58	16.42	16.31	16.15	18.26	18.20	18.25
2000	16.31	16.24	16.22	16.55	18.18	17.98	17.81
2100	16.51	16.57	16.66	17.55	18.89	18.51	18.08
2200	17.00	17.15	17.33	18.94	19.43	18.79	18.08
2300	17.51	17.76	18.00	20.29	19.95	19.03	18.15
2400	19.38	19.87	20.27	23.48	22.51	20.84	19.51
2500	22.28	23.48	24.50	29.92	24.88	21.90	20.17
2600	23.17	25.87	29.40	27.04	25.15	22.02	20.31
2700	19.49	21.50	23.68	20.74	22.30	20.81	19.66
2800	15.50	16.88	18.23	16.68	19.27	19.12	18.65
2900	12.63	13.73	14.79	13.93	16.73	17.39	17.46
3000	10.60	11.54	12.45	12.00	14.91	16.07	16.53



REV. OR  
DAT-3175A-SN+

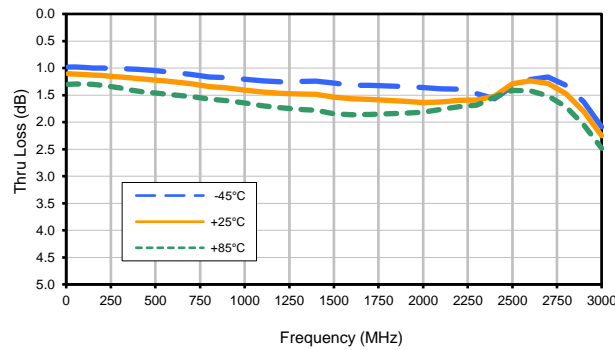
10/12/2016  
Page 9 of 9

# Digital Step Attenuator

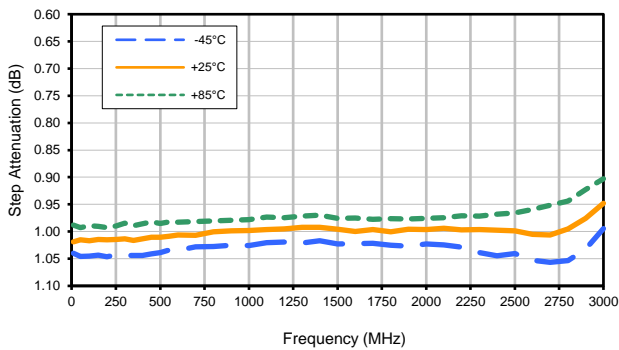
## Typical Performance Curves

# DAT-3175A-SN+

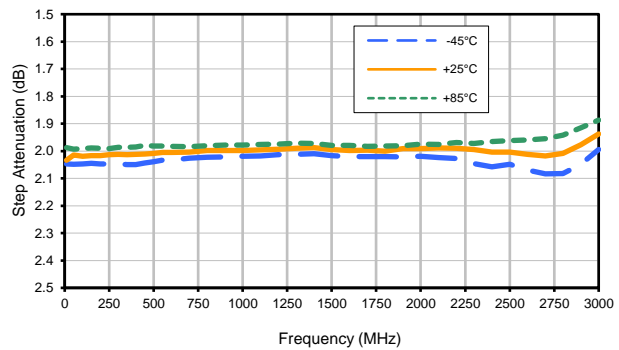
Thru Loss



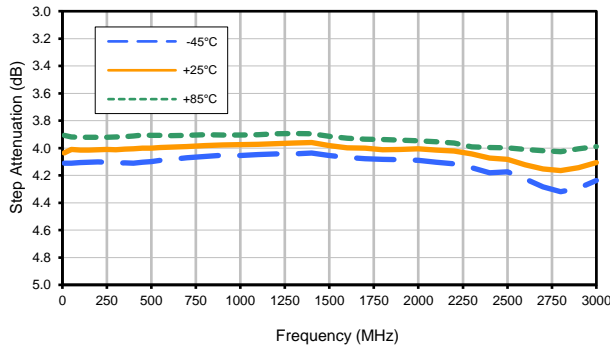
Step Attenuation (1dB)



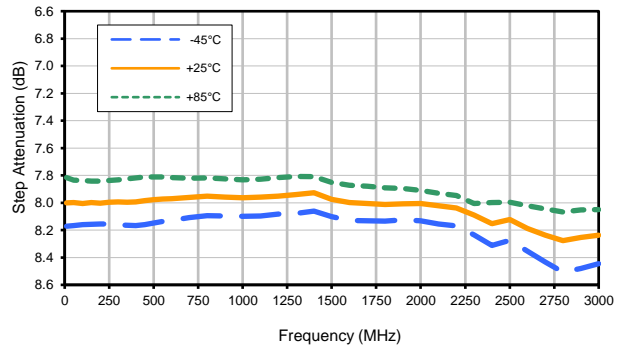
Step Attenuation (2dB)



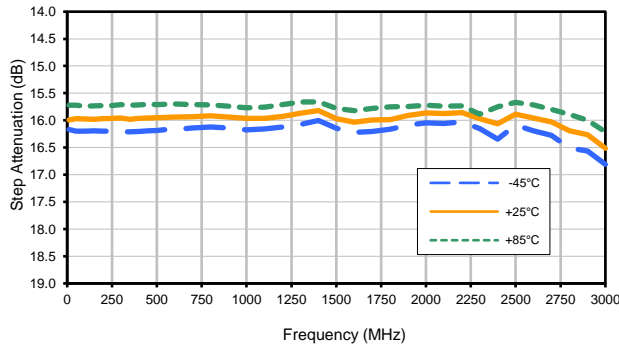
Step Attenuation (4dB)



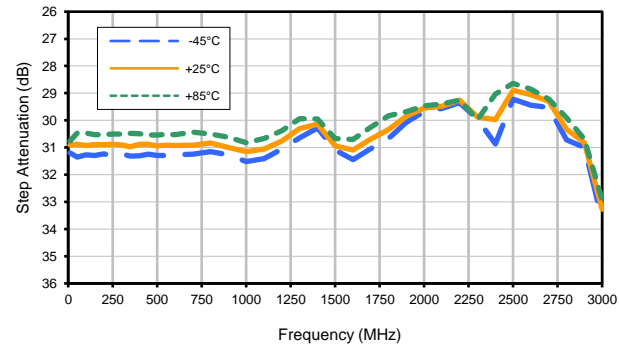
Step Attenuation (8dB)



Step Attenuation (16dB)



Step Attenuation (31dB)

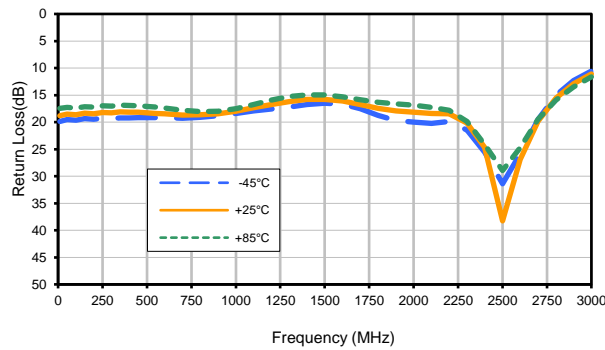


# Digital Step Attenuator

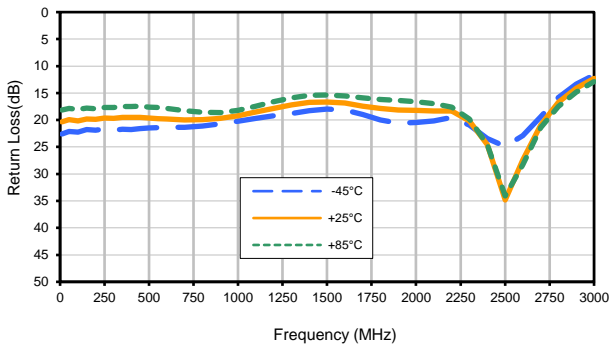
## Typical Performance Curves

# DAT-3175A-SN+

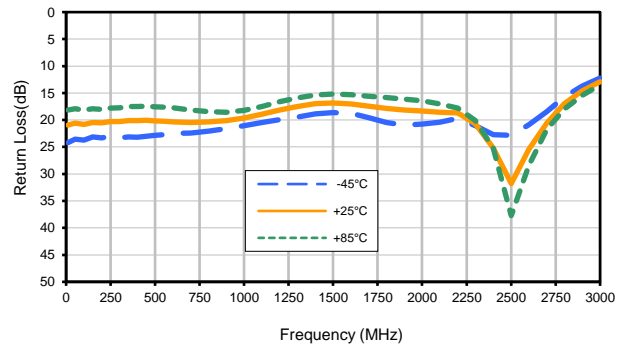
Input Return Loss (0dB)



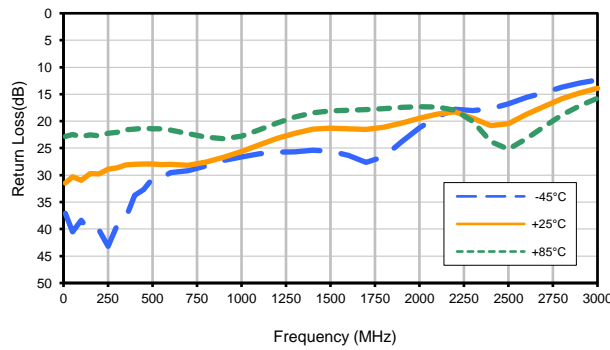
Input Return Loss (1dB)



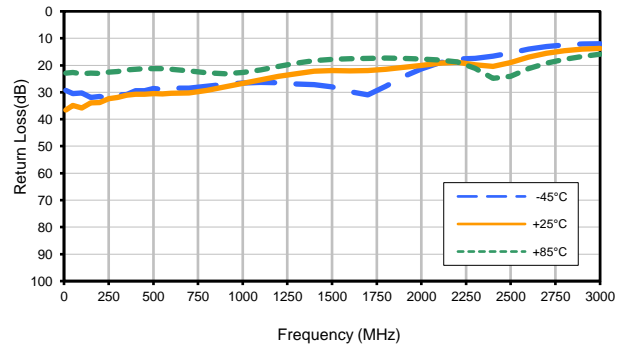
Input Return Loss (2dB)



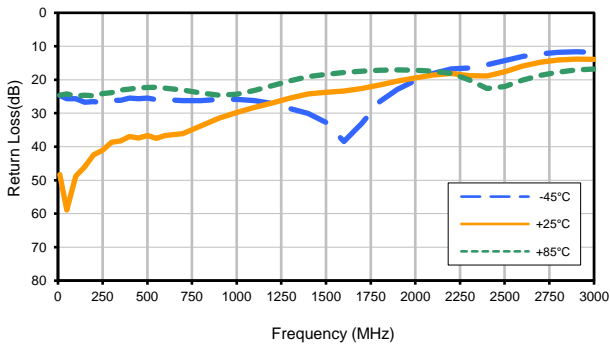
Input Return Loss (4dB)



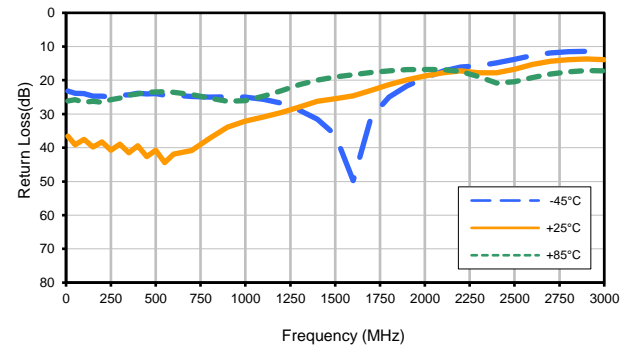
Input Return Loss (8dB)



Input Return Loss (16dB)



Input Return Loss (31dB)

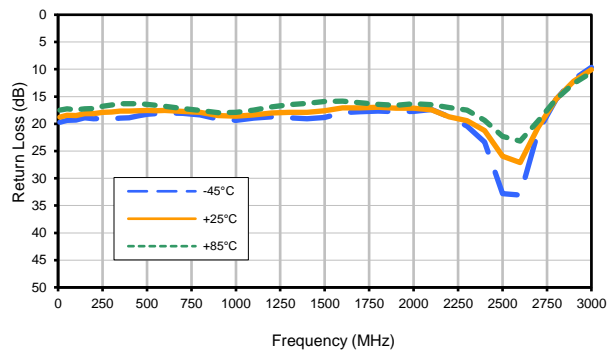


# Digital Step Attenuator

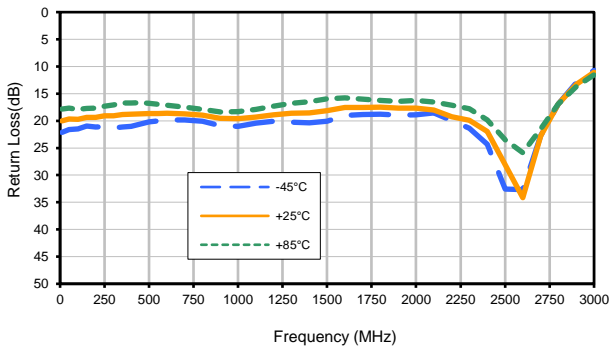
## Typical Performance Curves

# DAT-3175A-SN+

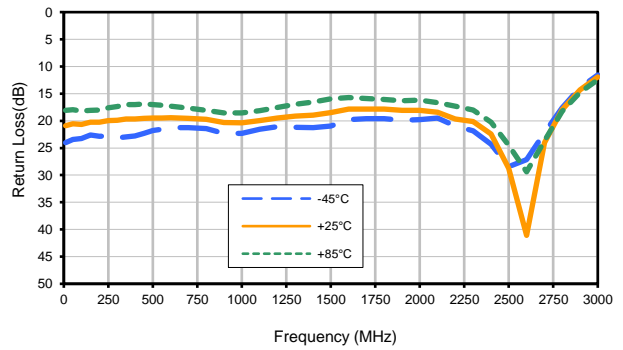
Output Return Loss (0dB)



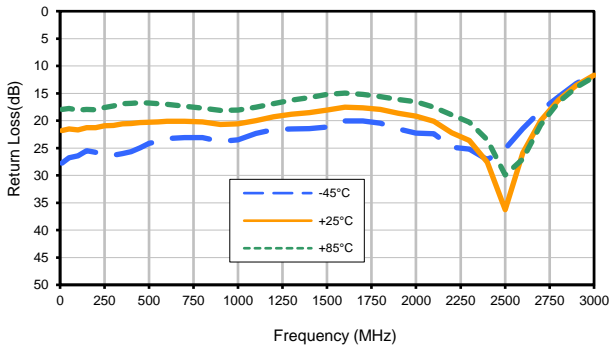
Output Return Loss (1dB)



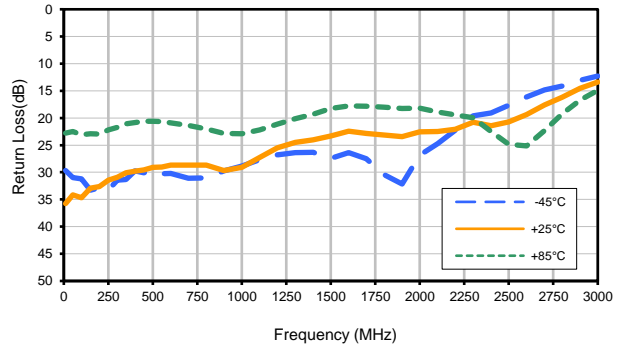
Output Return Loss (2dB)



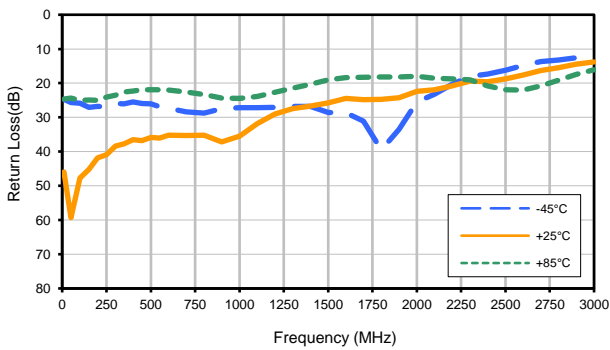
Output Return Loss (4dB)



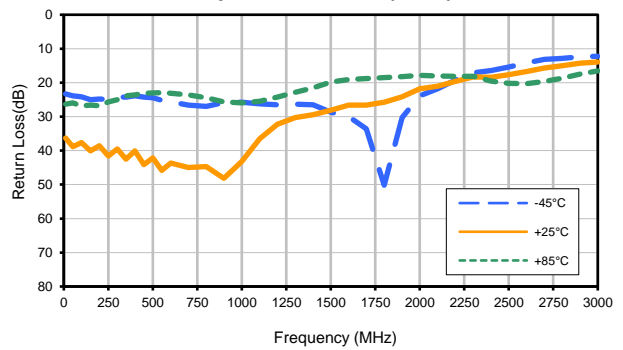
Output Return Loss(8dB)



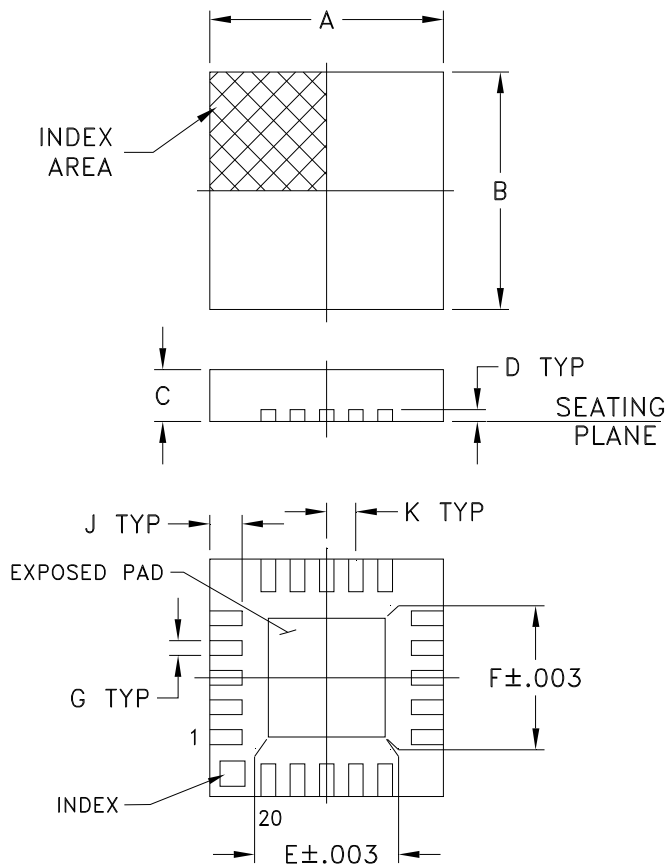
Output Return Loss(16dB)



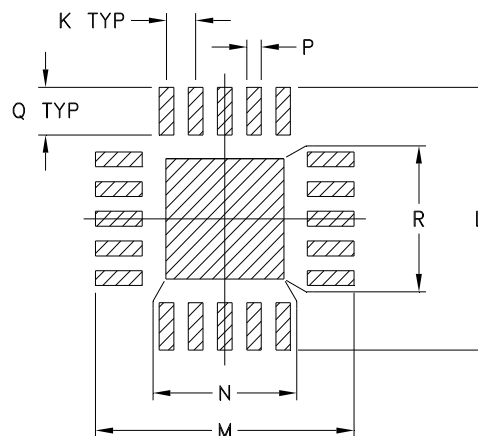
Output Return Loss (31dB)



### Outline Dimensions



### PCB Land Pattern



Suggested Layout,  
Tolerance to be within  $\pm .002$

CASE #	A	B	C	D	E	F	G	H	J	K
DG983-2	.157 (4.00)	.157 (4.00)	.033 (0.85)	.008 (0.20)	.085 (2.15)	.085 (2.15)	.009 (0.23)	-- --	.022 (0.55)	.020 (0.50)

CASE #	L	M	N	P	Q	R	WT. GRAM
DG983-2	.177 (4.50)	.177 (4.50)	.081 (2.06)	.010 (0.25)	.032 (0.81)	.081 (2.06)	.04

Dimensions are in inches (mm). Tolerances: 2 Pl.  $\pm .01$ ; 3 Pl.  $\pm .005$

#### Notes:

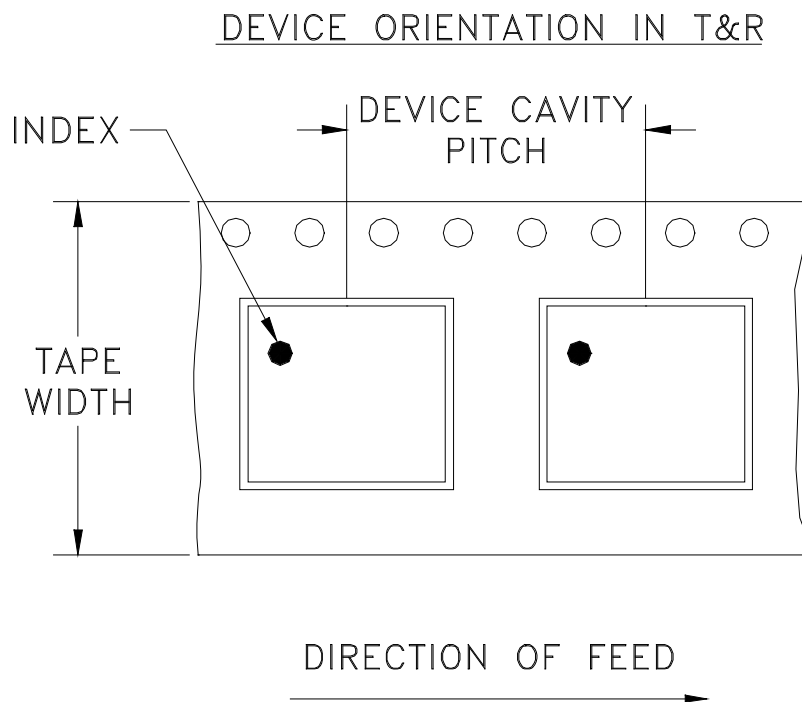
1. Case material: Plastic.
2. Termination finish:

For RoHS Case Styles: 0.2  $\mu$ inches of Gold (Au) over 0.1  $\mu$ inches of Palladium (Pd) over 10  $\mu$ inches of Nickel (Ni). All models, (+) suffix.

For RoHS-5 Case Styles: Tin-Lead plate. All models, no (+) suffix.



# Tape & Reel Packaging TR-F87



Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel	
12	8	7	Small quantity standards (see note)	20
				50
				100
				200
				500
		1000		
		13	Standard	3000

**Note : Please Consult individual model data sheet to determine device per reel availability**

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

Go to: [www.minicircuits.com/pages/pdfs/tape.pdf](http://www.minicircuits.com/pages/pdfs/tape.pdf)

**Mini-Circuits®**

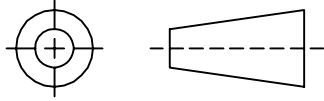
INTERNET <http://www.minicircuits.com>

P.O. Box 350166, Brooklyn, New York 11235-0003 (718) 934-4500 Fax (718) 332-4661

Distribution Centers NORTH AMERICA 800-654-7949 • 417-335-5935 • Fax 417-335-5945 • EUROPE 44-1252-832600 • Fax 44-1252-837010

Mini-Circuits ISO 9001 & ISO 14001 Certified

THIRD ANGLE PROJECTION

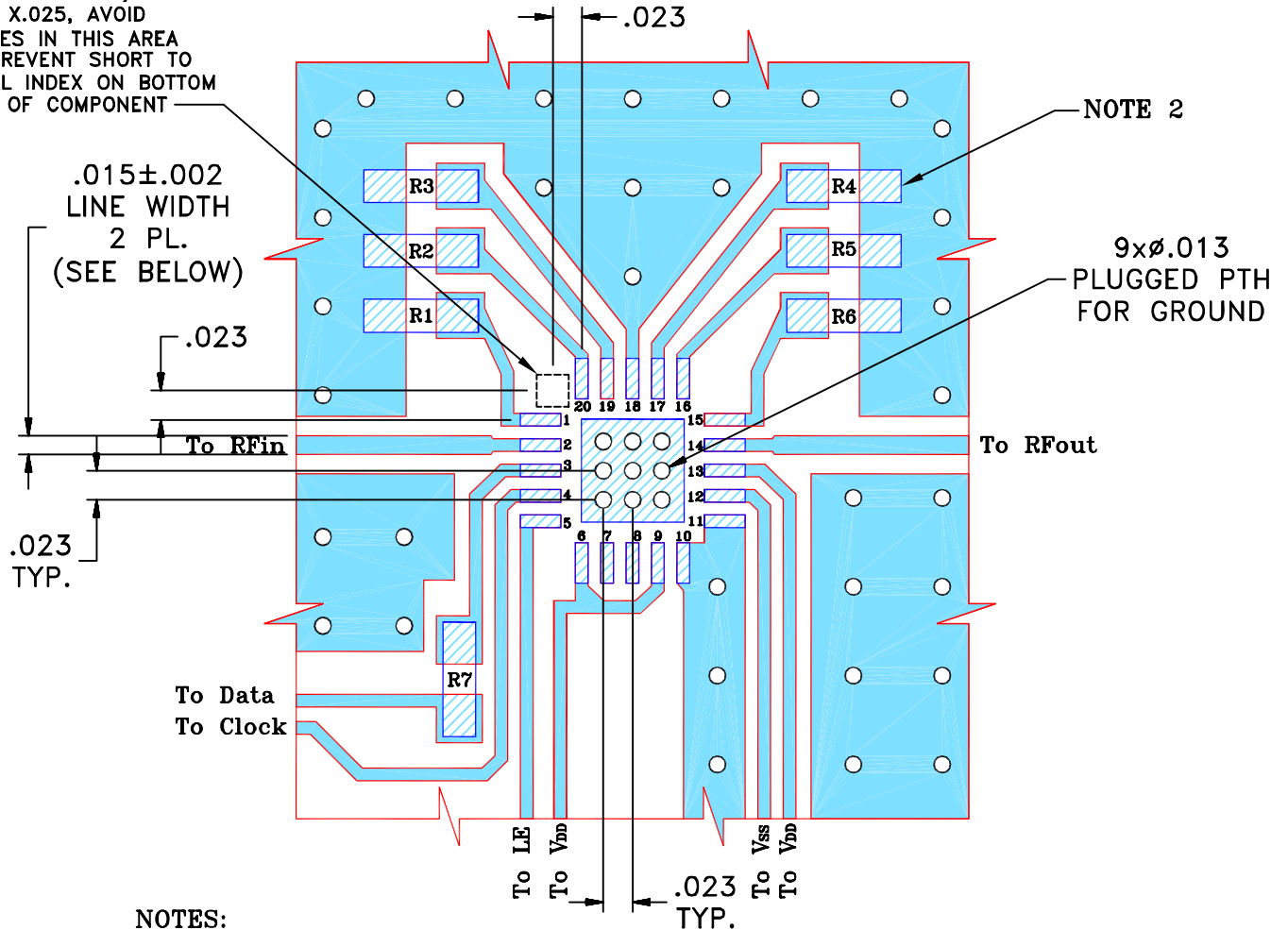


REVISIONS

REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
OR	M97254	NEW RELEASE (FROM RAVON)	03/05	DK	HH
A	M102713	MODIFIED HATCH, NOTES & ADDED "...WITH SMOBC"	01/06	GT	IL
B	M103510	ADD R7 & CHANGE LOCATION DESIGNATORS	07/09	EM	KN
B	R63339	ADD R7 & CHANGE LOCATION DESIGNATORS	07/09	EM	KN

**SUGGESTED MOUNTING CONFIGURATION  
FOR DG983-1 CASE STYLE, qj PIN CONNECTIONS, 75 Ω.**

KEEP-OUT ZONE,  
.025 X.025, AVOID  
TRACES IN THIS AREA  
TO PREVENT SHORT TO  
METAL INDEX ON BOTTOM  
SIDE OF COMPONENT



NOTES:

- TRACE WIDTH IS SHOWN FOR FR4 WITH DIELECTRIC THICKNESS .025"±.002". COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.
- 0603 SIZE CHIP FOOT PRINTS SHOWN FOR REFERENCE, VALUES OF RESISTORS WILL VARY BASED ON APPLICATION.
- BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.



DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)



DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

UNLESS OTHERWISE SPECIFIED

DIMENSIONS ARE IN INCHES  
TOLERANCES ON:  
2 PL DECIMALS ±  
3 PL DECIMALS ± .005  
ANGLES ±  
FRACTIONS ±

	INITIALS	DATE
DRAWN	DK (RAVON)	08 MAR 05
CHECKED	RZ (RAVON)	08 MAR 05
APPROVED	HH (RAVON)	08 MAR 05



**Mini-Circuits®** 13 Neptune Avenue  
Brooklyn NY 11235

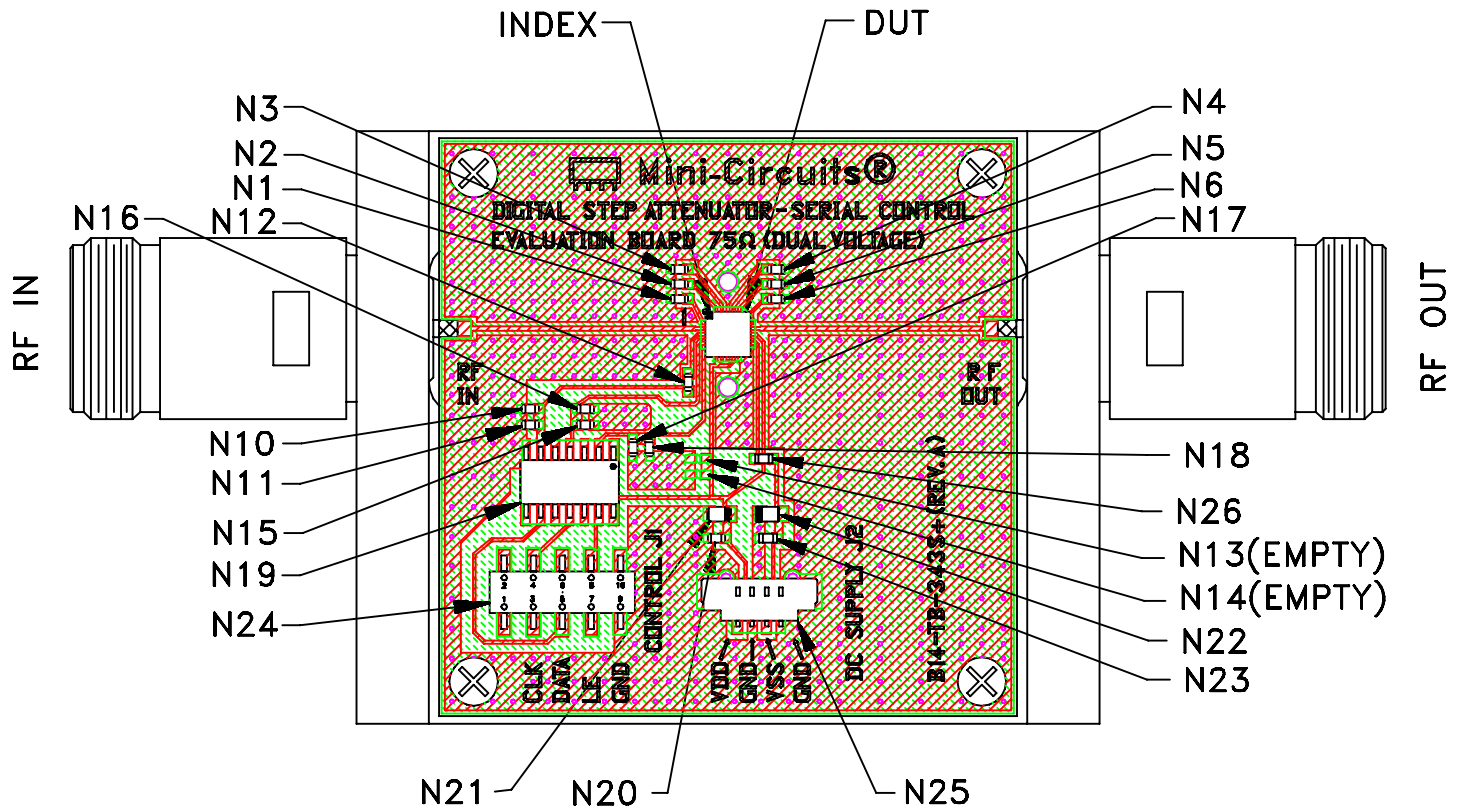
**PL, qj, DG983-1  
TB-343 (75 Ω)**

THIS DOCUMENT AND ITS CONTENTS ARE THE PROPERTY OF MINI-CIRCUITS. EXCEPT FOR USE EXPRESSLY GRANTED, IN WRITING, TO ITS VENDORS, VENDEE AND THE UNITED STATES GOVERNMENT, MINI-CIRCUITS RESERVES ALL PROPRIETARY DESIGN, USE, MANUFACTURING AND REPRODUCTION RIGHTS THERETO. THESE CONTENTS SHALL NOT BE USED, DUPLICATED OR DISCLOSED TO ANY OUTSIDE PARTY, IN WHOLE OR IN PART, WITHOUT WRITTEN PERMISSION OF MINI-CIRCUITS.

SIZE A	CODE IDENT 15542	DRAWING NO: 98-PL-194	REV: B
FILE: 98PL194	SCALE: 7:1	SHEET: 1 OF 1	




# Evaluation Board and Circuit



TB-343

## Notes:

1. N-Type Female connectors.
2. PCB Material: FR4 Grade IT 180TC (ITEQ Corporation) or equivalent, Dielectric Constant=4.7, Thickness=.025 inch.

 **Mini-Circuits®**



All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

<b>Specification</b>	<b>Test/Inspection Condition</b>	<b>Reference/Spec</b>
Operating Temperature	-40° to 85° C or -40° to 105° C Ambient Environment	Refer to Individual Model Data Sheet
Storage Temperature	-55° to 100° C or -65° to 150° C Ambient Environment	Refer to Individual Model Data Sheet
Temperature Humidity Bias	85°C, 85% RH, 96 hours	JESD22-A101B
Thermal Shock	-55° to 100°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Solder Reflow Heat	Pb-Free Process: 260°C peak	J-STD-020, Table 4-1, 4-2 and 5-2; Figure 5-1
Solderability	10X magnification, 95% coverage	JESD22-B102, Method 1: Dip and Look Test
Marking Resistance to Solvents	Laser marked, visual observation	Mini-Circuits D4-Q4T0-04