Ceramic

LTCC Bandpass Filter

BFCV-2895+

50Ω 2220 to 3570 MHz

Generic photo used for illustration purposes only

CASE STYLE: JV1210C

The Big Deal

- Small size 3.2mm x 2.5mm
- Wide passband (2220-3570 MHz)
- Low Insertion Loss (1.8 dB typical)
- Wide stopband rejection up to 7 GHz

Product Overview

The BFCV-2895+ LTCC Band Pass Filter is constructed with multiple layers in order to achieve a miniature size and high repeatability of performance. Wrap-around terminations minimize variations in performance due to parasitics. These units offer low insertion loss and very good wide band rejection.

Key Features

Feature	Advantages
Small Size (3.20mm x2.5 mm)	Allows for high layout density of circuit boards, while minimizing the effects of parasitics.
Wrap around termination	Provides excellent solderability and easy visual inspection capability.
Wide bandwidth	Enables high data rate in communication systems.
LTCC construction	Provides a rugged package that is well suited for tough environments including high humidity and high temperature extremes.

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Bandpass Filter

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Generic photo used for illustration purposes only
CASE STYLE: JV1210C

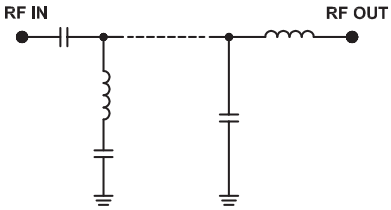
Features

- Small size
- Temperature stable
- Hermetically sealed
- LTCC construction

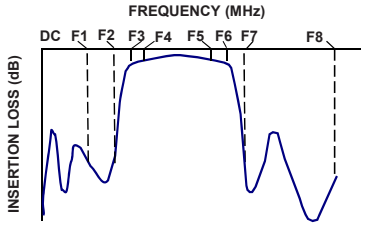
Applications

- Software defined radio
- WLAN
- Cellular network
- Satellite television broadcast

Functional Schematic



Typical Frequency Response



Electrical Specifications[1,2] at 25°C

Parameter		F#	Frequency (MHz)	Min.	Typ.	Max.	Unit
Pass Band	Center Frequency	—	—	—	2895	—	MHz
	Insertion Loss	F3-F5	2220-3570	—	1.8	—	dB
		F4-F5	2450-3570	—	1.8	4.0	dB
	VSWR	F3-F5	2220-3570	—	2.3	—	:1
Stop Band, Lower	Insertion Loss	DC-F1	DC-1680	15	17	—	dB
		F2	1785	—	17	—	dB
	VSWR	DC-F1	DC-1680	—	20	—	:1
Stop Band, Upper	Insertion Loss	F6	4440	—	16	—	dB
		F7-F8	5000-7000	14	20	—	dB
	VSWR	F7-F8	5000-7000	—	20	—	:1

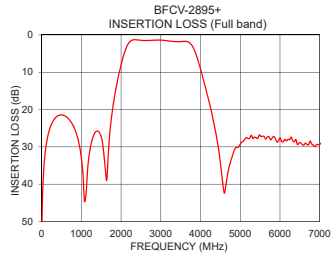
1. Measured on Mini-Circuits Characterization Test Board TB-946+
2. This filter is not intended for use as a DC Blocking circuit element. In Application where DC voltage is present at either input or output ports, blocking capacitors are required at the corresponding RF port.

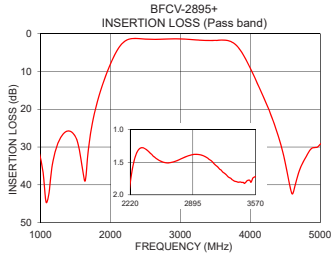
Maximum Ratings	
Operating Temperature	-55°C to 100°C
Storage Temperature	-55°C to 100°C
RF Power Input*	5 W max @ +25°C

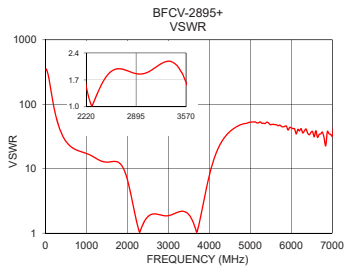
*Passband rating, derate linearly to 0.25W at 100°C ambient
Permanent damage may occur if any of these limits are exceeded.

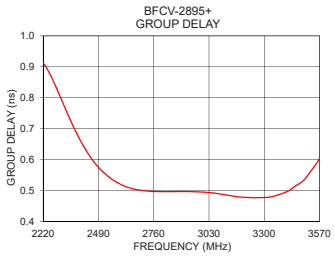
Typical Performance Data at 25°C

Frequency (MHz)	Insertion Loss (dB)	VSWR (:1)	Frequency (MHz)	Group Delay (nsec)
10	50.41	339.97	2220	0.91
1680	31.42	12.98	2240	0.89
1785	20.03	12.48	2300	0.81
1850	15.66	11.40	2400	0.66
2000	8.04	6.68	2500	0.57
2150	2.98	2.48	2600	0.52
2220	1.83	1.54	2700	0.50
2450	1.38	1.66	2800	0.50
2895	1.38	1.86	2895	0.50
3570	1.72	1.64	2900	0.50
3600	1.71	1.49	3000	0.50
3800	3.08	1.98	3050	0.49
4000	9.16	7.93	3100	0.49
4100	13.19	13.36	3150	0.48
4280	20.80	24.88	3200	0.48
4440	29.62	34.62	3250	0.48
4600	42.43	42.30	3300	0.48
5000	29.26	52.61	3400	0.49
6000	27.59	43.06	3500	0.54
7000	29.35	32.15	3570	0.60









+RoHS Compliant
The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

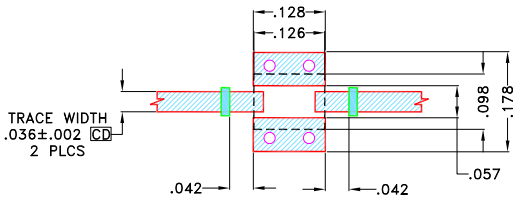
Notes
A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
C. The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the Standard Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/MCLStore/terms.jsp

Pad Connections

RF IN	1
RF OUT	3
GROUND	2,4

Product Marking: HL

Demo Board MCL P/N: TB-946+
Suggested PCB Layout (PL-502)



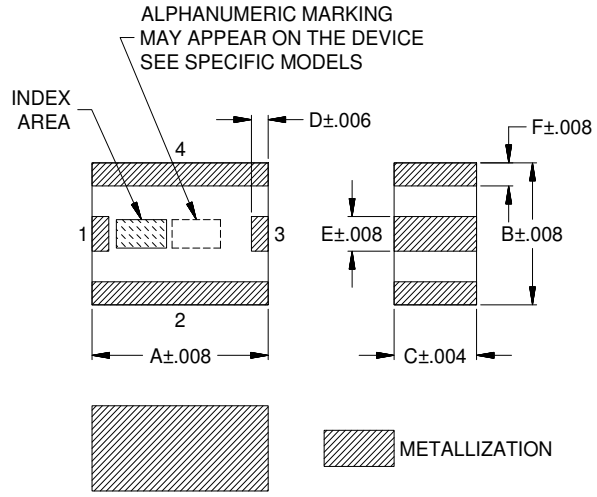
NOTES:

1. TRACE WIDTH & SPACE WIDTH IS SHOWN FOR ROGERS (RO4350B) WITH DIELECTRIC THICKNESS .0166"±.0015". COPPER 1/2 Oz. EACH SIDE
   FOR OTHER MATERIALS TRACE WIDTH & SPACE WIDTH MAY NEED TO BE MODIFIED.
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.

DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)

DENOTES COPPER LAND PATTERN FREE OF SOLDERMASK

Outline Drawing



Outline Dimensions (inch / mm)

A	B	C	D	E	F	Wt.
.126	**.098**	**.059**	**.012**	**.024**	**.016**	grams
3.2	2.5	1.5	.3	.6	.4	.03

Note: Please refer to case style drawing for details

Surface Mount Band Pass Filter BFCV-2895+

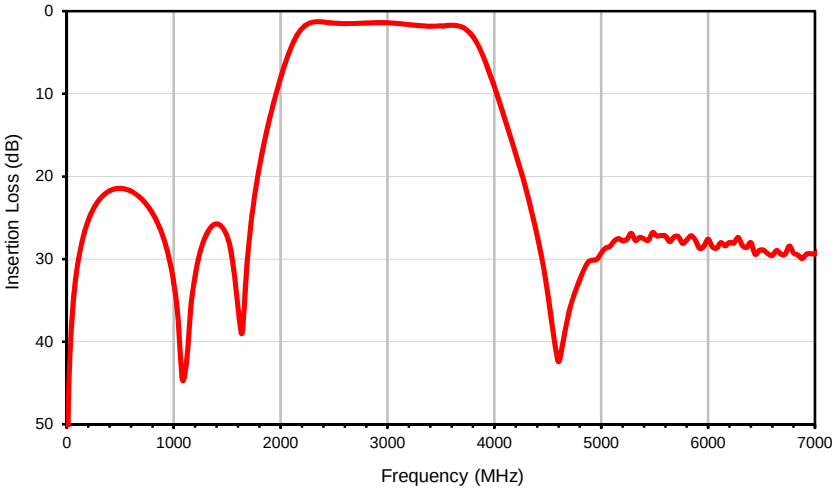
Typical Performance Data

FREQUENCY (MHz)	INSERTION LOSS (dB)	RETURN LOSS (dB)
10	50.41	0.04
20	44.45	0.04
40	38.46	0.04
120	29.19	0.03
160	26.94	0.03
200	25.32	0.03
400	21.73	0.06
600	21.78	0.09
800	24.41	0.12
1000	32.90	0.16
1200	31.97	0.21
1320	26.64	0.26
1400	25.74	0.29
1600	35.66	0.36
1680	31.42	0.41
1720	25.93	0.45
1750	22.90	0.49
1785	20.03	0.55
1800	18.93	0.58
1900	12.83	0.95
2000	8.04	1.87
2140	3.21	5.90
2220	1.83	11.50
2450	1.38	12.06
2895	1.38	12.44
3570	1.72	10.77
3800	3.08	7.52
4000	9.16	1.92
4270	20.26	0.69
4350	24.31	0.62
4440	29.62	0.51
4480	32.54	0.52
5000	29.26	0.33
5200	27.80	0.33
5400	27.55	0.23
5600	27.20	0.24
5800	27.69	0.21
6000	27.59	0.16
6040	28.54	0.17
6080	28.65	0.17
6120	28.01	0.19
6160	28.40	0.16
6200	27.99	0.16
6240	28.07	0.16
6280	27.39	0.18
6320	28.34	0.19
6360	28.59	0.17
6400	28.01	0.19
6440	29.42	0.21
6480	28.94	0.21
6520	28.92	0.22
6560	29.37	0.25
6600	29.56	0.23
6640	28.97	0.27
6680	29.39	0.28
6720	29.42	0.29
6760	28.42	0.32
6800	29.28	0.35
6840	29.52	0.40
7000	29.35	0.62

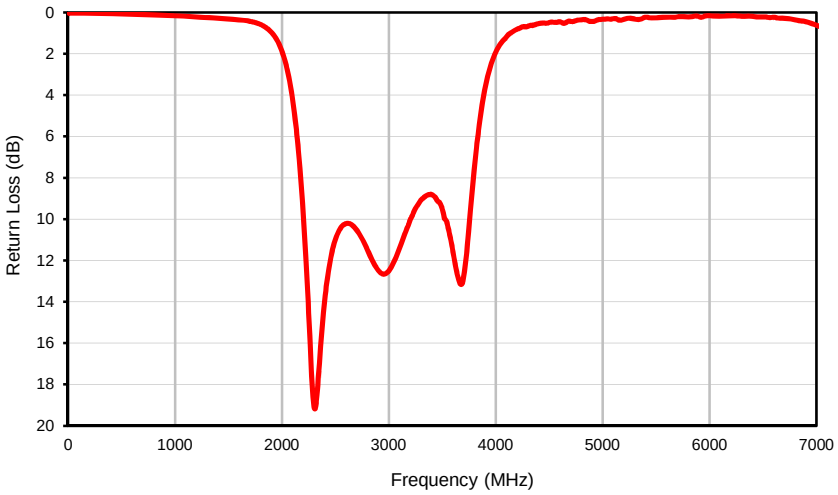
FREQUENCY (MHz)	GROUP DELAY (nsec)
2220	0.91
2240	0.89
2260	0.87
2280	0.84
2300	0.81
2320	0.78
2340	0.75
2360	0.72
2380	0.69
2400	0.66
2420	0.64
2440	0.62
2460	0.60
2480	0.58
2500	0.57
2520	0.56
2540	0.54
2560	0.54
2580	0.53
2600	0.52
2620	0.51
2640	0.51
2660	0.51
2680	0.50
2700	0.50
2720	0.50
2740	0.50
2760	0.50
2780	0.50
2800	0.50
2820	0.50
2840	0.50
2860	0.50
2895	0.50
2900	0.50
2920	0.50
2940	0.50
2980	0.50
3000	0.50
3060	0.49
3100	0.49
3140	0.48
3180	0.48
3200	0.48
3240	0.48
3260	0.48
3280	0.48
3300	0.48
3320	0.48
3340	0.48
3380	0.49
3400	0.49
3420	0.50
3450	0.51
3480	0.53
3500	0.54
3520	0.56
3540	0.57
3550	0.58
3570	0.60

Typical Performance Curves

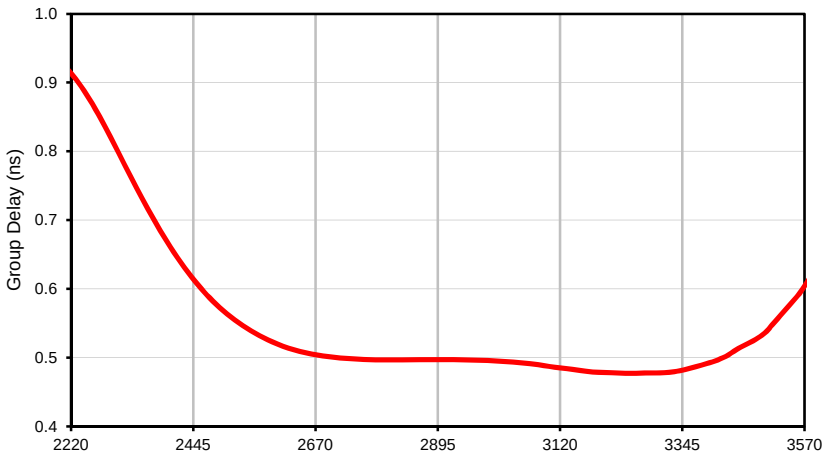
Insertion Loss



Return Loss



Group Delay

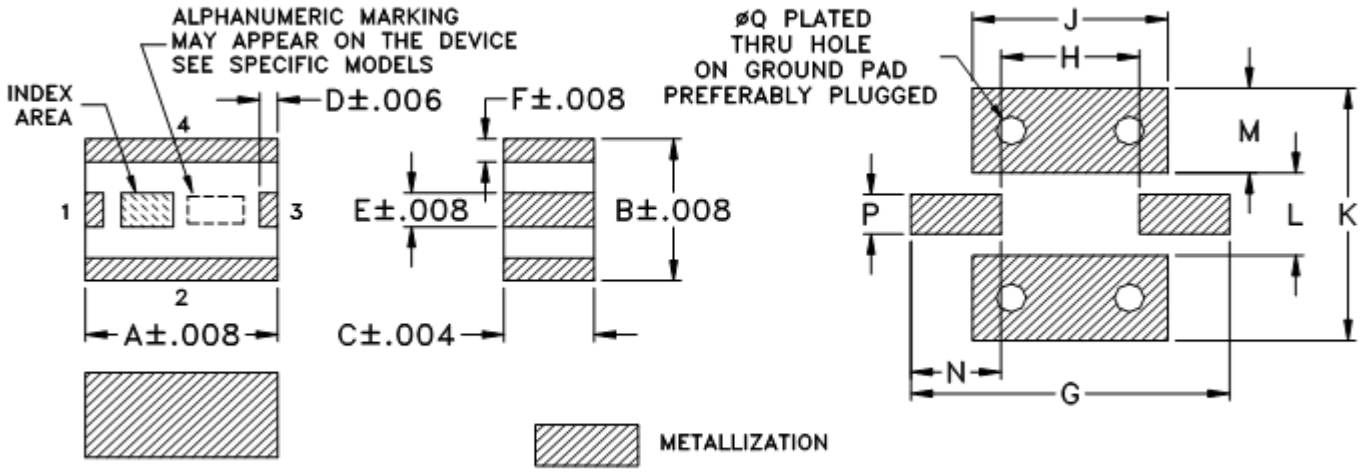


Case Style JV

Outline Dimensions

JV1210C

PCB Land Pattern



Suggested Layout,
Tolerance to be within ±.002

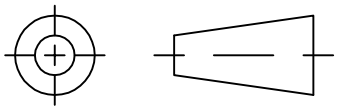
CASE #	A	B	C	D	E	F	G	H	J	K	L	M	N	P	Q	WT. GRAM
JV1210C	.126 (3.2)	.098 (2.5)	.059 (1.5)	.012 (.3)	.024 (.6)	.016 (.4)	.209 (5.3)	.091 (2.3)	.128 (3.25)	.175 (4.45)	.057 (1.45)	.059 (1.5)	.059 (1.5)	.028 (.7)	.020 (.5)	.03

Dimensions are in inches (mm). Tolerances: 2 Pl. ± .01; 3 Pl. ± .005

Notes:

1. Open style, ceramic base.
2. Termination finish: **as shown below or indicated on Data Sheet.**
   For RoHS Case Styles: Tin plate over Nickel plate. All models, (+) suffix.
   For RoHS-5 Case Styles: Tin-Lead plate. All models, no (+) suffix.
3. Pad tolerance is non-cumulative. Minimum spacing between each pad is .004.

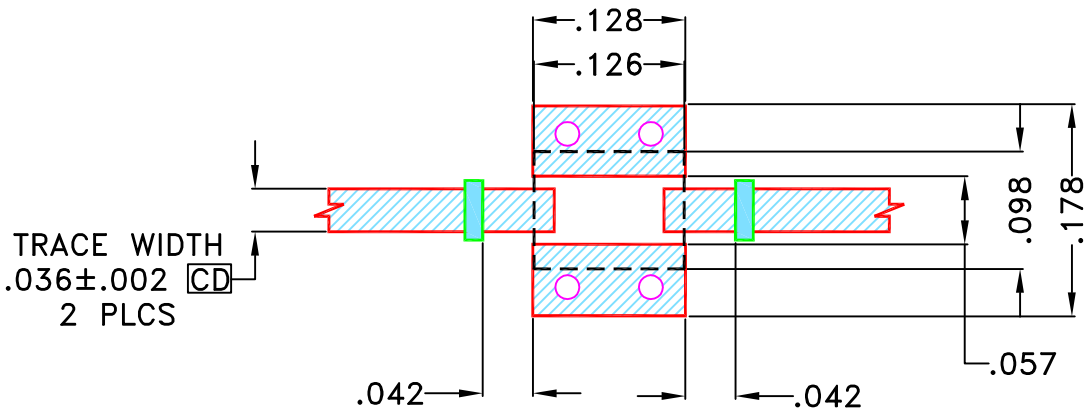
THIRD ANGLE PROJECTION



REVISIONS

REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
OR	M160679	NEW RELEASE	MAR 17	TM	MD

SUGGESTED MOUNTING CONFIGURATION FOR JV1210C CASE STYLE



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(SOLDER MASK OVER BARE COPPER)

DENOTES COPPER LAND PATTERN FREE OF SOLDERMASK

UNLESS OTHERWISE SPECIFIED		INITIALS	DATE
DIMENSIONS ARE IN INCHES	DRAWN	TM	8 MAR 17
TOLERANCES ON:	CHECKED	MD	8 MAR 17
2 PL DECIMALS ± 3 PL DECIMALS ± .005"	APPROVED	RV	8 MAR 17
ANGLES ±			
FRACTIONS ±			

ASHEETA1.DWG REV:A DATE:01/12/95

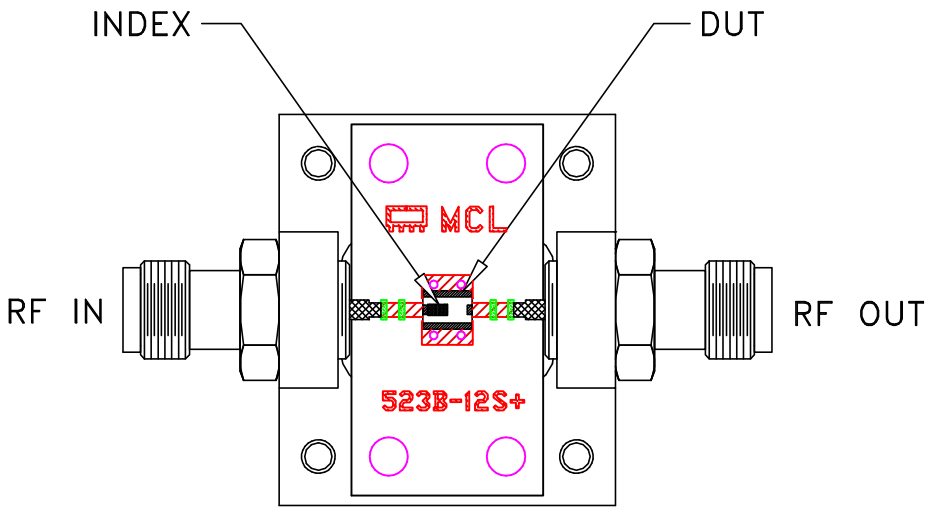
Mini-Circuits®
13 Neptune Avenue
Brooklyn NY 11235

PL, JV1210C, BFCV-4085+, BFCV-2895+, BFCV-3350+, BFCV-2610+, TB-946+
50 OHM

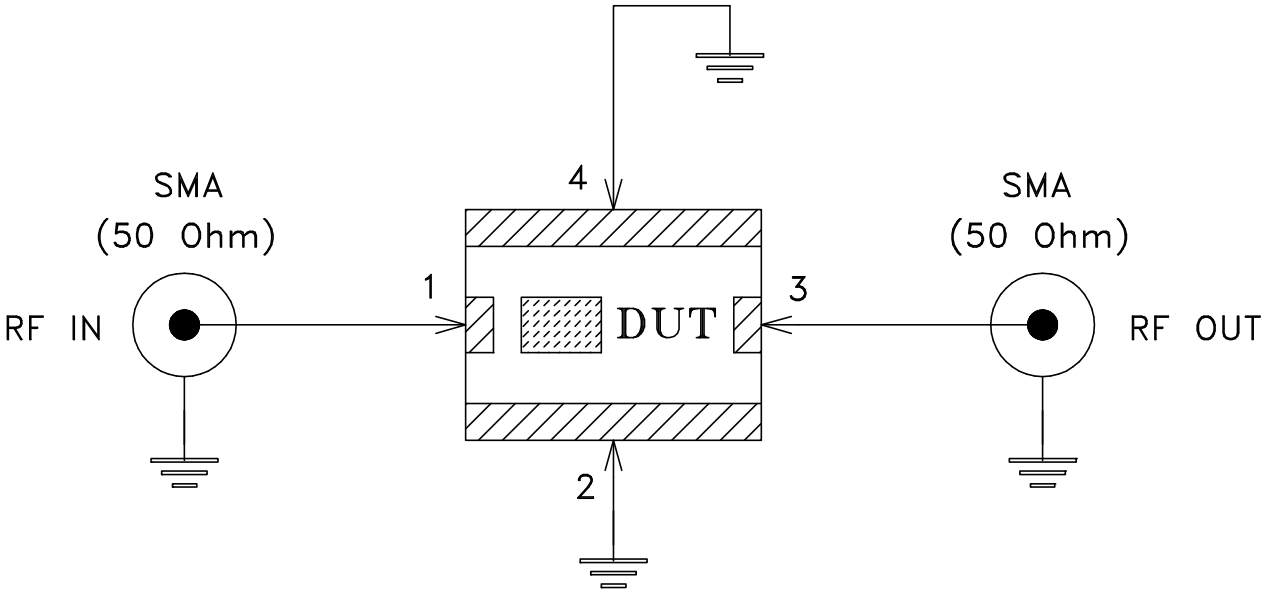
SIZE	CODE IDENT	DRAWING NO:	REV:
A	15542	98-PL-502	OR

FILE:	SCALE:	SHEET:
98PL502	6:1	1 OF 1

Evaluation Board and Circuit



TB-946+



Schematic Diagram

Notes:

1. 50 Ohm SMA Female connectors.
2. PCB Material: RO4350 or equivalent,
   Dielectric Constant=3.48, Thickness=.0166 inch.

Mini-Circuits®

Mini-Circuits

Environmental Specifications ENV02T1

All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-40° to 85°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Thermal Shock	-55° to 100°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Solder Reflow Heat	Sn-Pb Eutetic Process: 225°C peak Pb-Free Process 245° - 250°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, 95% Coverage
Vibration (High Frequency)	20g peak, 10-2000 Hz, 12 times in each of three perpendicular directions (total 36)	MIL-STD-202, Method 204, Condition D
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A
Marking Resistance to Solvents	Isopropyl alcohol + mineral spirits at 25°C; terpene defluxer at 25°C; distilled water + proylene glycol monomethyl ether + monoethanolamine at 63°C to 70°C	MIL-STD-202, Method 215