



LTCC SURFACE MOUNT

Bandpass Filter & Balun

BBFCG2-672+

50Ω 6375 to 7175 MHz 1:2 Ratio

THE BIG DEAL

- Compact Design includes Balun and Filter in One Package
- Low Passband Insertion Loss, Typ. 1.9 dB
- Outstanding CMRR, Typ. 29 dB
- Small 0805 Surface Mount Footprint

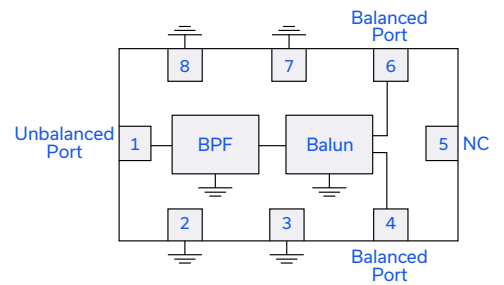


Generic photo used for illustration purposes only

APPLICATIONS

- 5G Sub-6 GHz, MIMO Wireless Infrastructure Systems
- Satellite Communications

FUNCTIONAL DIAGRAM



PRODUCT OVERVIEW

Mini-Circuits' BBFCG2-672+ is a tiny ceramic RF balun filter with an impedance ratio of 1:2, covering a variety of wireless communications applications from 6375 to 7175 MHz. This model provides low insertion loss, low phase unbalance (relative to 180°), and low amplitude unbalance. Fabricated using LTCC technology, the unit comes housed in a tiny, rugged ceramic package (0.079" x 0.049" x 0.037") suitable for harsh operating environments.

KEY FEATURES

Features	Advantages
Compact Design	Integrates filter and balun in one small package.
Small, 0805 Surface Mount Footprint	Saves space in dense circuit board layouts and minimizes the effects of parasitics.
LTCC Construction	Provides repeatable performance in a rugged, ceramic package, well suited for tough environments with high humidity and temperature extremes.



ELECTRICAL SPECIFICATIONS^{1,2} AT +25°C

Parameter	F#	Frequency (MHz)	Min.	Typ.	Max.	Units	
Impedance Ratio				1:2		—	
Passband	Center Frequency ³	—	—	6700	—	MHz	
	Average Insertion Loss ⁴	F3-F4	6375 - 7175	—	1.9	3	dB
	Return Loss - Unbalanced Port	F3-F4	6375 - 7175	10	13	—	dB
	Return Loss - Balanced Port ⁴	F3-F4	6375 - 7175	10	11.8	—	dB
Stopband, Lower Rejection ⁴	DC-F1	10 - 2400	24	39	—	dB	
	F1-F2	2400 - 5400	23	33	—	dB	
Stopband, Upper Rejection ⁴	F5-F6	8900 - 17800	25	35	—	dB	
	F6-F7	17800 - 26000	20	27	—	dB	
Amplitude Unbalance (±)	F3-F4	6375 - 7175	-1.1	±0.72	+1.1	dB	
Phase Unbalance (Relative to 180°)	F3-F4	6375 - 7175	-10	±6	+10	Deg.	
CMRR	F3-F4	6375 - 7175	20	28		dB	

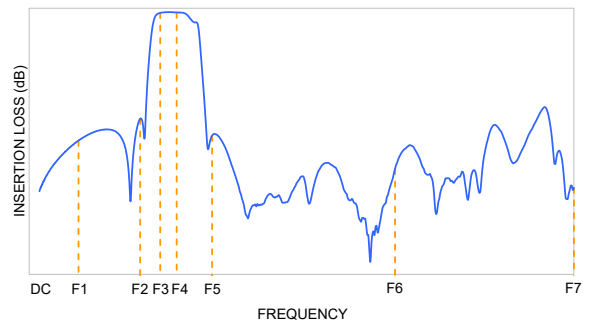
1. Tested on Evaluation Board P/N TB-BBFCG2-672+ with connectors and feedline loss compensated.
2. This component should not be used as a DC Block. In applications where DC voltage and/or current is present at either the input or output ports, external DC blocking capacitors are required.
3. Typical variation ±3%.
4. Measured in mixed mode.

ABSOLUTE MAXIMUM RATINGS⁶

Parameter	Ratings
Operating Temperature	-55°C to +125°C
Storage Temperature	-55°C to +125°C
Input Power ⁷	2 W

6. Permanent damage may occur if any of these limits are exceeded.
7. Power rating applies only to signals within the passband at +25°C. Power rating above +25°C operating temperature decreases linearly to 0.5 W at +125°C.

TYPICAL FREQUENCY RESPONSE AT +25°C



DC RESISTANCE PORT-PORT

Function	Pad Number
Unbalanced Port to Ground	DC Short
Unbalanced Port to Balanced Port	DC Open
Balanced Port to Ground	DC Open
Balanced Port to Balanced Port	DC Short



LTCC SURFACE MOUNT

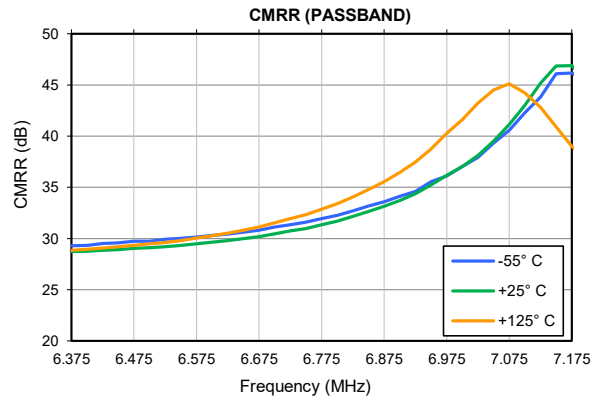
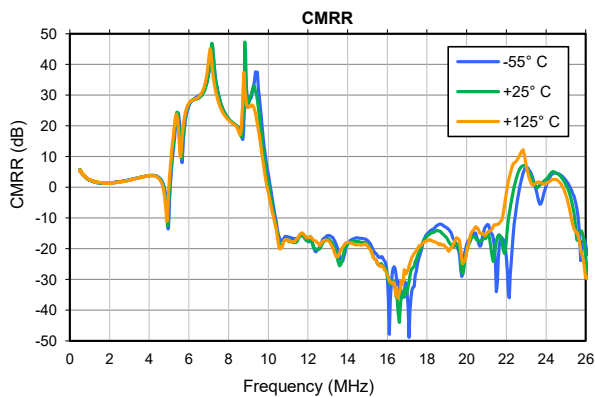
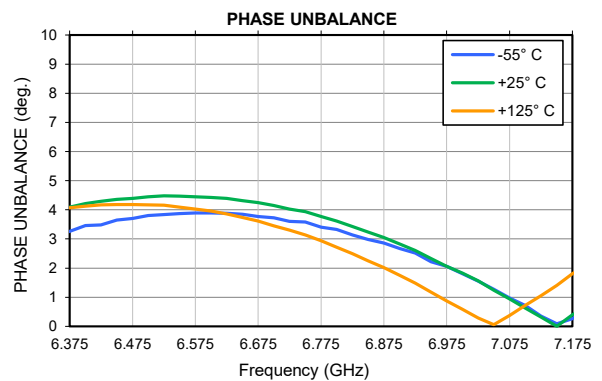
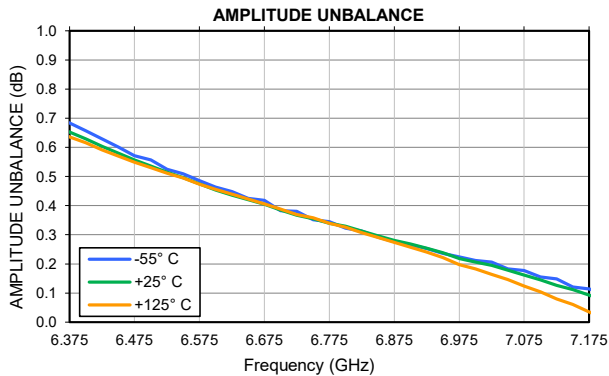
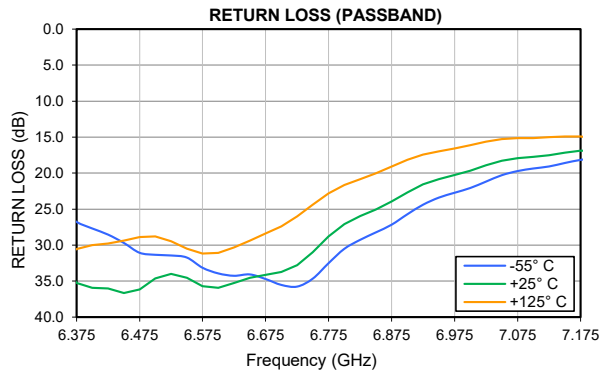
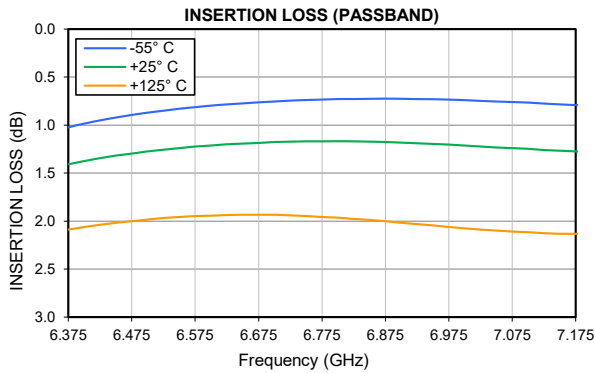
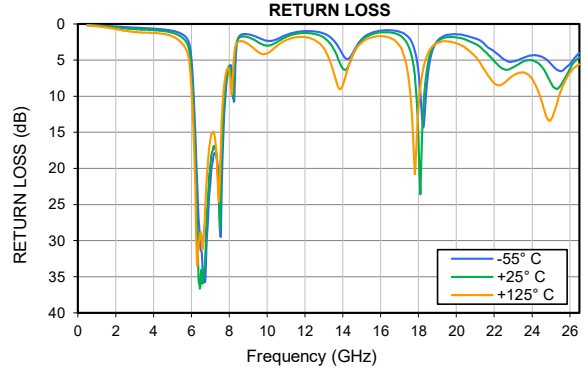
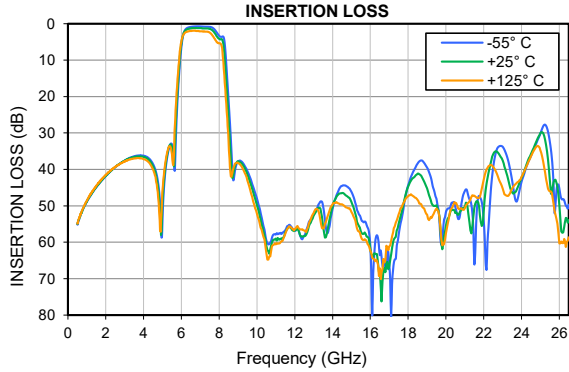
Bandpass Filter & Balun

BBFCG2-672+

Mini-Circuits

50Ω 6375 to 7175 MHz 1:2 Ratio

TYPICAL PERFORMANCE GRAPHS





FUNCTIONAL DIAGRAM

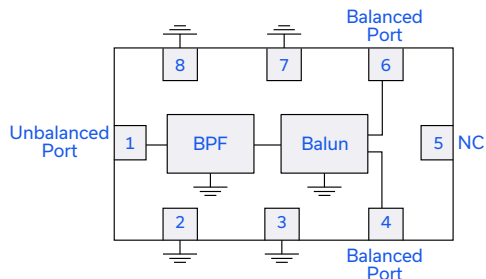
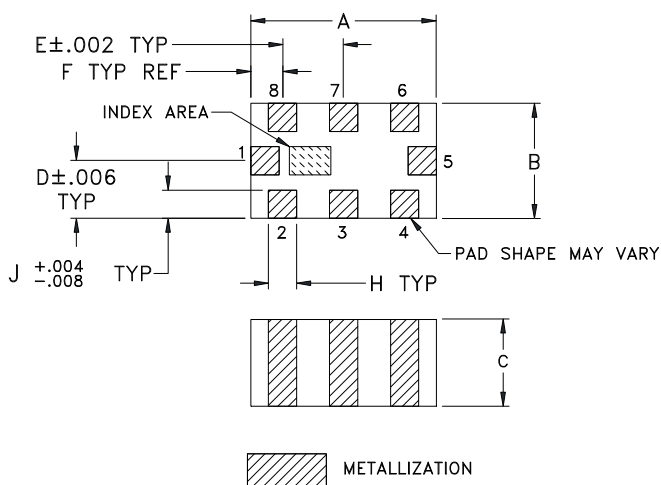


Figure 1. BBFCG2-672+ Functional Diagram

PAD DESCRIPTION

Function	Pad Number	Description
Unbalanced Port	1	Unbalanced Input Port
Balanced Ports	4, 6	Balanced Output Ports
Ground	2, 3, 7, 8	Connects to Ground on PCB. (See drawing PL-724)
NC	5	No connection, not used internally. See drawing PL-724 for connection to PCB.

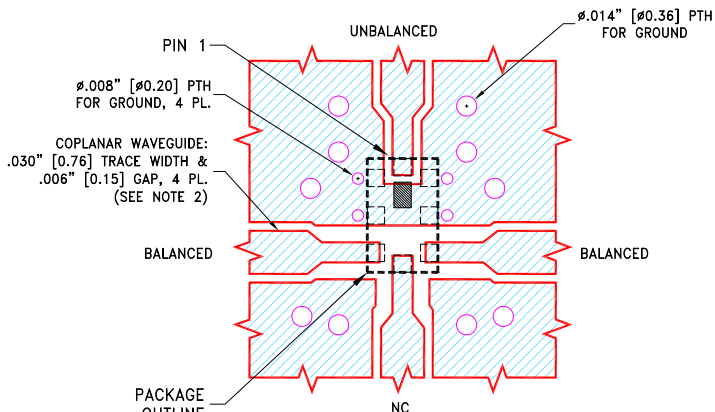
CASE STYLE DRAWING



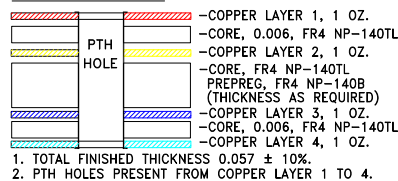
OUTLINE DIMENSIONS (Inch/mm)

A	B	C	D	E	F	G	H	J	wt
.079	.049	.037	.025	.026	.014	.110	.012	.010	grams
2.00	1.25	0.95	0.63	0.65	0.35	2.80	0.30	0.25	.008

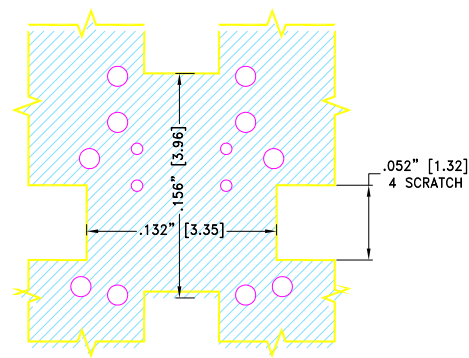
SUGGESTED PCB LAYOUT (PL-724)



STACK-UP DIAGRAM



- TOTAL FINISHED THICKNESS 0.057 ± 10%.
- PTH HOLES PRESENT FROM COPPER LAYER 1 TO 4.



TOP VIEW TO LAYER 2

NOTES:

- PCB IS MULTILAYER PCB, SEE STA K-UP DIAGRAM.
- TRACE WIDTH & GAP PARAMETERS ARE SHOWN FOR FR4 NP-140TL WITH DIELECTRIC THICKNESS .006" ± .0005"; COPPER: 1 OZ. FOR OTHER MATERIALS TRACE WIDTH AND GAP MAY NEED TO BE MODIFIED.
- COPPER LAYERS 3,4 OF THE PCB IS CONTINUOUS GROUND PLANES.

Denotes copper land pattern free of solder mask.

Figure 2. Suggested PCB Layout PL-724

PRODUCT MARKING*: NO MARKING

*Marking may contain other features or characters for internal lot control.



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ADDITIONAL DETAILED INFORMATION IS AVAILABLE ON OUR DASHBOARD. [CLICK HERE](#)

Performance Data & Graphs	Data Graphs S-Parameter (S3P Files) Data Set (.zip file) with connectors and feedline loss compensated.
Case Style	GE0805C-15 Lead Finish: Tin over Nickel Plating
RoHS Status	Compliant
Tape and Reel	F114
Suggested Layout for PCB Design	PL-724
Evaluation Board	TB-BBFCG2-672+ Gerber File
Environmental Rating	ENV06T10

- NOTES
- A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
 - B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
 - C. The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the standard terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/terms/viewterm.html

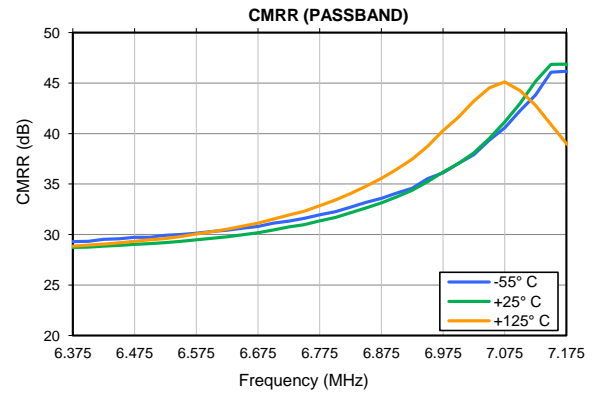
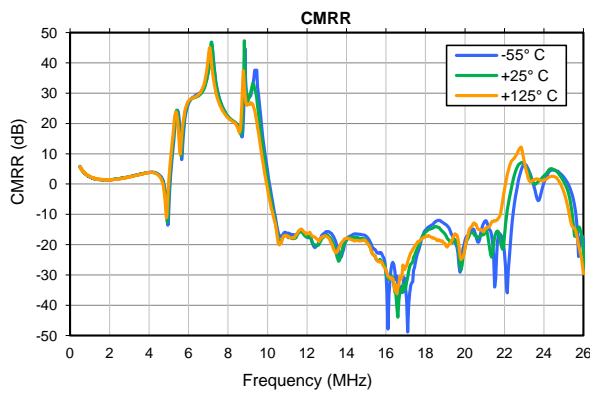
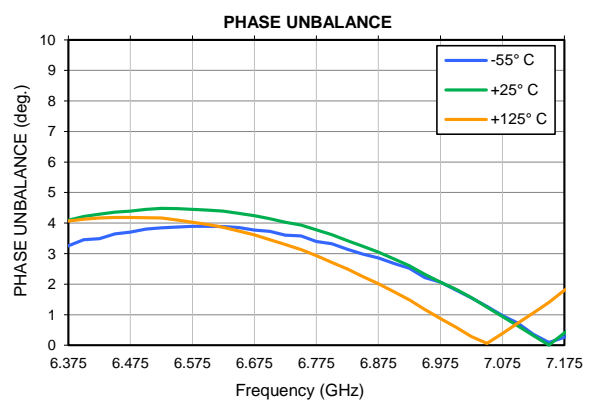
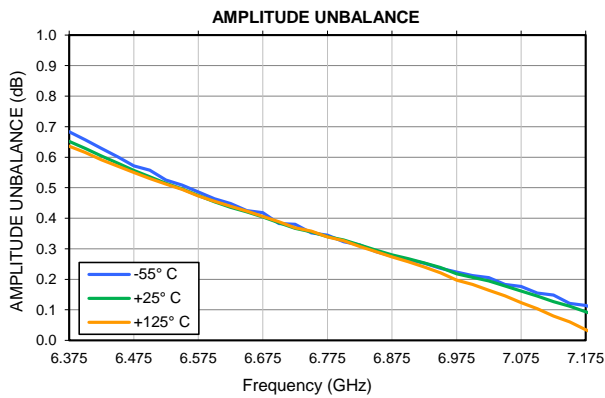
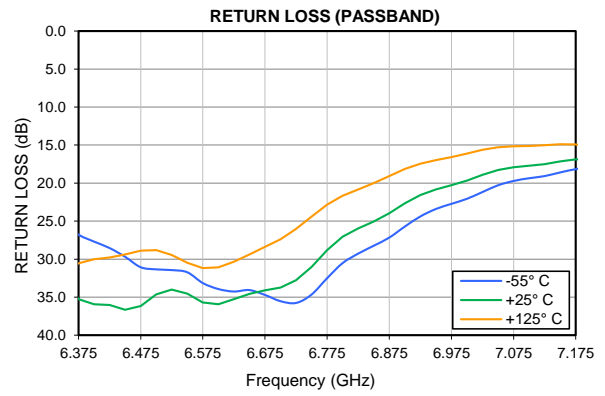
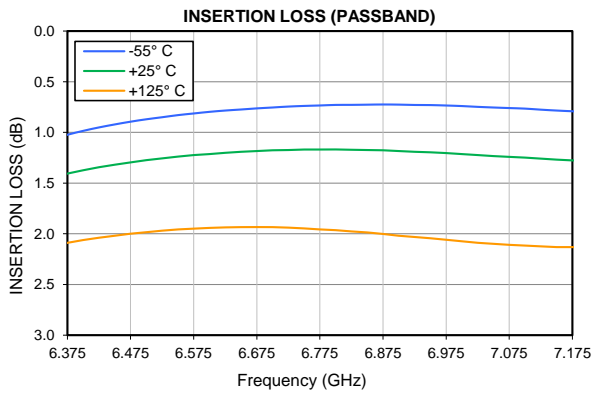
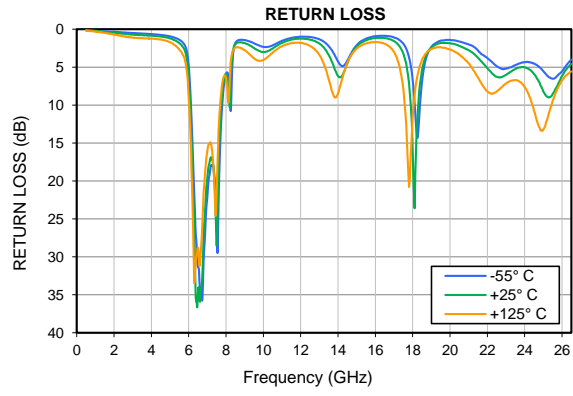
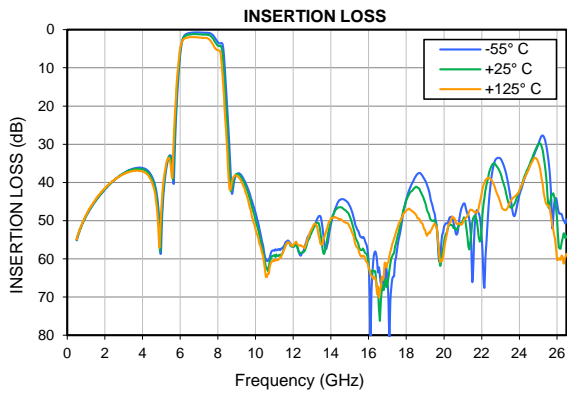


Typical Performance Data

FREQUENCY (MHz)	INSERTION LOSS (dB)	RETURN LOSS (dB)	AMPLITUDE UNBALANCE (dB)	PHASE UNBALANCE ¹ (deg.)
500.00	55.02	0.14	5.03	68.42
1000.0	48.80	0.24	8.26	96.66
1500.0	44.77	0.33	10.29	108.29
2000.0	41.74	0.46	11.46	111.74
2500.0	39.33	0.59	12.77	108.59
3000.0	37.62	0.71	13.57	100.52
3500.0	36.62	0.79	13.83	88.18
4000.0	36.55	0.85	13.04	77.73
4500.0	39.08	0.96	11.76	90.76
5000.0	49.79	1.21	1.69	129.39
5500.0	33.89	1.83	2.25	5.34
6000.0	5.57	6.42	1.09	0.48
6375.0	1.41	35.27	0.65	4.10
6500.0	1.27	34.62	0.54	4.45
6875.0	1.18	23.96	0.28	3.05
7000.0	1.21	19.68	0.21	1.83
7175.0	1.27	16.89	0.09	0.40
7500.0	1.39	28.53	0.18	5.17
8000.0	4.16	5.87	0.67	11.56
8500.0	25.95	1.95	1.07	16.05
9000.0	37.78	1.83	0.08	7.07
9500.0	41.41	2.47	1.23	1.64
10000.0	49.86	2.99	20.56	160.25
10500.0	61.06	2.54	1.47	172.82
11000.0	59.98	1.60	0.03	168.53
11500.0	57.43	1.39	0.27	168.43
12000.0	56.70	1.25	0.64	169.82
12500.0	58.30	1.37	0.43	172.56
13000.0	52.70	1.89	0.87	169.81
13500.0	55.41	3.37	0.61	176.44
14000.0	50.18	6.17	0.22	170.46
14500.0	46.50	4.51	0.83	170.95
15000.0	48.97	2.30	1.23	175.08
15500.0	57.07	1.46	0.68	178.75
16000.0	61.62	1.16	0.15	177.31
16500.0	68.19	1.17	0.07	178.78
17000.0	66.56	1.57	0.24	179.99
17500.0	54.95	3.24	0.89	179.06
18000.0	44.77	15.61	1.79	174.72
18500.0	41.17	5.41	0.88	165.05
19000.0	45.26	2.43	0.75	169.02
19500.0	49.82	1.83	1.12	173.47
20000.0	55.54	1.85	0.92	175.89
20500.0	51.95	2.20	0.11	168.76
21000.0	50.16	2.78	0.07	167.47
21500.0	50.58	3.77	1.01	171.44
22000.0	51.64	5.13	1.72	179.06
22500.0	35.92	6.27	9.60	90.63
23000.0	37.59	6.00	0.51	65.85
23500.0	46.33	5.24	7.39	115.69
24000.0	42.34	5.03	12.67	94.51
24500.0	35.84	5.90	8.57	72.77
25000.0	30.08	8.19	6.02	107.46
25500.0	43.45	8.55	1.78	168.72
26000.0	49.77	6.17	0.36	174.02
26500.0	53.98	4.58	0.53	178.05

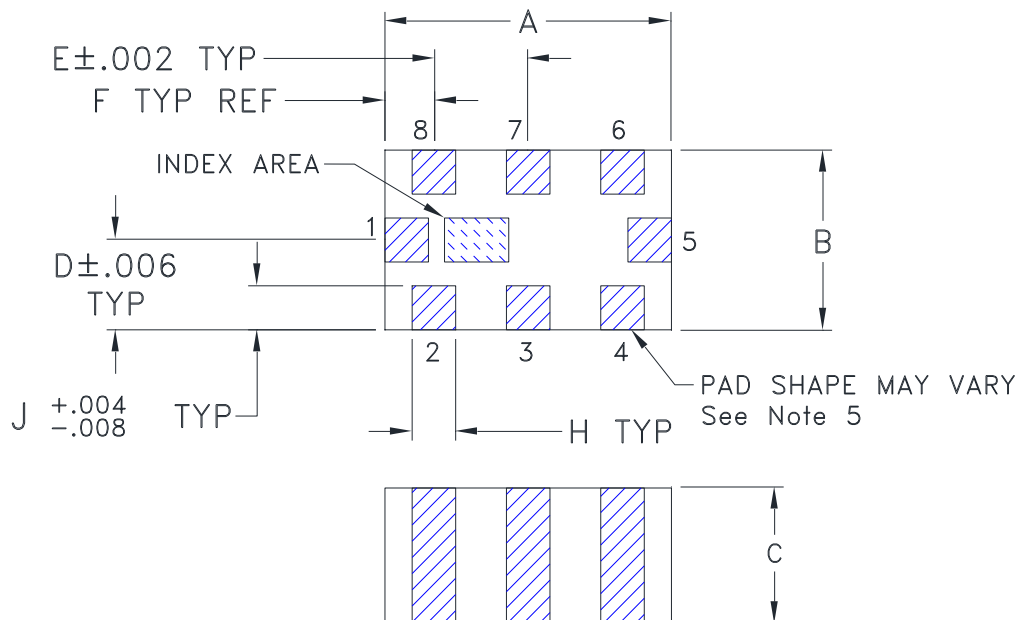
1. Relative to 180°

Typical Performance Curves



Outline Dimensions

Top View



CASE#	A	B	C	D	E	F	G	H	J	WT, GRAM
GE0805C-15	.079 (2.00)	.049 (1.25)	.037 (0.95)	.025 (0.63)	.026 (0.65)	.014 (0.35)	.110 (2.80)	.012 (0.30)	.010 (0.25)	.008

Dimensions are in inches (mm). Tolerances: 2 Pl. $\pm .01(.25)$; 3 Pl. $\pm .005(.100)$

Notes:

- Open style, ceramic base
- Termination finish: For RoHS Case Styles: Tin plate over Nickel plate. All model , (+) suffix.
For RoHS-5 Case Styles: Tin-lead plate over Nickel plate. All model ,no (+) suffix.
- Pad tolerance to be non-cumulative. Minimum spacing between each pad is .004(0.1).
- Pin numbers do not appear on unit. For reference only.
- During the manufacturing process, the pad shape may not be rectangular and may take on a more semi-circle shape. However, the pad dimensions reflect this, with the pad shape being within the specified lengths. The metallization compensates accordingly and so performance will not be affected. In addition, solderability will not be influenced by the pad shape



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DEVICE ORIENTATION IN T&R

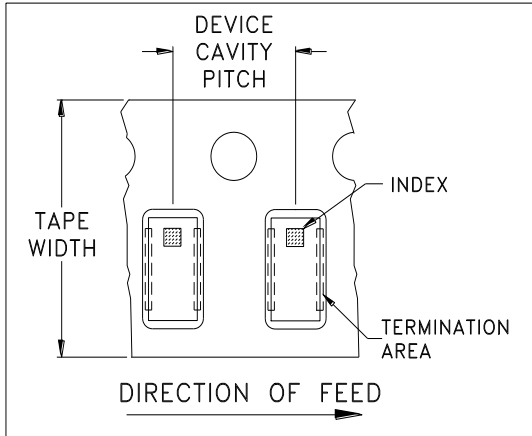


ILLUSTRATION 1

Applicable Case Styles	
GE0805C	JC0603C
GE0805C-1	JC0603C-4
GE0805C-1AP	JC0603C-6
GE0805C-7	
GE0805C-9	
GE0805C-10	
GE0805C-11	
GE0805C-12	

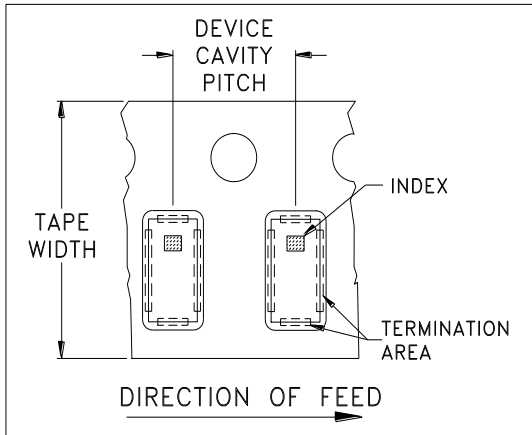


ILLUSTRATION 2

Applicable Case Styles	
GE0805C-2	JC0603C-1
GE0805C-3	JC0603C-2
GE0805C-4	JC0603C-3
GE0805C-5	JC0603C-5
GE0805C-6	JC0603C-7
GE0805C-8	
GE0805C-15	

Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel	
8	4	7	Small quantity standards (see note)	20
				50
				100
				200
				500
				1000
			Standard	4000

Note: Please Consult individual model data sheet to determine device per reel availability.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

Go to: www.minicircuits.com/pages/pdfs/tape.pdf



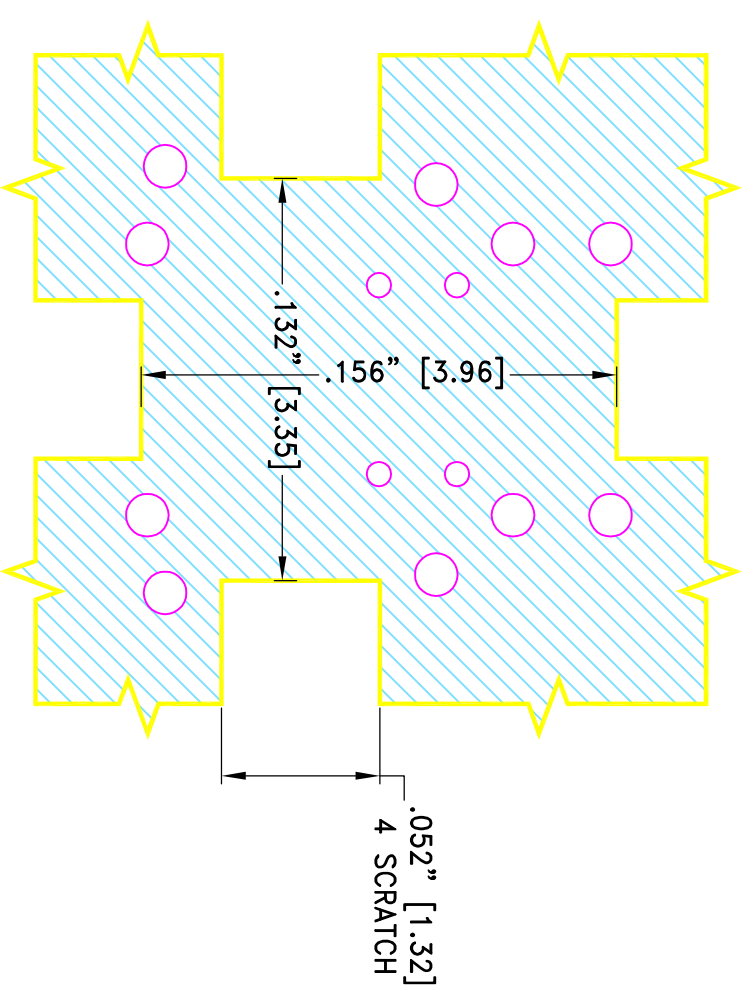
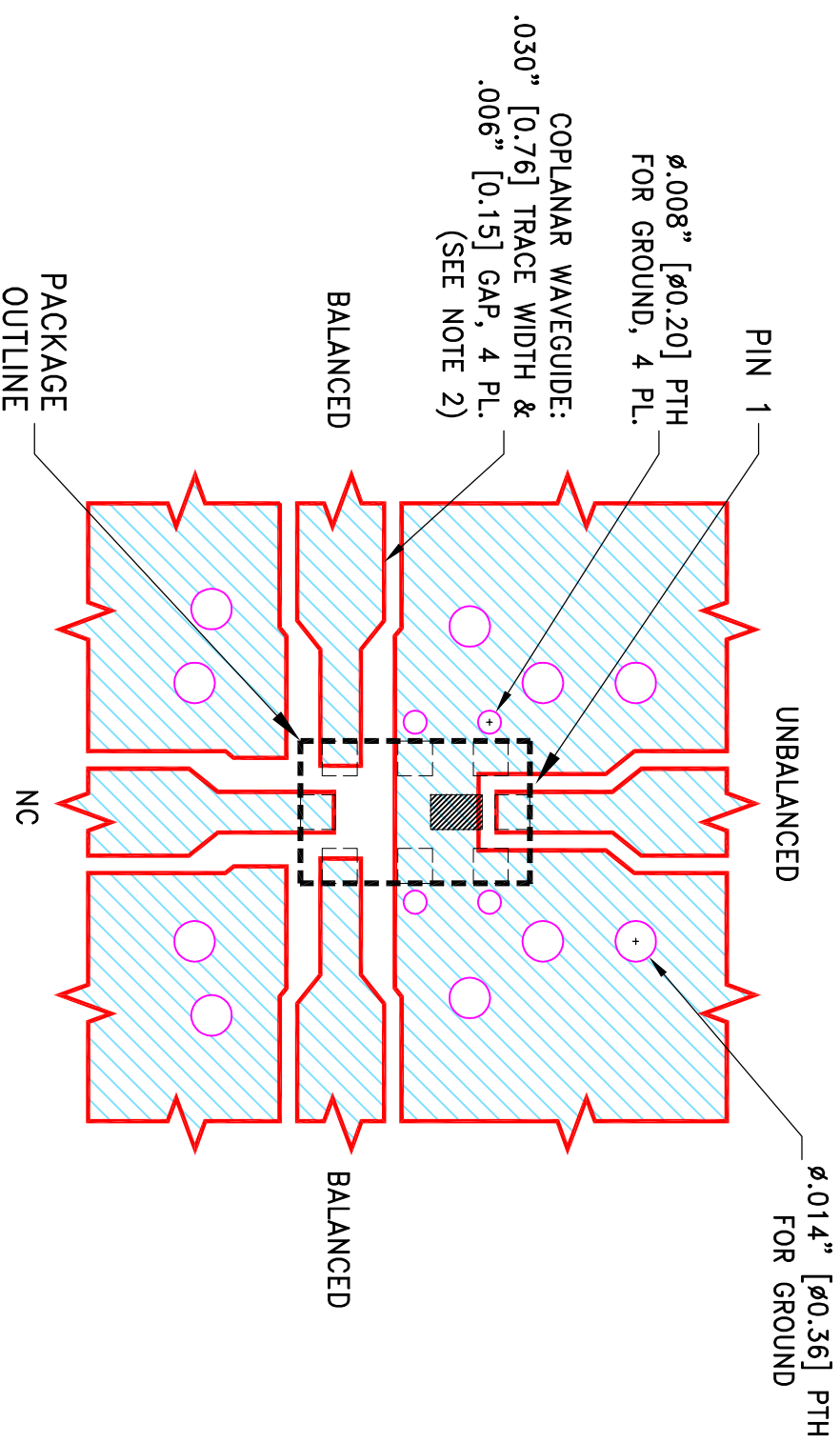
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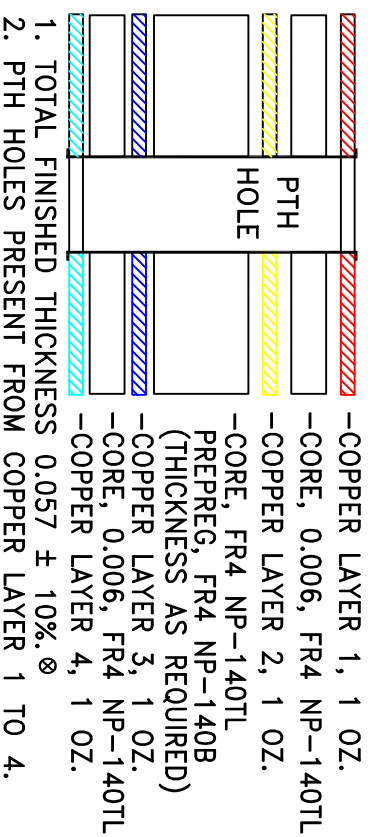
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**SUGGESTED MOUNTING CONFIGURATION
FOR GE0805C-15 CASE STYLE**




TOP VIEW TO LAYER 2

STACK-UP DIAGRAM

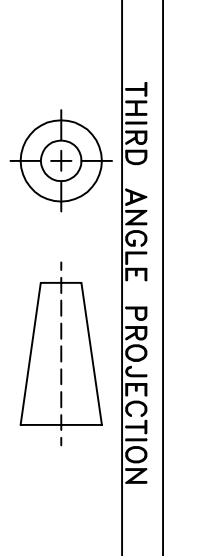


- NOTES:**
1. PCB IS MULTILAYER PCB, SEE STACK-UP DIAGRAM.
 2. TRACE WIDTH & GAP PARAMETERS ARE SHOWN FOR FR4 NP-140TL WITH DIELECTRIC THICKNESS .006"±.0005"; COPPER: 1 OZ. FOR OTHER MATERIALS TRACE WIDTH AND GAP MAY NEED TO BE MODIFIED.
 3. COPPER LAYERS 3,4 OF THE PCB IS CONTINUOUS GROUND PLANES.
-  DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK.

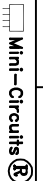
UNLESS OTHERWISE SPECIFIED	INITIALS	DATE
DIMENSIONS ARE IN INCHES	TTG	12/02/21
TOLERANCES ON:	CHECKED	GF
2 PL DECIMALS ±	APPROVED	IL
3 PL DECIMALS ± .005		12/02/21
ANGLES ± 1°		


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 13 Neptune Avenue
 Brooklyn NY 11235

PL, GE0805C-15, TB-1233+



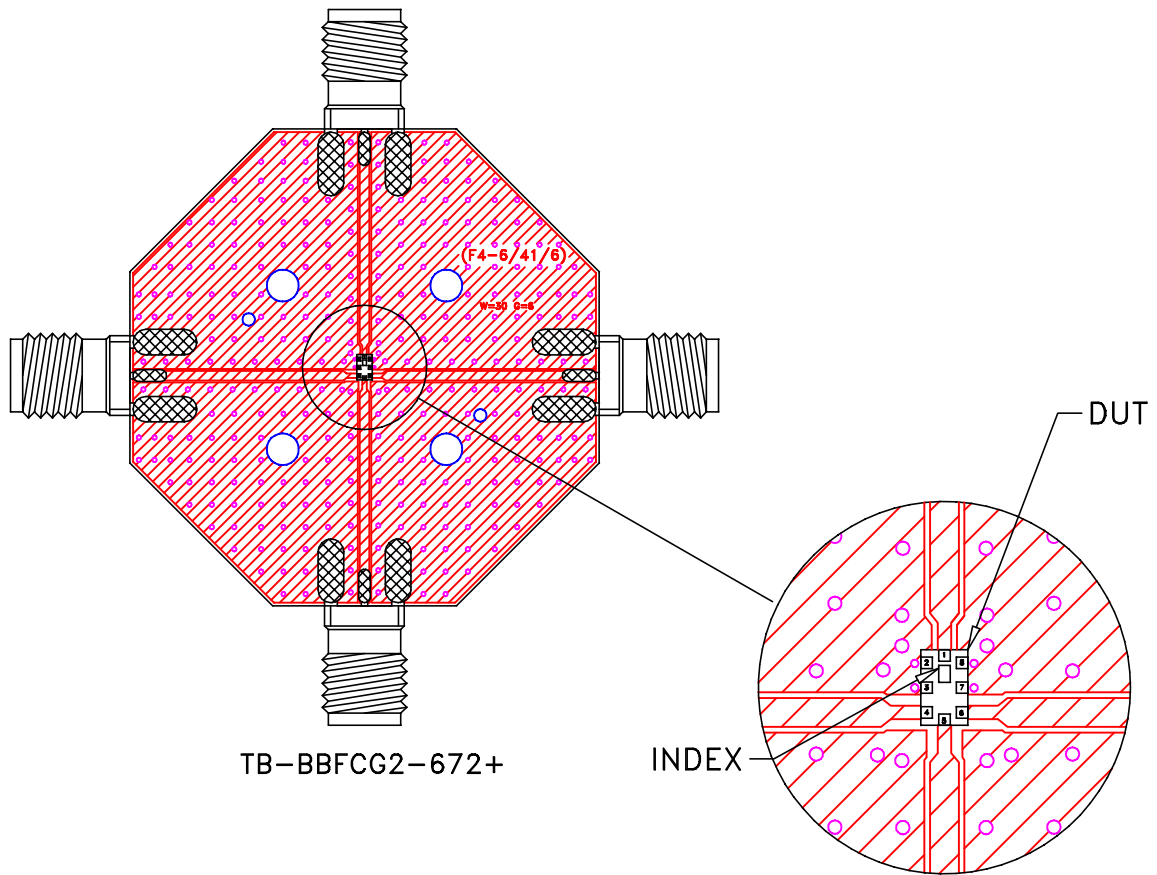
OR	ECN	NO.	DESCRIPTION	DATE	DR	AUTH
ECO-010908			NEW RELEASE	12/02/21	TTG	IL
REV	ECN	No.	DESCRIPTION	DATE	DR	AUTH

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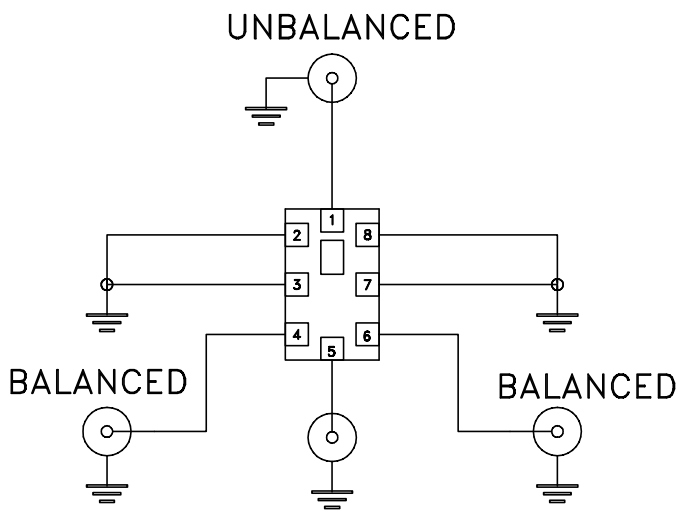
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FILE:	98PL724	SCALE:	15:1	SHEET:	1 OF 1
SIZE	B	CODE IDENT	15542	DRAWING NO:	98-PL-724
REV:	OR	REV:	OR	REV:	OR

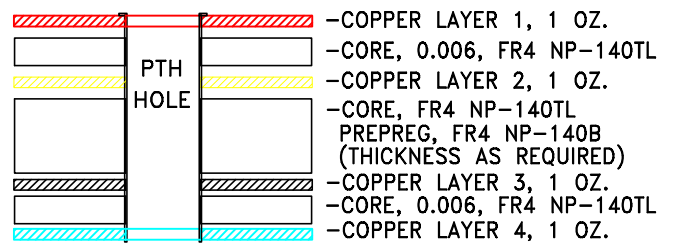
Evaluation Board and Circuit



TB-BBFCG2-672+




Schematic Diagram



Stack-up Diagram

Notes:

1. 50 Ohm SMA Female connectors.
2. PCB Material: FR4 or equivalent, Dielectric Constant=4.5, Total finished Thickness = .057 inch.

 **Mini-Circuits®**

All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-55° to 125° C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 125° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Solder Reflow Heat	Sn-Pb Eutectic Process 225°C peak Pb-Free Process 245° - 250°C peak	J-STD-020C, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, Test B,B1, 95% Coverage
Thermal Shock	-55° to +125°C, 15 min dwell,250 cycles	MIL-STD-202, Method 107
Bend Test	1mm, deflection for 5 seconds Span of bending: 2.75"	--
High Temp Storage	125°C to 1000 Hrs	---